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Server/Workstation

Motherboard

W880D4ID-2Q

User Manual

English



Version 1.50

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CALIFORNIA, USA ONLY

The Lithium battery adopted on this motherboard contains Perchlorate, a toxic substance controlled in Perchlorate Best Management Practices (BMP) regulations passed by the California Legislature. When you discard the Lithium battery in California, USA, please follow the related regulations in advance.

"Perchlorate Material-special handling may apply, see www.dtsc.ca.gov/hazardouswaste/perchlorate"

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This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) this device may not cause harmful interference, and
- (2) this device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications.

However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.



ASRock Rack INC. hereby declares that this device is in compliance with the essential requirements and other relevant provisions of related UKCA Directives. Full text of UKCA declaration of conformity is available at: <http://www.asrockrack.com>



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ASRock Rack follows the green design concept to design and manufacture our products, and makes sure that each stage of the product life cycle of ASRock Rack product is in line with global environmental regulations. In addition, ASRock Rack disclose the relevant information based on regulation requirements.

Please refer to <https://www.asrockrack.com/general/about.asp?cat=Responsibility> for information disclosure based on regulation requirements ASRock Rack is complied with.



DO NOT throw the motherboard in municipal waste. This product has been designed to enable proper reuse of parts and recycling. This symbol of the crossed out wheeled bin indicates that the product (electrical and electronic equipment) should not be placed in municipal waste. Check local regulations for disposal of electronic products.

Contents

Chapter 1 Introduction	1
1.1 Package Contents	1
1.2 Specifications	2
1.3 Unique Features	5
1.4 Motherboard Layout	6
1.5 Onboard LED Indicators	9
1.6 I/O Panel	11
1.7 Block Diagram	13
Chapter 2 Installation	14
2.1 Screw Holes	14
2.2 Pre-installation Precautions	14
2.3 Installing the CPU	15
2.4 Installing the CPU Fan and Heatsink	17
2.5 Installing the Memory Modules (DIMM)	18
2.6 Expansion Slot (PCI Express Slot)	20
2.7 Jumpers Setup	21
2.8 Onboard Headers and Connectors	22
2.9 Dr. Debug	31
2.10 ATX PSU / DC-IN Power Connections	37
2.11 Unit Identification purpose LED/Switch	38
2.12 Dual LAN and Teaming Operation Guide	39
2.13 M.2 SSD Module Installation Guide	40

Chapter 3	UEFI Setup Utility	41
3.1	Introduction	41
3.1.1	UEFI Menu Bar	41
3.1.2	Navigation Keys	42
3.2	Main Screen	43
3.2.1	Motherboard Information	44
3.2.2	Processor Information	44
3.2.3	Memory Information	45
3.3	OC Tweaker	46
3.3.1	CPU Configuration	48
3.3.2	DRAM Configuration	52
3.3.3	Voltage Configuration	64
3.3.4	CPU DLVR Configuration	68
3.4	Advanced Screen	69
3.4.1	CPU Configuration	70
3.4.2	Memory Configuration	73
3.4.3	Chipset Configuration	74
3.4.4	PCH-FW Configuration	77
3.4.5	Storage Configuration	78
3.4.6	NVME Configuration	79
3.4.7	VMD Configuration	80
3.4.8	ACPI Configuration	81
3.4.9	USB Configuration	83
3.4.10	Super IO Configuration	84

3.4.11	Serial Port Console Redirection	85
3.4.12	H/W Monitor	88
3.4.13	Trusted Computing	89
3.4.14	Intel ME Configuration	91
3.4.15	Network Stack Configuration	92
3.4.16	Driver Health	94
3.4.17	Tls Auth Configuration	95
3.4.18	Intel(R) Platform Service Record	96
3.4.19	Intel(R) Rapid Storage Technology	97
3.4.20	Intel(R) Network Connection	98
3.4.21	Instant Flash	99
3.5	Security Screen	100
3.5.1	Expert Key Management	101
3.6	Server Mgmt	105
3.6.1	BMC Network Configuration	107
3.6.2	DNS Configuration	109
3.6.3	System Event Log	111
3.6.4	BMC Tools	112
3.7	Event Logs	113
3.8	Boot Screen	114
3.9	Exit Screen	116
Chapter 4 Software Support		117
4.1	Download and Install Operating System	117
4.2	Download and Install Software Drivers	117

Chapter 5 Troubleshooting	118
5.1 Troubleshooting Procedures	118
5.2 Technical Support Procedures	120
5.3 Returning Merchandise for Service	120
Contact Information	121

Chapter 1 Introduction

Thank you for purchasing ASRock Rack **W880D4ID-2Q** motherboard, a reliable motherboard produced under ASRock Rack's consistently stringent quality control. It delivers excellent performance with robust design conforming to ASRock Rack's commitment to quality and endurance.



Because the motherboard specifications and the BIOS software might be updated, the content of this manual will be subject to change without notice. In case any modifications of this manual occur, the updated version will be available on ASRock Rack website without further notice. Find the latest memory and CPU support lists on ASRock Rack website as well. ASRock Rack's Website: www.ASRockRack.com

*Please visit the website for specific information about the using model.
<http://www.asrockrack.com/support/>*

1.1 Package Contents

- ASRock Rack W880D4ID-2Q motherboard
(Deep mini ITX form factor: 170.2 mm x 218.3 mm)
- Quick installation guide
- 1 Oculink to 4 SATA cable (60 cm)
- 1 ATX 4P to 24P power cable (8 cm)
- 1 SATA power cable (80cm)
- 1 I/O shield
- 2 Screws for M.2 sockets



If any items are missing or appear damaged, contact the authorized dealer.

1.2 Specifications

W880D4ID-2Q	
Physical Status	
Form Factor	Deep mini-ITX
Dimension	6.7 in x 8.593 in (70.2 mm x 218.3 mm)
Processor System	
CPU	Supports Intel® Core™ Ultra Desktop Processors (Series 2)
Socket	Single Socket LGA 1851
Chipset	Intel® W880
System Memory	
Supported DIMM Quantity	4 DIMM slots (2DPC)
Supported Type	DDR5 288-pin ECC/non-ECC UDIMM, CUDIMM
Max. Capacity per DIMM	64 GB
Max. Frequency	ECC/non-ECC UDIMM: 5600 MT/s (2SPC 0R2R) 4800 MT/s (2SPC 1R1R) 4400 MT/s (2SPC 2R2R) CUDIMM: 6400 MT/s (1SPC) 5600 MT/s (2SPC 0R2R) 4800 MT/s (2SPC 1R1R) 4400 MT/s (2SPC 2R2R)
<i>Note: Memory support is to be validated.</i>	
PCIe Expansion Slots (SLOT7 close to CPU)	
PCIe x16	SLOT7: PCIe 5.0 x16 [CPU]
Other PCIe Expansion Connectors	
M.2	1 M-key (PCIe 5.0 x4); support 2280 form factor [CPU] 1 M-key (PCIe 4.0 x4); support 2280 form factor [PCH]
OCuLink	2 OCuLink (PCIe 4.0 x4 or 4 SATA 6 Gb/s) [PCH] 1 OCuLink (PCIe 4.0 x4) [PCH]
SATA/SAS Storage	
PCH Built-in Storage	Intel® W880 (8 SATA 6 Gb/s; RAID 0/1/5/10): 2 OCuLink
Ethernet	
Additional GbE Controller	2 SFP28 (25 GbE) by Intel® E810

Server Management	
BMC Controller	ASPEED AST2600: IPMI 2.0 with iKVM and vMedia support
Dedicated IPMI LAN	1 RJ45 dedicated IPMI LAN port by Realtek RTL8211F
Graphics	
Controller	ASPEED AST2600: 1 DB15 (VGA) Intel® integrated processor graphics: 1 HDMI
Rear I/O	
UID Button/LED	1 UID button, 1 UID LED
Video Output	1 DB15 (VGA), 1 HDMI
USB Port	2 Type-A (USB 3.2 Gen1)
LAN Port	2 SFP28 (25 GbE), 1 dedicated IPMI
Internal Connectors/Headers	
Power Connector	1 Micro-Fit (4-pin, ATX PSU signal) w/ ATX 24-pin adapter cable, 2 (8-pin, ATX 12V) support 12V DC-in
Other Power Connectors	1 (4-pin) for HDD power when using 12V DC-in power source
Auxiliary Panel Header	1 (9-pin): chassis intrusion, system fault LED, LAN activity LEDs, locate
System Panel Header	1 (9-pin): power switch, reset switch, system power LED, HDD activity LED
NMI Header	1
COM Header	1 (9-pin)
Speaker Header	1
Fan Header	3 (4-pin)
Thermal Sensor Header	1
TPM Header	1 (13-pin, SPI)
SGPIO Header	1
SMBus Header	1
PMbus Header	1
IPMB Header	1
Clear CMOS	1 (2-pin)
USB Header	1 (19-pin, 2 USB 3.2 Gen1)
Others	1 UID_HD header, 1 IPMI_LED header, 1 NCSI header
LED Indicators	
Standby Power LED	1 (5VSB)
Fan Fail LED	3
BMC Heartbeat LED	1

System BIOS	
Type	AMI 256 Mb SPI Flash ROM
Features	Plug and Play, ACPI 6.4 (and above) compliance wake-up events, SMBIOS 2.3 and above , ASRock Rack Instant Flash
Hardware Monitor	
Temperature	CPU, PCH, MB, card side, TR, VR, M.2, E810 temperature sensing
Fan	Fan tachometer CPU quiet fan (Allow chassis fan speed auto-adjust by CPU temperature.) Fan multi-speed control
Voltage	VCORE, GT, VCCSA, VNNAON, VCCIO, 1.8V_CPU, VDD2, 0.82V_PCH, 1.8VSB, 3V, 5V, 12V, BAT, 3VSB, 5VSB
Support OS	
OS	Microsoft® Windows® - Windows® 10 (64 bit) - Windows® 11 (64 bit) <i>* ASPEED VGA Driver Installation for Windows® 11 24H2 / 25H2 Procedure:</i> 1. Begin by installing the Intel® Onboard VGA Driver (HDMI). If prompted to reboot after installation, dismiss the prompt and do not restart yet. 2. Install the ASPEED VGA driver. 3. Restart the system to complete the installation. Linux - Red Hat Enterprise Linux Server 9.6 (64 bit) / 10.0 (64 bit) - SUSE SLES 15.6 (64 bit) - Ubuntu 24.04.2 (64 bit) / 24.10 (64 bit) / 25.04 (64 bit) <i>* Supports UEFI BOOT only.</i> <i>* Windows® U.2 NVME doesn't support RAID Mode.</i> <i>* Linux doesn't support RAID Mode.</i>
Enviroment	
Temperature	Operating temperature: 10°C ~ 35°C Non-operating temperature: -40°C ~ 70°C
Humidity	Non-operating humidity: 20% ~ 90% (non-condensing)

NOTE: Please refer to the website for the latest specifications.



This motherboard supports Wake from on Board LAN. To use this function, please make sure that the "Wake on Magic Packet from power off state" is enabled in Device Manager > Intel® Ethernet Connection > Power Management. And the "PCI Devices Power On" is enabled in UEFI SETUP UTILITY > Advanced > ACPI Configuration. After that, onboard LAN1&2 can wake up S5 under OS.



If installing Intel® LAN utility or Marvell SATA utility, this motherboard may fail Windows® Hardware Quality Lab (WHQL) certification tests. If installing the drivers only, it will pass the WHQL tests.

1.3 Unique Features

ASRock Rack Instant Flash is a BIOS flash utility embedded in Flash ROM. This convenient BIOS update tool allows user to update system BIOS without entering operating systems first like MS-DOS or Windows. With this utility, press the <F6> key during the POST or the <F2> key to enter into the BIOS setup menu to access ASRock Rack Instant Flash. Just launch this tool and save the new BIOS file to the USB flash drive, floppy disk or hard drive, then update the BIOS only in a few clicks without preparing an additional floppy diskette or other complicated flash utility. Please be noted that the USB flash drive or hard drive must use FAT32/16/12 file system.

The diagram shows the top view of the ASRock W880D4ID-2Q motherboard. Key components and their locations are labeled:

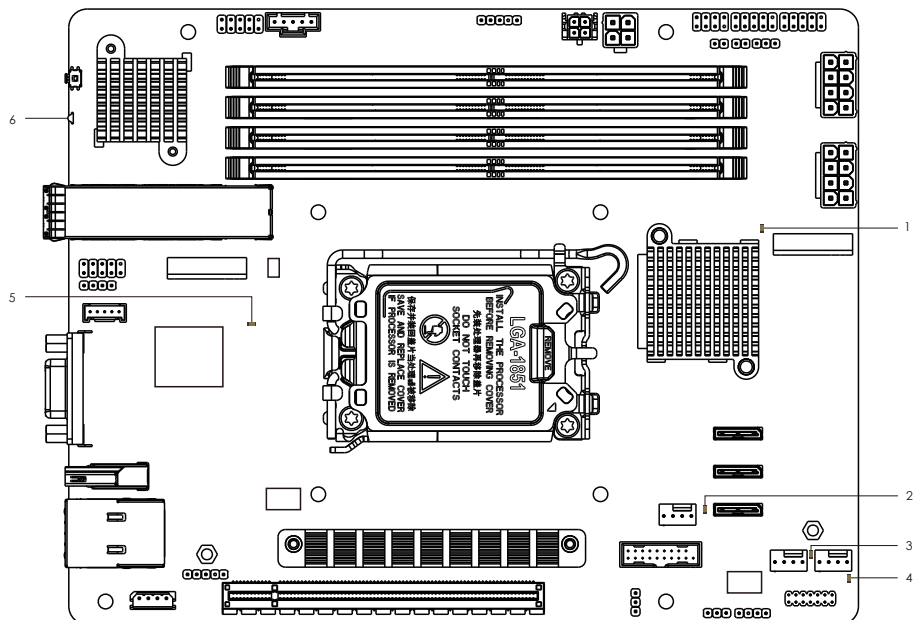
- Dimensions:** 218.3 mm (8.593 in) width and 170.2 mm (6.7 in) height.
- Processors:** CPU1 (Intel E810), CPU2 (Intel E810), CPU3 (Intel E810), CPU4 (Intel E810).
- Chipsets:** PCH (ASPEED AST2600).
- Memory:** DIMM1, DIMM2, DIMM3, DIMM4, DIMM5, DIMM6, DIMM7, DIMM8, DIMM9, DIMM10, DIMM11, DIMM12, DIMM13, DIMM14, DIMM15, DIMM16, DIMM17, DIMM18, DIMM19, DIMM20, DIMM21, DIMM22, DIMM23, DIMM24, DIMM25, DIMM26, DIMM27, DIMM28, DIMM29, DIMM30, DIMM31, DIMM32, DIMM33, DIMM34, DIMM35, DIMM36, DIMM37, DIMM38, DIMM39, DIMM40, DIMM41, DIMM42, DIMM43, DIMM44, DIMM45, DIMM46, DIMM47, DIMM48, DIMM49, DIMM50, DIMM51, DIMM52, DIMM53, DIMM54, DIMM55, DIMM56, DIMM57, DIMM58, DIMM59, DIMM60, DIMM61, DIMM62, DIMM63, DIMM64, DIMM65, DIMM66, DIMM67, DIMM68, DIMM69, DIMM70, DIMM71, DIMM72, DIMM73, DIMM74, DIMM75, DIMM76, DIMM77, DIMM78, DIMM79, DIMM80, DIMM81, DIMM82, DIMM83, DIMM84, DIMM85, DIMM86, DIMM87, DIMM88, DIMM89, DIMM90, DIMM91, DIMM92, DIMM93, DIMM94, DIMM95, DIMM96, DIMM97, DIMM98, DIMM99, DIMM100, DIMM101, DIMM102, DIMM103, DIMM104, DIMM105, DIMM106, DIMM107, DIMM108, DIMM109, DIMM110, DIMM111, DIMM112, DIMM113, DIMM114, DIMM115, DIMM116, DIMM117, DIMM118, DIMM119, DIMM120, DIMM121, DIMM122, DIMM123, DIMM124, DIMM125, DIMM126, DIMM127, DIMM128, DIMM129, DIMM130, DIMM131, DIMM132, DIMM133, DIMM134, DIMM135, DIMM136, DIMM137, DIMM138, DIMM139, DIMM140, DIMM141, DIMM142, DIMM143, DIMM144, DIMM145, DIMM146, DIMM147, DIMM148, DIMM149, DIMM150, DIMM151, DIMM152, DIMM153, DIMM154, DIMM155, DIMM156, DIMM157, DIMM158, DIMM159, DIMM160, DIMM161, DIMM162, DIMM163, DIMM164, DIMM165, DIMM166, DIMM167, DIMM168, DIMM169, DIMM170, DIMM171, DIMM172, DIMM173, DIMM174, DIMM175, DIMM176, DIMM177, DIMM178, DIMM179, DIMM180, DIMM181, DIMM182, DIMM183, DIMM184, DIMM185, DIMM186, DIMM187, DIMM188, DIMM189, DIMM190, DIMM191, DIMM192, DIMM193, DIMM194, DIMM195, DIMM196, DIMM197, DIMM198, DIMM199, DIMM200, DIMM201, DIMM202, DIMM203, DIMM204, DIMM205, DIMM206, DIMM207, DIMM208, DIMM209, DIMM210, DIMM211, DIMM212, DIMM213, DIMM214, DIMM215, DIMM216, DIMM217, DIMM218, DIMM219, DIMM220, DIMM221, DIMM222, DIMM223, DIMM224, DIMM225, DIMM226, DIMM227, DIMM228, DIMM229, DIMM230, DIMM231, DIMM232, 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No.	Description
1	2 x 288-pin DDR5 DIMM Slots (DDR5_A1, DDR5_B1)*
2	2 x 288-pin DDR5 DIMM Slots (DDR5_A2, DDR5_B2)*
3	NCSI Header (NCSI1)
4	PSU SMBus Header (PSU_SMB1)
5	UID Header (UID_HD)
6	ATX 4-PIN Power Connector (ATX4PIN1 (<i>ATX 24pin-to-4pin</i>))
7	SATA Power Connector (SATA_PWR1)
8	Non Maskable Interrupt Button (NMI_BTN1)
9	Serial General Purpose Input/Output Header (SATA_SGPIO1)
10	Clear CMOS Jumper (CLRMOS1)
11	Auxiliary Panel Header (ITX_AUX_PANEL1)
12	System Panel Header (PANEL1)
13	Security Override Jumper (SEC_OR1)
14	ATX 12V Power Connector (ATX12V2)
15	ATX 12V Power Connector (ATX12V1)
16	M.2 Socket (M2_1) (Type 2280)
17	LGA1851 CPU Socket (CPU1)
18	OCuLink PCIe4.0 x4 Connector (OCU3)
19	OCuLink PCIe4.0 x4 or 4 SATA 6Gb/s Connector (OCU1)
20	System Fan Header (FAN3)
21	OCuLink PCIe4.0 x4 or 4 SATA 6Gb/s Connector (OCU2)
22	System Fan Header (FAN2)
23	System Fan Header (FAN1)
24	SPI TPM Header (TPM_BIOS_PH1)
25	P80 Debug Header (P80_DEBUG1)
26	Thermal Sensor Header (TR1)
27	USB 3.2 Gen1 Header (USB3_3_4)
28	PWM Configuration Header (PWM_CFG1)
29	PCI Express 5.0 x16 Slot (PCIE7)
30	IPMI LED Header (IPMI_LED1)
31	Intelligent Platform Management Bus Header (IPMB1)
32	CMOS Battery Connector (BAT1)
33	Baseboard Management Controller SMBus Header (BMC_SMB1)
34	Speaker Header (SPEAKER1)

No.	Description
35	Serial Port Header (COM1)
36	M.2 Socket (M2_2) (Type 2280)

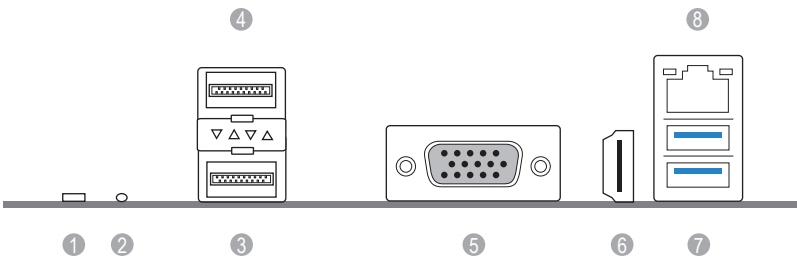
**For DIMM installation and configuration instructions, please see p.18 (Installing the Memory Modules (DIMM)) for more details.*

1.5 Onboard LED Indicators



No.	Item	Status	Description
1	SB_PWR1	Green	STB PWR ready
2	LED_FAN3	Red	FAN3 failed
3	LED_FAN1	Red	FAN1 failed
4	LED_FAN2	Red	FAN2 failed
5	BMC_LED1	Blinking four times and then off	Start initialization after AC power-on or BMC reset
		Blinking at 1Hz	BMC ready
		Steady on	Error
		Steady off	(The actual behavior depends on the state at the time of failure.)
		Other frequency	
6	UID_LED1	Blue	BMC UID LED

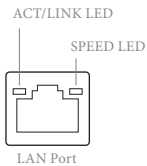
1.6 I/O Panel



No.	Description	No.	Description
1	UID Switch (UID1)	5	VGA Port (VGA1)
2	UID LED (UID_LED1)	6	HDMI Port (HDMI1)
3	25G SFP28 Port (SFP+1, shared NIC)**	7	USB 3.2 Gen1 Ports (USB3_1_2)
4	25G SFP28 Port (SFP+2)**	8	LAN RJ-45 Port (IPMI_LAN1)*

LAN Port LED Indications

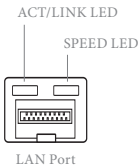
*There are two LEDs on the IPMI LAN port specified for link activity and speed. Please refer to the table below for the LAN port LED indications.



IPMI LAN Port LED Indications

Activity / Link LED		Speed LED	
Status	Description	Status	Description
Off	No link	Off	10 Mbps connection or no link
Blinking Yellow	Data activity	Yellow	100 Mbps connection
On	Link	Green	1 Gbps connection

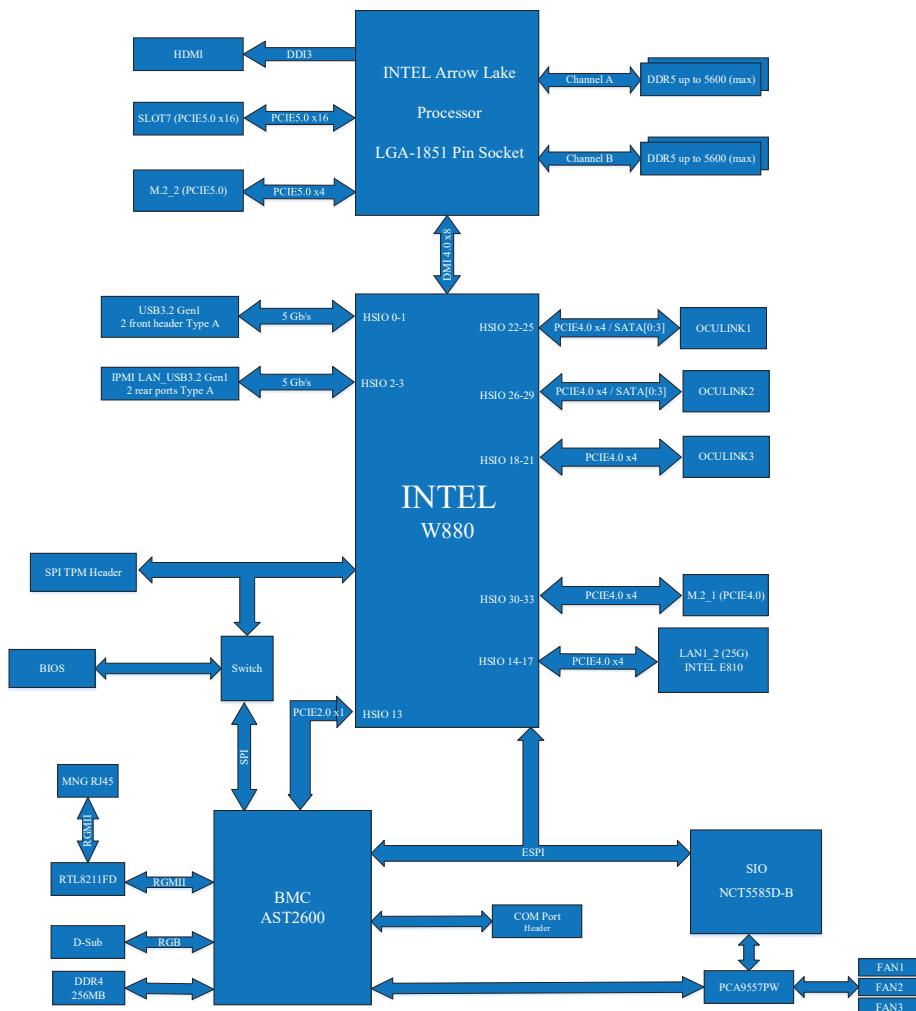
**There are two LEDs on the SFP LAN port specified for link activity and speed. Please refer to the table below for the LAN port LED indications.



SFP LAN Port LED Indications

Activity / Link LED		Speed LED	
Status	Description	Status	Description
Off	No link	Off	No link
Blinking Orange	Data activity	Orange	10 Gbps / 1 Gbps connection
On	Link	Green	25 Gbps connection

1.7 Block Diagram



Chapter 2 Installation

This is a deep mini ITX form factor (170.2 mm x 218.3 mm) motherboard. Before installing the motherboard, study the configuration of the chassis to ensure that the motherboard fits into it.



Make sure to unplug the power cord before installing or removing the motherboard. Failure to do so may cause physical injuries and motherboard damages.

2.1 Screw Holes

Place screws into the holes indicated by circles to secure the motherboard to the chassis.



Do not over-tighten the screws! Doing so may damage the motherboard.

2.2 Pre-installation Precautions

Take note of the following precautions before installing motherboard components or change any motherboard settings.

1. Unplug the power cord from the wall socket before touching any components.
2. To avoid damaging the motherboard's components due to static electricity, NEVER place the motherboard directly on the carpet or the like. Also remember to use a grounded wrist strap or touch a safety grounded object before handling the components.
3. Hold components by the edges and do not touch the ICs.
4. Whenever uninstall any component, place it on a grounded anti-static pad or in the bag that comes with the component.
5. When placing screws into the screw holes to secure the motherboard to the chassis, please do not over-tighten the screws! Doing so may damage the motherboard.



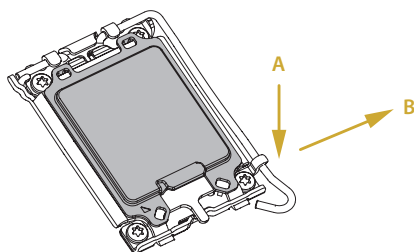
Before installing or removing any component, ensure that the power supply is off or the power cord is detached from the power supply. Failure to do so may cause severe damage to the motherboard, peripherals, and/or components.

2.3 Installing the CPU

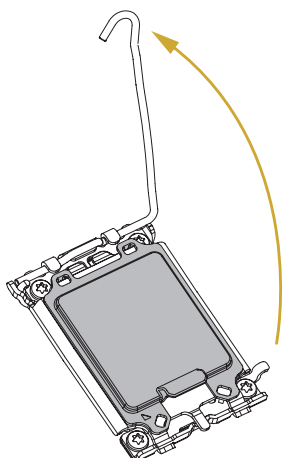


1. Before inserting the CPU into the socket, please check if the **PnP cap** is on the socket, if the CPU surface is unclean, or if there are any **bent pins** in the socket. Do not force to insert the CPU into the socket if above situation is found. Otherwise, the CPU will be seriously damaged.
2. Unplug all power cables before installing the CPU.

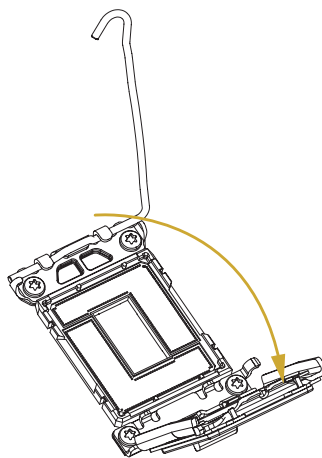
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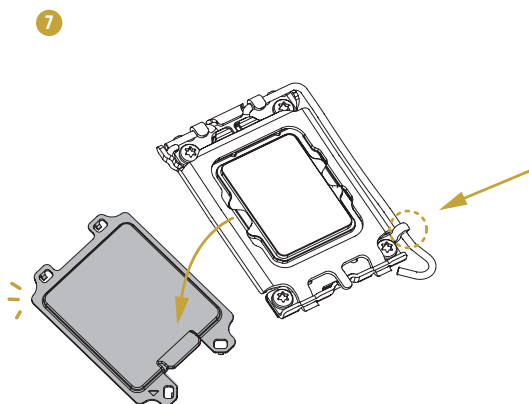
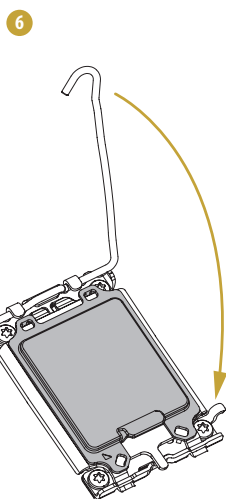
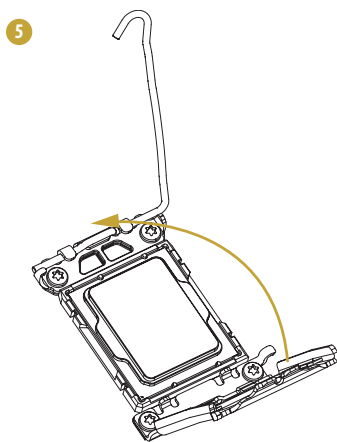
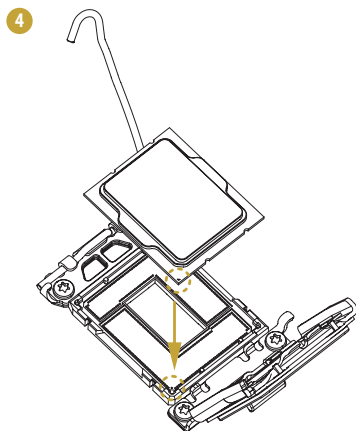


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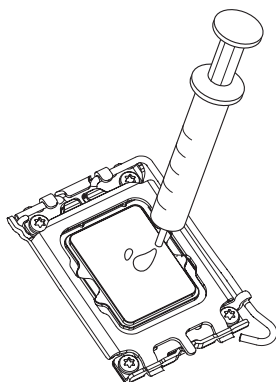


Please save and replace the cover if the processor is removed. The cover must be placed if wishing to return the motherboard for after service.

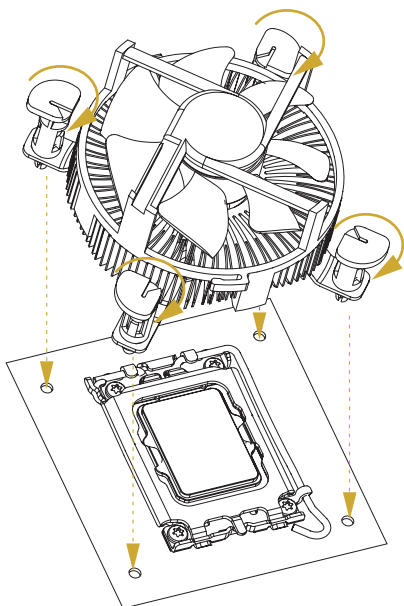
2.4 Installing the CPU Fan and Heatsink



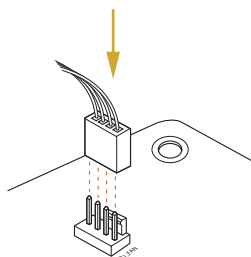
Please avoid using push-pin type CPU coolers, as the PCB thickness may prevent proper installation. A screw-based mounting system is recommended for a secure and reliable fit.



1



2



2.5 Installing the Memory Modules (DIMM)

This motherboard provides four 288-pin DDR5 (Double Data Rate 5) DIMM slots, and supports Dual Channel Memory Technology.



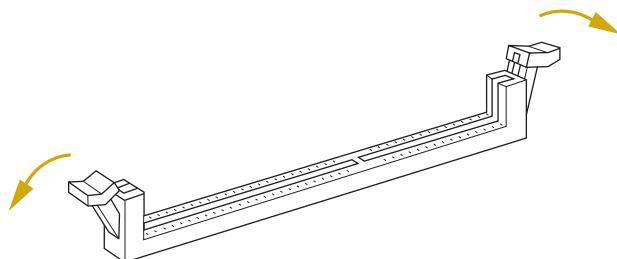
1. For dual channel configuration, it always needs to install identical (the same brand, speed, size and chip-type) DDR5 DIMM pairs.
2. It is unable to activate Dual Channel Memory Technology with only one or three memory module installed.
3. It is not allowed to install a DDR, DDR2, DDR3 or DDR4 memory module into a DDR5 slot; otherwise, this motherboard and DIMM may be damaged.

Dual Channel Memory Configuration

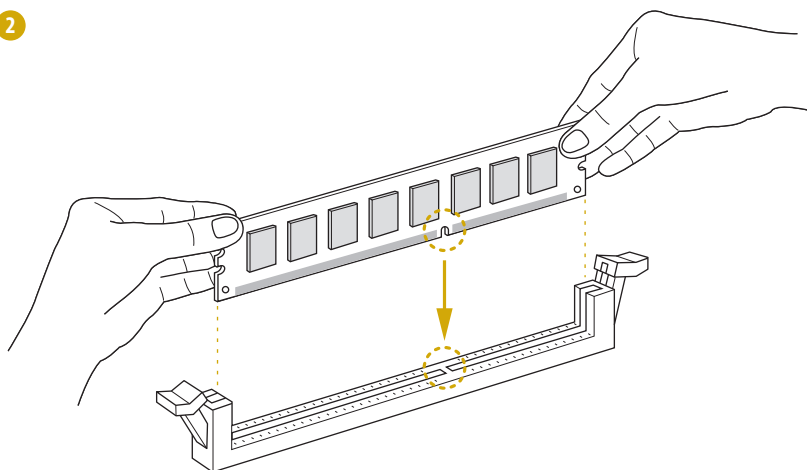
Priority	DDR5_A1	DDR5_A2	DDR5_B1	DDR5_B2
1		V		V
2	V	V	V	V

The symbol V indicates the slot is populated.

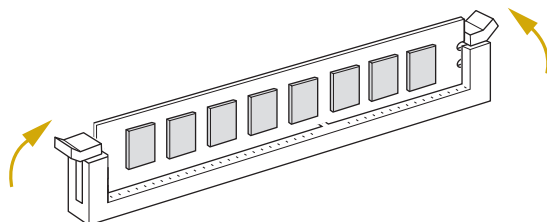
1



2



3



The DIMM only fits in one correct orientation. It will cause permanent damage to the motherboard and the DIMM if forcing the DIMM into the slot at incorrect orientation.

2.6 Expansion Slot (PCI Express Slot)

There is 1 PCI Express slot on this motherboard.

PCIe slot:

PCIe7 (PCIe 5.0 x16 slot) is used for PCI Express x16 lane width cards.

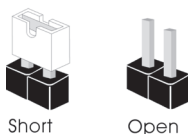
Slot	Generation	Mechanical	Electrical	Source
PCIe 7	5.0	x16	x16	CPU

Installing an expansion card

- Step 1. Before installing an expansion card, please make sure that the power supply is switched off or the power cord is unplugged. Please read the documentation of the expansion card and make necessary hardware settings for the card before starting the installation.
- Step 2. Remove the system unit cover (if the motherboard is already installed in a chassis).
- Step 3. Remove the bracket facing the slot that intending to use. Keep the screws for later use.
- Step 4. Align the card connector with the slot and press firmly until the card is completely seated on the slot.
- Step 5. Fasten the card to the chassis with screws.
- Step 6. Replace the system cover.

2.7 Jumpers Setup

The illustration shows how jumpers are setup. When the jumper cap is placed on the pins, the jumper is “Short”. If no jumper cap is placed on the pins, the jumper is “Open”.



Clear CMOS Jumper
(CLRMOS1)
(see p.6, No. 10)



Short: Clear CMOS
Open: Default

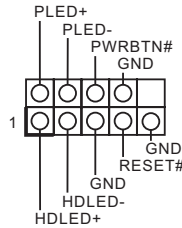
CLRMOS1 allows user to clear the data in CMOS. The data in CMOS includes system setup information such as system password, date, time, and system setup parameters. To clear and reset the system parameters to default setup, please turn off the computer and unplug the power cord, then use a jumper cap to short the pins on CLRMOS1 for 3 seconds. Please remember to remove the jumper cap after clearing the CMOS. If it needs to clear the CMOS when finishing to update the BIOS, it must boot up the system first, and then shut it down before processing the clear-CMOS action.

2.8 Onboard Headers and Connectors



Onboard headers and connectors are NOT jumpers. Do NOT place jumper caps over these headers and connectors. Placing jumper caps over the headers and connectors will cause permanent damage to the motherboard.

System Panel Header
(9-pin PANEL1)
(see p.6, No. 12)



Connect the power switch, reset switch and system status indicator on the chassis to this header according to the pin assignments. Particularly note the positive and negative pins before connecting the cables.



PWRBTN (Power Switch):
Connect to the power switch on the chassis front panel. Configure the way to turn off the system using the power switch.

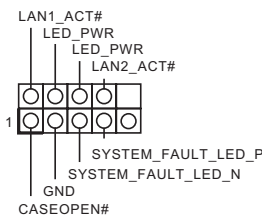
RESET (Reset Switch):
Connect to the reset switch on the chassis front panel. Press the reset switch to restart the computer if the computer freezes and fails to perform a normal restart.

PLED (System Power LED):
Connect to the power status indicator on the chassis front panel. The LED is on when the system is operating. The LED is off when the system is in S4 sleep state or powered off (S5).

HDLED (Hard Drive Activity LED):
Connect to the hard drive activity LED on the chassis front panel. The LED is on when the hard drive is reading or writing data.

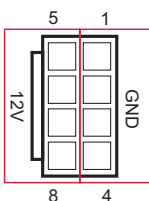
The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker and etc. When connecting the chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

Auxiliary Panel Header
(9-pin
ITX_AUX_PANEL1)
(see p.6, No. 11)



This header supports multiple functions on the front panel, including front panel SMB, internet status indicator.

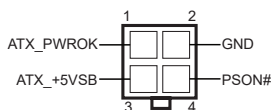
ATX 12V Power
Connectors
(8-pin ATX12V1)
(see p.6, No. 15)
(8-pin ATX12V2)
(see p.6, No. 14)



The motherboard provides two 8-pin 12V power connectors which are required input for either DC-IN 12V or ATX +12V power source.

When using ATX power, it is necessary to use a 24pin-to-4pin power cable to connect between the 24pin power connector of PSU and the ATX12V1 or ATX12V2 connector on the motherboard for power supply and signal communication.

ATX 4-PIN Power
Connector
(4-pin ATX4PIN1
(ATX 24pin-to-4pin))
(see p.6, No. 6)

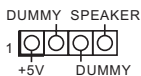


The motherboard provides one 4-pin power/signal connector which is a required input for ATX power source.

When using ATX power, it is necessary to use a 24pin-to-4pin power cable to connect between the 24pin power connector of PSU and the ATX4PIN connector on the motherboard for power supply and signal communication.

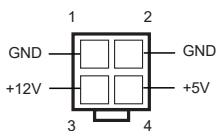
For DC-IN 12V application, it is not necessary to use this ATX 4PIN power connector.

Speaker Header
(4-pin SPEAKER1)
(see p.6, No. 34)



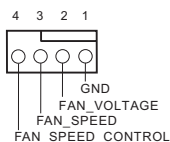
Please connect the chassis speaker to this header.

SATA Power Connector
(DC-IN mode)
(4-pin SATA_PWR1)
(see p.6, No. 7)



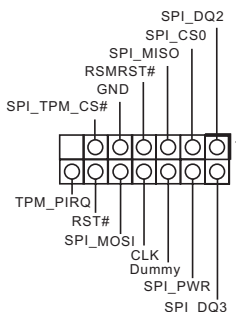
Please use a SATA power cable to connect this SATA Power Connector and the SATA HDD for supplying power from the motherboard, when using DC-IN mode without SATA power supply.

System Fan Headers
(4-pin FAN1)
(see p.6, No. 23)
(4-pin FAN2)
(see p.6, No. 22)
(4-pin FAN3)
(see p.6, No. 20)



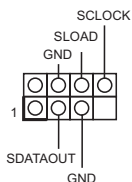
Please connect fan cables to the fan connectors and match the black wire to the ground pin. All fans support Fan Control. The fan max. current is 4A and the max. power is 48Watts.

SPI TPM Header
(13-pin TPM_BIOS_PH1)
(see p.6, No. 24)



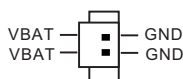
This connector supports SPI Trusted Platform Module (TPM) system, which can securely store keys, digital certificates, passwords, and data. A TPM system also helps enhance network security, protects digital identities, and ensures platform integrity.

Serial General Purpose
Input/Output Header
(7-pin SATA_SGPIO1)
(see p.6, No. 9)



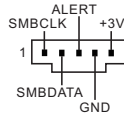
The header supports Serial Link interface for onboard SATA connections.

CMOS Battery Connector
(BAT1)
(see p.6, No. 32)



The server board comes with external CMOS battery connector. This 2-pin connector is used to connect the external cable battery.

PSU SMBus Header
(PSU_SMB1)
(see p.6, No.4)



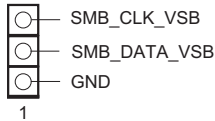
PSU SMBus monitors the status of the power supply, fan and system temperature.

Non Maskable Interrupt
Button
(NMI_BTN1)
(see p.6, No. 8)



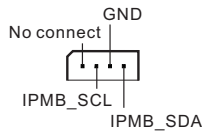
Please connect a NMI device to this header.

PWM Configuration
Header
(3-pin PWM_CFG1)
(see p.6, No. 28)



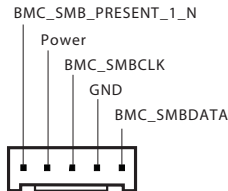
This header is used for PWM configurations.

Intelligent Platform
Management Bus Header
(4-pin IPMB1)
(see p.6, No. 31)



This 4-pin connector is used to provide a cabled base-board or front panel connection for value added features and 3rd-party add-in cards, such as Emergency Management cards, that provide management features using the IPMB.

Baseboard Management
Controller SMBus Header
(5-pin BMC_SMB1)
(see p.6, No. 33)



The header is used for the SM BUS devices.

Thermal Sensor Header
(3-pin TR1)
(see p.6, No. 26)



Please connect the thermal sensor cable to either pin 1-2 or pin 2-3 and the other end to the device to monitor its temperature.

OCuLink Connectors
(OCU1)
(see p.6, No. 19)
(OCU2)
(see p.6, No. 21)
(OCU3)
(see p.6, No. 18)



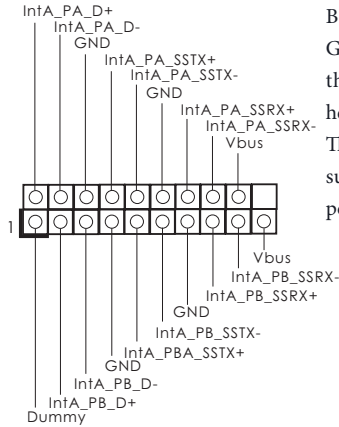
Please connect PCIE SSDs or
OCulink-to-SATA x4 cable to
the connectors.
* OCU1: GEN4/SATA
OCU2: GEN4/SATA
OCU3: GEN4

OCU1			
Pin	De finition	Pin	De finition
A21	-	B21	+3V
A20	GND	B20	GND
A19	RXN16	B19	TXN16
A18	RXP16	B18	TXP16
A17	GND	B17	GND
A16	RXN15	B16	TXN15
A15	RXP15	B15	TXP15
A14	GND	B14	GND
A13	PCH_CLK_10#	B13	PRSNT_N
A12	PCH_CLK_10	B12	PERST_BUF_N
A11	GND	B11	GND
A10	WAKE#	B10	SMB_DATA
A9	-	B9	SMB_CLK
A8	GND	B8	GND
A7	RXN14	B7	TXN14
A6	RXP14	B6	TXP14
A5	GND	B5	GND
A4	RXN13	B4	TXN13
A3	RXP13	B3	TXP13
A2	GND	B2	GND
A1	+3V	B1	-
1	NP_NC1	3	P_GND1
2	NP_NC2	4	P_GND2

OCU2			
Pin	Defeinition	Pin	Defeinition
A21	-	B21	+3V
A20	GND	B20	GND
A19	RXN20	B19	TXN20
A18	RXP20	B18	TXP20
A17	GND	B17	GND
A16	RXN19	B16	TXN19
A15	RXP19	B15	TXP19
A14	GND	B14	GND
A13	PCH_CLK_11#	B13	PRSENT_N
A12	PCH_CLK_11	B12	PERST_BUF_N
A11	GND	B11	GND
A10	WAKE#	B10	SMB_DATA
A9	-	B9	SMB_CLK
A8	GND	B8	GND
A7	RXN18	B7	TXN18
A6	RXP18	B6	TXP18
A5	GND	B5	GND
A4	RXN17	B4	TXN17
A3	RXP17	B3	TXP17
A2	GND	B2	GND
A1	+3V	B1	-
1	NP_NC1	3	P_GND1
2	NP_NC2	4	P_GND2

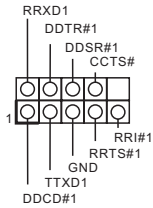
OCU3			
Pin	Definition	Pin	Definition
A21	-	B21	+3V
A20	GND	B20	GND
A19	RXN12	B19	TXN12
A18	RXP12	B18	TXP12
A17	GND	B17	GND
A16	RXN11	B16	TXN11
A15	RXP11	B15	TXP11
A14	GND	B14	GND
A13	PCH_CLK_8#	B13	PRSNT_N
A12	PCH_CLK_8	B12	PERST_BUF_N
A11	GND	B11	GND
A10	WAKE#	B10	SMB_DATA
A9	-	B9	SMB_CLK
A8	GND	B8	GND
A7	RXN10	B7	TXN10
A6	RXP10	B6	TXP10
A5	GND	B5	GND
A4	RXN9	B4	TXN9
A3	RXP9	B3	TXP9
A2	GND	B2	GND
A1	+3V	B1	-
1	NP_NC1	3	P_GND1
2	NP_NC2	4	P_GND2

USB 3.2 Gen1 Header
(19-pin USB3_3_4)
(see p.6, No. 25)



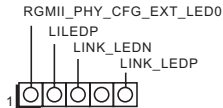
Besides two default USB 3.2 Gen1 ports on the I/O panel, there is one USB 3.2 Gen1 header on this motherboard. This USB 3.2 Gen1 header can support two USB 3.2 Gen1 ports.

Serial Port Header
(9-pin COM1)
(see p.6, No. 35)



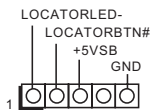
This COM header supports a serial port module.

IPMI LAN LED Header
(4-pin IPMI_LED1)
(see p.6, No. 30)



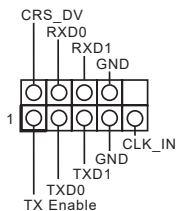
This 4-pin header is used for the IPMI LAN status indicator.

UID Header
(4-pin UID_HD)
(see p.6, No. 5)



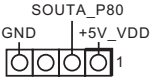
This 4-pin header is used for the Unit Identification LED and switch functions.

NCSI Header
(9-pin NCSI1)
(see p.6, No. 3)



The onboard NCSI header is used for external connections..

80 Debug Header
(4-pin P80_DEBUG)
(see p.6, No. 25)



This header is used to connect to a debug display for showing the motherboard debug information.

2.9 Dr. Debug

Dr. Debug is used to provide code information, which makes troubleshooting even easier. Please see the diagrams below for reading the Dr. Debug codes.

Code	Description
0x10	PEI_CORE_STARTED
0x11	PEI_CAR_CPU_INIT
0x15	PEI_CAR_NB_INIT
0x19	PEI_CAR_SB_INIT
0x31	PEI_MEMORY_INSTALLED
0x32	PEI_CPU_INIT
0x33	PEI_CPU_CACHE_INIT
0x34	PEI_CPU_AP_INIT
0x35	PEI_CPU_BSP_SELECT
0x36	PEI_CPU_SMM_INIT
0x37	PEI_MEM_NB_INIT
0x3B	PEI_MEM_SB_INIT
0x4F	PEI_DXE_IPL_STARTED
0x60	DXE_CORE_STARTED
0x61	DXE_NVRAM_INIT
0x62	DXE_SBRUN_INIT

0x63	DXE_CPU_INIT
0x68	DXE_NB_HB_INIT
0x69	DXE_NB_INIT
0x6A	DXE_NB_SMM_INIT
0x70	DXE_SB_INIT
0x71	DXE_SB_SMM_INIT
0x72	DXE_SB_DEVICES_INIT
0x78	DXE_ACPI_INIT
0x79	DXE_CSM_INIT
0x90	DXE_BDS_STARTED
0x91	DXE_BDS_CONNECT_DRIVERS
0x92	DXE_PCI_BUS_BEGIN
0x93	DXE_PCI_BUS_HPC_INIT
0x94	DXE_PCI_BUS_ENUM
0x95	DXE_PCI_BUS_REQUEST_RESOURCES
0x96	DXE_PCI_BUS_ASSIGN_RESOURCES
0x97	DXE_CON_OUT_CONNECT
0x98	DXE_CON_IN_CONNECT

0x99	DXE_SIO_INIT
0x9A	DXE_USB_BEGIN
0x9B	DXE_USB_RESET
0x9C	DXE_USB_DETECT
0x9D	DXE_USB_ENABLE
0xA0	DXE_IDE_BEGIN
0xA1	DXE_IDE_RESET
0xA2	DXE_IDE_DETECT
0xA3	DXE_IDE_ENABLE
0xA4	DXE_SCSI_BEGIN
0xA5	DXE_SCSI_RESET
0xA6	DXE_SCSI_DETECT
0xA7	DXE_SCSI_ENABLE
0xA8	DXE_SETUP_VERIFYING_PASSWORD
0xA9	DXE_SETUP_START
0xAB	DXE_SETUP_INPUT_WAIT
0xAD	DXE_READY_TO_BOOT
0xAE	DXE_LEGACY_BOOT

0xAF	DXE_EXIT_BOOT_SERVICES
0xB0	RT_SET_VIRTUAL_ADDRESS_MAP_BEGIN
0xB1	RT_SET_VIRTUAL_ADDRESS_MAP_END
0xB2	DXE_LEGACY_OPROM_INIT
0xB3	DXE_RESET_SYSTEM
0xB4	DXE_USB_HOTPLUG
0xB5	DXE_PCI_BUS_HOTPLUG
0xB6	DXE_NVRAM_CLEANUP
0xB7	DXE_CONFIGURATION_RESET
0xF0	PEI_RECOVERY_AUTO
0xF1	PEI_RECOVERY_USER
0xF2	PEI_RECOVERY_STARTED
0xF3	PEI_RECOVERY_CAPSULE_FOUND
0xF4	PEI_RECOVERY_CAPSULE_LOADED
0xE0	PEI_S3_STARTED
0xE1	PEI_S3_BOOT_SCRIPT
0xE2	PEI_S3_VIDEO_REPOST

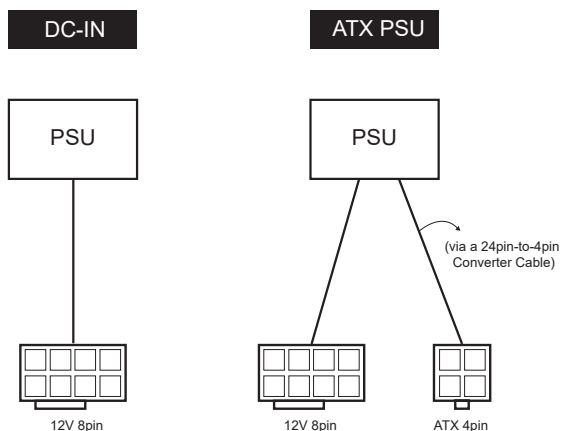
0xE3	PEI_S3_OS_WAKE
0x50	PEI_MEMORY_INVALID_TYPE
0x53	PEI_MEMORY_NOT_DETECTED
0x55	PEI_MEMORY_NOT_INSTALLED
0x57	PEI_CPU_MISMATCH
0x58	PEI_CPU_SELF_TEST_FAILED
0x59	PEI_CPU_NO_MICROCODE
0x5A	PEI_CPU_ERROR
0x5B	PEI_RESET_NOT_AVAILABLE
0xD0	DXE_CPU_ERROR
0xD1	DXE_NB_ERROR
0xD2	DXE_SB_ERROR
0xD3	DXE_ARCH_PROTOCOL_NOT_AVAILABLE
0xD4	DXE_PCI_BUS_OUT_OF_RESOURCES
0xD5	DXE_LEGACY_OPROM_NO_SPACE
0xD6	DXE_NO_CON_OUT
0xD7	DXE_NO_CON_IN

0xD8	DXE_INVALID_PASSWORD
0xD9	DXE_BOOT_OPTION_LOAD_ERROR
0xDA	DXE_BOOT_OPTION_FAILED
0xDB	DXE_FLASH_UPDATE_FAILED
0xDC	DXE_RESET_NOT_AVAILABLE
0xE8	PEI_MEMORY_S3_RESUME_FAILED
0xE9	PEI_S3_RESUME_PPI_NOT_FOUND
0xEA	PEI_S3_BOOT_SCRIPT_ERROR
0xEB	PEI_S3_OS_WAKE_ERROR

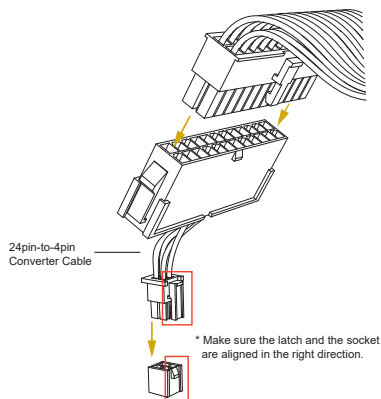
2.10 ATX PSU / DC-IN Power Connections

This motherboard supports both +12V DC and ATX power input. Please refer to the table below for the required connections between the motherboard and the power supply.

Connector	DC-IN	ATX PSU
12V 8pin	O	O
ATX 4pin	X	O (with the bundled ATX 24pin-to-4pin converter cable)



The following diagram illustrates how to connect the bundled ATX 24pin-to-4pin converter cable.



2.11 Unit Identification purpose LED/Switch

Use the UID button to locate the server working on behind a rack of servers.

Unit Identification
purpose LED (UID_
LED1)/Switch
(UID1)



UID
Switch



UID
LED

When the UID button on the front or rear panel is pressed, the front/rear UID blue LED indicator will be turned on. Press the UID button again to turn off the indicator.



1. Press and hold the UID button for 4 seconds, BMC will trigger an external reset.
2. Press and hold the UID button for 10 seconds, BMC will reset and load default values.

2.12 Dual LAN and Teaming Operation Guide

Dual LAN with Teaming enabled on this motherboard allows two single connections to act as one single connection(s) for twice the transmission bandwidth, making data transmission more effective and improving the quality of transmission of distant images. Fault tolerance on the dual LAN network prevents network downtime by transferring the workload from a failed port to a working port.



The speed of transmission is subject to the actual network environment or status even with Teaming enabled.

Before setting up Teaming, please make sure whether the Switch (or Router) supports Teaming (IEEE 802.3ad Link Aggregation). Specify a preferred adapter in Intel PROSet. Under normal conditions, the Primary adapter handles all non-TCP/IP traffic. The Secondary adapter will receive fallback traffic if the primary fails. If the Preferred Primary adapter fails, but is later restored to an active status, control is automatically switched back to the Preferred Primary adapter.

Step 1

From **Device Manager**, open the properties of a team.

Step 2

Click the **Settings** tab.

Step 3

Click the **Modify Team** button.

Step 4

Select the adapter that want to be the primary adapter and click the **Set Primary** button.

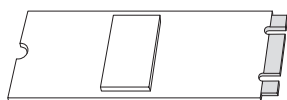
If do not specify a preferred primary adapter, the software will choose an adapter of the highest capability (model and speed) to act as the default primary. If a failover occurs, another adapter becomes the primary. The adapter will, however, rejoin the team as a non-primary.

2.13 M.2 SSD Module Installation Guide

The M.2 Socket (M2_1, Key M) supports type 2280 M.2 PCI Express module up to Gen4 x4 (16GT/s x4).

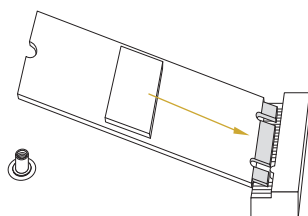
The M.2 Socket (M2_2, Key M) supports type 2280 M.2 PCI Express module up to Gen5 x4 (32GT/s x4).

Installing the M.2 SSD Module



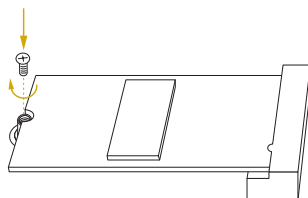
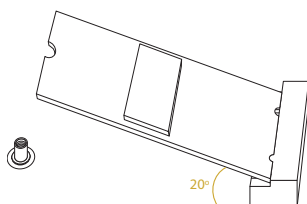
Step 1

Prepare a M.2 SSD module and the screw.



Step 2

Gently insert the M.2 SSD module into the M.2 slot. Please be aware that the M.2 SSD module only fits in one orientation.



Step 3

Tighten the screw with a screwdriver to secure the module into place. Please do not overtighten the screw as this might damage the module.

Chapter 3 UEFI Setup Utility

3.1 Introduction

This section explains how to use the UEFI SETUP UTILITY to configure the system. The UEFI chip on the motherboard stores the UEFI SETUP UTILITY. Run the UEFI SETUP UTILITY when starting up the computer. Please press <F2> or during the Power-On-Self-Test (POST) to enter the UEFI SETUP UTILITY; otherwise, POST will continue with its test routines.

Restart the system by pressing <Ctrl> + <Alt> + <Delete> to enter the UEFI SETUP UTILITY after POST, or by pressing the reset button on the system chassis. This allows user to restart by turning the system off and then back on.



Because the UEFI software is constantly being updated, the following UEFI setup screens and descriptions are for reference purpose only, and they may not exactly match what seeing on the screen.

3.1.1 UEFI Menu Bar

The top of the screen has a menu bar with the following selections:

Item	Description
Main	To set up the system time/date information
OC Tweaker	For overclocking configurations
Advanced	To set up the advanced UEFI features
Security	To set up the security features
Server Mgmt	To manage the server
Event Logs	For event log configuration
Boot	To set up the default system device to locate and load the Operating System
Exit	To exit the current screen or the UEFI SETUP UTILITY

Use <←> key or <→> key to choose among the selections on the menu bar, and then press <Enter> to get into the sub screen.

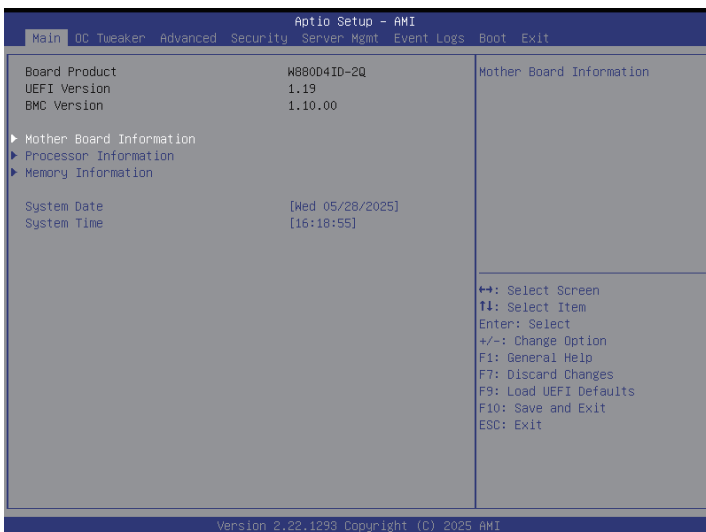
3.1.2 Navigation Keys

Please check the following table for the function description of each navigation key.

Navigation Key(s)	Function Description
← / →	Moves cursor left or right to select Screens
↑ / ↓	Moves cursor up or down to select items
+ / -	To change option for the selected items
<Tab>	Switch to next function
<Enter>	To bring up the selected screen
<PGUP>	Go to the previous page
<PGDN>	Go to the next page
<HOME>	Go to the top of the screen
<END>	Go to the bottom of the screen
<F1>	To display the General Help Screen
<F7>	Discard changes and exit the UEFI SETUP UTILITY
<F9>	Load optimal default values for all the settings
<F10>	Save changes and exit the UEFI SETUP UTILITY
<F12>	Print screen
<ESC>	Jump to the Exit Screen or exit the current screen

3.2 Main Screen

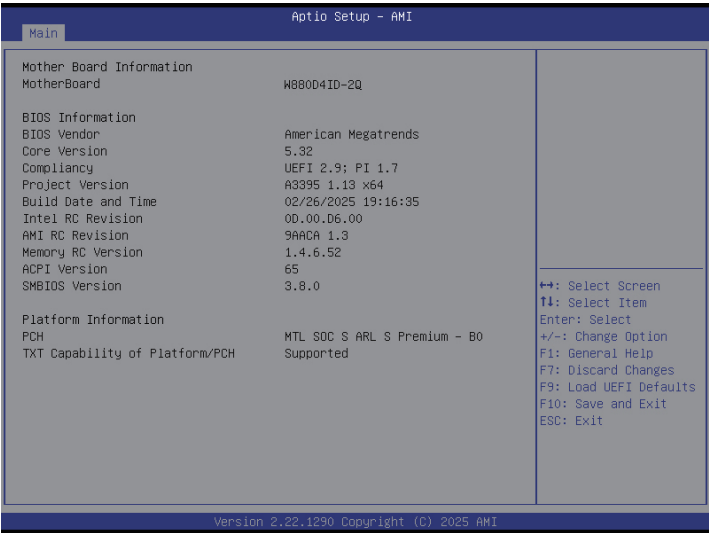
Enter the UEFI SETUP UTILITY, the Main screen will appear and display the system overview. The Main screen provides system overview information and allows user to set the system time and date.



The screenshots in this user manual are examples and for references only. The actual images may slightly vary depending on the model and the version used.

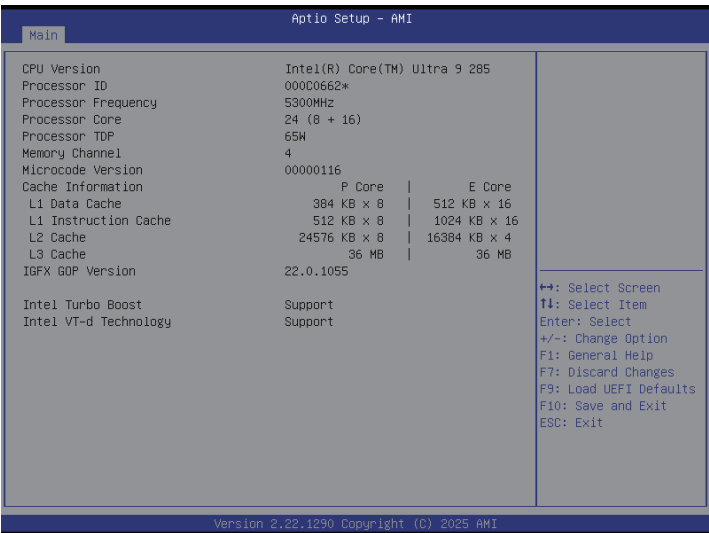
3.2.1 Motherboard Information

Press [Enter] to view the information of the motherboard.



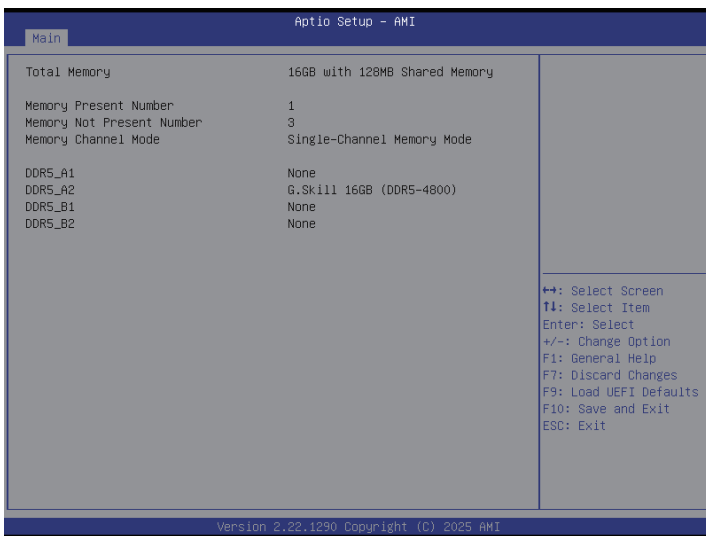
3.2.2 Processor Information

Press [Enter] to view the information of the processor.

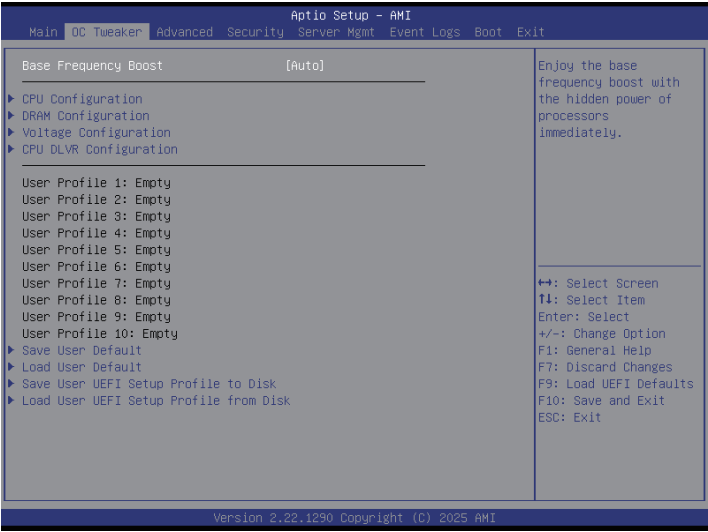


3.2.3 Memory Information

Press [Enter] to view the information of the memory.



3.3 OC Tweaker



Base Frequency Boost

Enjoy the base frequency boost with the hidden power of processors immediately.

CPU Configuration

Configure CPU options.

DRAM Configuration

Configure DRAM Timing.

Voltage Configuration

Configure Voltage options.

CPU DLVR Configuration

Configure CPU DLVR options.

Save User Default

Type a profile name and press enter to save the settings as user default.

Load User Default

Load previously saved user defaults.

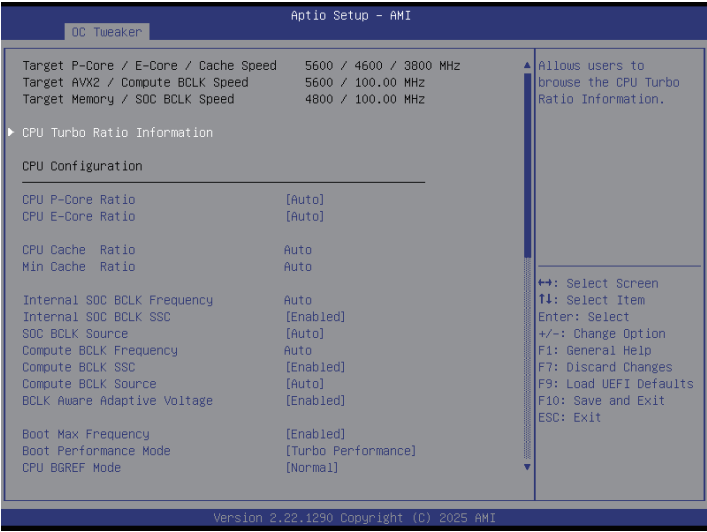
Save User UEFI Setup Profile to Disk

It helps user to save current UEFI settings as an user profile to disk.

Load User UEFI Setup Profile from Disk

Load previous saved profile from the disk.

3.3.1 CPU Configuration



CPU Turbo Ratio Information

The CPU speed is determined by the CPU P-Core Ratio multiplied with the BCLK. Increasing the CPU P-Core Ratio will increase the internal CPU clock speed without affecting the clock speed of other components.

CPU P-Core Ratio

The CPU speed is determined by the CPU P-Core Ratio multiplied with the BCLK. Increasing the CPU P-Core Ratio will increase the internal CPU clock speed without affecting the clock speed of other components.

CPU E-Core Ratio

The E-Core speed is determined by the E-Core Ratio multiplied with the BCLK. Increasing the E-Core Ratio will increase the internal E-Core clock speed without affecting the clock speed of other components.

CPU Cache Ratio

The CPU Internal Bus Speed Ratio. The maximum should be the same as the CPU Ratio.

Min Cache Ratio Limit

The CPU Internal Bus Speed minimum ratio.

CPU Flex Ratio Override

Enable or disable CPU Flex Ratio Programming. Flex Ratio can lower maximum non-turbo, especially for CPU without turbo function.

Internal SOC BCLK Frequency

Configure SOC-die internal BCLK frequency.

Internal SOC BCLK SSC

Configure SOC-die spread spectrum clocking.

SOC BCLK Source

Configure source for SOC BCLK. The BCLK affect memory, iGPU, NPU, D2D Fabric, SAF/ NOC Fabric.

Compute BCLK Frequency

Configure compute-die internal BCLK frequency.

Compute BCLK SSC

Configure compute-die spread spectrum clocking.

Compute BCLK Source

Configure source for compute BCLK. To control the compute BCLK independently, please select Compute-Die Internal BCLK. The BCLK affect P-Core, E-Core and Cache.

BCLK Aware Adaptive Voltage

BCLK Aware Adaptive Voltage enable/disable. When enabled, pcode will be aware of the BCLK frequency when calculating the CPU V/F curves. This is ideal for BCLK OC to avoid high voltage overrides.

Boot Max Frequency

Select this to enable or disable Boot Maximum Frequency in CPU strap.

Boot Performance Mode

Select the performance state that the BIOS will set before OS handoff. Max Battery mode will set CPU ratio as xB till OS handoff. This option is suggested for BCLK overclocking.

CPU BGREF Mode

Select CPU Bandgap Reference Mode between Normal and Bandgap Bypassed.
CPU Bandgap Reference Mode - the default voltage is Normal.

VCCIA Boot Voltage

Select VCCIA boot voltage between Nominal and High Voltage. VCCIA boot Voltage - The default voltage is Nominal, to support the high voltage, BIOS can program VCCIA boot voltage higher than 1.65v (max 2.01v).

VCCSA Boot Voltage

Select VCCSA boot voltage between Nominal and High Voltage (up to 1.2/1.3V). VCCSA boot Voltage - The default voltage is Nominal,.

Ring to Core Ratio Offset

Disable Ring to Core Ratio Offset so the ring and core can run at the same frequency.

FLL Overclocking Mode

Nominal is good for normal core ratio overclocking. Elevated and Extremely Elevated are good for high BCLK OC.

SA PLL Frequency Override

Configure SA PLL Frequency.

BCLK TSC HW Fixup

BCLK TSC HW Fixup disable during TSC copy from PMA to APIC.

Intel SpeedStep Technology

Intel SpeedStep technology allows processors to switch between multiple frequencies and voltage points for better power saving and heat dissipation. CPU turbo ratio can be fixed when Intel SpeedStep Technology set Disabled and Intel Turbo Boost Technology set Enabled.

Intel Turbo Boost Technology

Intel Turbo Boost Technology enables the processor to run above its base operating frequency when the operating system requests the highest performance state.

Intel Speed Shift Technology

Enable/Disable Intel Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states.

To Get best support for Intel Turbo Boost Max Technology 3.0 (ITBMT 3.0), it needs to enable Intel Speed Shift Technology. If the CPU does not support ITBMT 3.0, option will still grayed out.

Intel Turbo Boost Max Technology 3.0

Select this to enable or disable Intel Turbo Boost Technology 3.0 (ITBMT 3.0) support. Disabling will report the maximum ratio of the slowest core in _CPC object. Processors supporting the ITBMT 3.0 feature contain at least one processor core whose maximum ratio is higher than the others.

Intel Dynamic Tuning Technology

Select this to enable or disable Intel Dynamic Platform Thermal Framework.

Long Duration Power Limit

Configure Package Power Limit 1 in watts. When the limit is exceeded, the CPU ratio will be lowered after a period of time. A lower limit can protect the CPU and save power, while a higher limit may improve performance.

Long Duration Maintained

Configure the period of time until the CPU ratio is lowered when the Long Duration Power Limit is exceeded.

Short Duration Power Limit

Configure Package Power Limit 2 in watts. When the limit is exceeded, the CPU ratio will be lowered immediately. A lower limit can protect the CPU and save power, while a higher limit may improve performance.

CPU Core Unlimited Current Limit

To unlock voltage regulator current limit completely, set this option to Enabled.

CPU Core Current Limit

Configure the Voltage Regulator Current Limit. This value represents the Maximum instantaneous current allowed at any given time.

GT Unlimited Current Limit

To unlock voltage regulator current limit completely, set this option to Enabled.

GT Current Limit

Configure the Voltage Regulator Current Limit. This value represents the Maximum instantaneous current allowed at any given time.

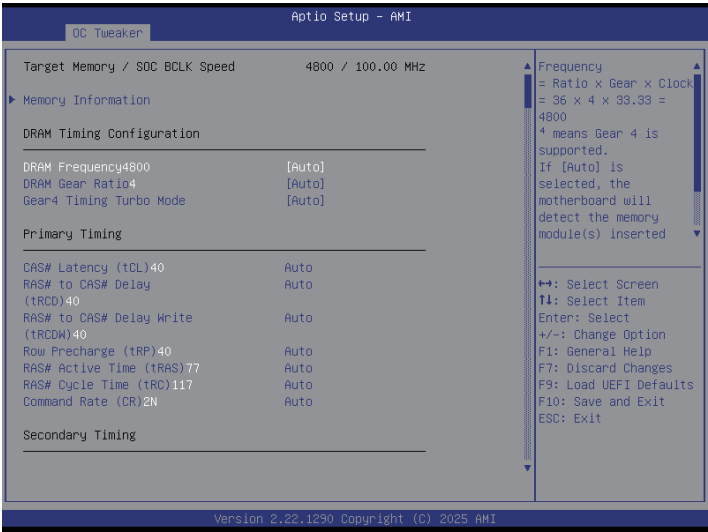
GT CEP Enable

Select this to enable or disable GT CEP (Current Excursion Protection) Support.

IA CEP Enable

Select this to enable or disable IA CEP (Current Excursion Protection) Support.

3.3.2 DRAM Configuration



Memory Information

Allows users to browse the serial presence detect (SPD) and Intel extreme memory profile (XMP) for DDR modules.

DRAM Timing Configuration

DRAM Frequency

If [Auto] is selected, the motherboard will detect the memory module(s) inserted and assign the appropriate frequency automatically.

DRAM Gear Ratio

High gear is good for high frequency.

Gear4 Timing Turbo Ratio

The default value is [Auto] for Gear4 Timing Turbo Mode.

Primary Timing

CAS# Latency (tCL)

The time between sending a column address to the memory and the beginning of the data in response.

RAS# to CAS# Delay (tRCD)

The number of clock cycles required between the opening of a row of memory and accessing columns within it.

RAS# to CAS# Delay Write (tRCDW)

The number of clock cycles required between the opening of a row of memory and accessing columns within it.

Row Precharge Time (tRP)

The number of clock cycles required between the issuing of the precharge command and opening the next row.

RAS# Active Time (tRAS)

The number of clock cycles required between a bank active command and issuing the precharge command.

RAS# Cycle Time (tRC)

Configure the minimum active to active/Refresh Time.

Command Rate (CR)

The delay between when a memory chip is selected and when the first active command can be issued.

Secondary Timing**Write Recovery Time (tWR)**

The amount of delay that must elapse after the completion of a valid write operation, before an active bank can be precharged.

Refresh Cycle Time2 (tRFC2)

The number of clocks from a Refresh command until the first Activate command to the same rank.

Refresh Cycle Time per Bank (tRFCpb)

The number of clocks that a per bank Refresh command takes to complete.

CAS to CAS CMD Delay (tCCD_L)

Internal Write to Read Command Delay Time.

Write CAS to CAS CMD Delay (tCCD_L_WR)

Internal Write to Write Command Delay Time.

Write to Read Delay (tWTR_L)

The number of clocks between the last valid write operation and the next read command to the same internal bank.

Write to Read Delay (tWTR_S)

The number of clocks between the last valid write operation and the next read command to the same internal bank.

RAS to RAS Delay (tRRD_L)

The number of clocks between two rows activated in different banks of the same rank.

RAS to RAS Delay (tRRD_S)

The number of clocks between two rows activated in different banks of the same rank.

Read to Precharge (tRTP)

The number of clocks that are inserted between a read command to a row pre-charge command to the same rank.

Four Activate Window (tFAW)

The time window in which four activates are allowed the same rank.

CAS Write Latency (tCWL)

Configure CAS Write Latency.

Refresh Delay Same Bank (tREFSBRD)

Configure tREFSBRD, Same Bank Refresh to ACT delay.

Refresh Interval x9 (tREFIx9)

Configure tREFIx9 for max time between refreshes per rank.

Refresh Interval (tREFI)

Configure refresh cycles at an average periodic interval.

Power Down Timing

tCKE

Configure the period of time the DDR5 initiates a minimum of one refresh command internally once it enters Self-Refresh mode.

tXP

Configure the CKE low exit time before a new command can be sent after CKE comes up.

tCPDED

Configure CPDED.

tRDPDEN

Configure CASrd to CKE low time.

tWRPDEN

Configure CASwr to CKE low time.

tCKCKEH

Configure CKCKEH.

tCSH

Configure tCSH.

tCSL

Configure tCSL

tCA2CS

Configure tCA2CS.

tPRPDEN

Configure tPRPDEN. tPRPDEN, tACTPDEN, tREFPDEN will use this single value.

tOSCO

Configure tOSCO.

tMRR

Configure tMRR.

tMRD

Configure DDR tMRD timing parameter. MRS to MRS minimum delay in number of DCLK.

Turn Around Timing**TAT Pre Training****tRDRD_sg**

Configure between module read to read delay.

tRDRD_dg

Configure between module read to read delay.

tRDRD_dr

Configure between module read to read delay.

tRDRD_dd

Configure between module read to read delay.

tRDWR_sg

Configure between module read to write delay.

tRDWR_dg

Configure between module read to write delay.

tRDWR_dr

Configure between module read to write delay.

tRDWR_dd

Configure between module read to write delay.

tWRRD_sg

Configure between module write to read delay.

tWRRD_dg

Configure between module write to read delay.

tWRRD_dr

Configure between module write to read delay.

tWRRD_dd

Configure between module write to read delay.

tWRWR_sg

Configure between module write to write delay.

tWRWR_dg

Configure between module write to write delay.

tWRWR_dr

Configure between module write to write delay.

tWRWR_dd

Configure between module write to write delay.

TAT Post Training**tRDRD_sg**

Configure between module write to read delay.

tRDRD_dg

Configure between module write to read delay.

tRDRD_dr

Configure between module write to read delay.

tRDRD_dd

Configure between module write to read delay.

tRDWR_sg

Configure between module write to read delay.

tRDWR_dg

Configure between module write to read delay.

tRDWR_dr

Configure between module write to read delay.

tRDWR_dd

Configure between module write to read delay.

tWRRD_sg

Configure between module write to read delay.

tWRRD_dg

Configure between module write to read delay.

tWRRD_dr

Configure between module write to read delay.

tWRRD_dd

Configure between module write to read delay.

tWRWR_sg

Configure between module write to write delay.

tWRWR_dg

Configure between module write to write delay.

tWRWR_dr

Configure between module write to write delay.

tWRWR_dd

Configure between module write to write delay.

Round Trip Timing

RTL IO Delay Offset

Configure round trip latency IO delay initial offset.

RTL Offset (MC0 A1/A2)

Configure round trip latency Offset value.

RTL Offset (MC1 A1/A2)

Configure round trip latency Offset value.

RTL Offset (MC0 B1/B2)

Configure round trip latency Offset value.

RTL Offset (MC1 B1/B2)

Configure round trip latency Offset value.

ODT Setting

RTT WR (A1)

Configure the memory on die termination resistors RTT WR.

RTT NOM RD (A1)

Configure the memory on die termination resistors RTT NOM RD.

RTT NOM WR (A1)

Configure the memory on die termination resistors RTT NOM WR.

RTT PARK (A1)

Configure the memory on die termination resistors RTT PARK.

RTT PARK DQS (A1)

Configure the memory on die termination resistors RTT PARK DQS.

RTT CA Gruop A (A1)

Configure the memory on die termination resistors RTT CA Group A.

RTT CS Gruop A (A1)

Configure the memory on die termination resistors RTT CS Group A.

RTT CK Gruop A (A1)

Configure the memory on die termination resistors RTT CK Group A.

RTT CA Gruop B (A1)

Configure the memory on die termination resistors RTT CA Group B.

RTT CS Gruop B (A1)

Configure the memory on die termination resistors RTT CS Group B.

RTT CK Gruop B (A1)

Configure the memory on die termination resistors RTT CK Group B.

RTT WR (A2)

Configure the memory on die termination resistors RTT WR.

RTT NOM RD (A2)

Configure the memory on die termination resistors RTT NOM RD.

RTT NOM WR (A2)

Configure the memory on die termination resistors RTT NOM WR.

RTT PARK (A2)

Configure the memory on die termination resistors RTT PARK.

RTT PARK DQS (A2)

Configure the memory on die termination resistors RTT PARK DQS.

RTT CA Gruop A (A2)

Configure the memory on die termination resistors RTT CA Group A.

RTT CS Gruop A (A2)

Configure the memory on die termination resistors RTT CS Group A.

RTT CK Gruop A (A2)

Configure the memory on die termination resistors RTT CK Group A.

RTT CA Gruop B (A2)

Configure the memory on die termination resistors RTT CA Group B.

RTT CS Gruop B (A2)

Configure the memory on die termination resistors RTT CS Group B.

RTT CK Gruop B (A2)

Configure the memory on die termination resistors RTT CK Group B.

RTT WR (B1)

Configure the memory on die termination resistors RTT WR.

RTT NOM RD (B1)

Configure the memory on die termination resistors RTT NOM RD.

RTT NOM WR (B1)

Configure the memory on die termination resistors RTT NOM WR.

RTT PARK (B1)

Configure the memory on die termination resistors RTT PARK.

RTT PARK DQS (B1)

Configure the memory on die termination resistors RTT PARK DQS.

RTT CA Gruop A (B1)

Configure the memory on die termination resistors RTT CA Group A.

RTT CS Gruop A (B1)

Configure the memory on die termination resistors RTT CS Group A.

RTT CK Gruop A (B1)

Configure the memory on die termination resistors RTT CK Group A.

RTT CA Gruop B (B1)

Configure the memory on die termination resistors RTT CA Group B.

RTT CS Gruop B (B1)

Configure the memory on die termination resistors RTT CS Group B.

RTT CK Gruop B (B1)

Configure the memory on die termination resistors RTT CK Group B.

RTT WR (B2)

Configure the memory on die termination resistors RTT WR.

RTT NOM RD (B2)

Configure the memory on die termination resistors RTT NOM RD.

RTT NOM WR (B2)

Configure the memory on die termination resistors RTT NOM WR.

RTT PARK (B2)

Configure the memory on die termination resistors RTT PARK.

RTT PARK DQS (B2)

Configure the memory on die termination resistors RTT PARK DQS.

RTT CA Gruop A (B2)

Configure the memory on die termination resistors RTT CA Group A.

RTT CS Gruop A (B2)

Configure the memory on die termination resistors RTT CS Group A.

RTT CK Gruop A (B2)

Configure the memory on die termination resistors RTT CK Group A.

RTT CA Gruop B (B2)

Configure the memory on die termination resistors RTT CA Group B.

RTT CS Gruop B (B2)

Configure the memory on die termination resistors RTT CS Group B.

RTT CK Gruop B (B2)

Configure the memory on die termination resistors RTT CK Group B.

MRS Setting**Pull Up Output Driver Impedance MC0**

Configure the Pull Up Output Driver Impedance for DRAM A1 A2 MRS MR5 OP [2:1].

Pull Up Output Driver Impedance MC1

Configure the Pull Up Output Driver Impedance for DRAM B1 B2 MRS MR5 OP [2:1].

Pull Down Output Driver Impedance MC0

Configure the Pull Down Output Driver Impedance for DRAM A1 A2 MRS MR5 OP [7:6].

Pull Down Output Driver Impedance MC1

Configure the Pull Down Output Driver Impedance for DRAM B1 B2 MRS MR5 OP [7:6].

Read Preamble

Configure the Read Preamble for DRAM MRS MR8 OP[2:0].

Write Preamble

Configure the Write Preamble for DRAM MRS MR8 OP[4:3].

Read Postamble

Configure the Read Postamble for DRAM MRS MR8 OP[6].

Write Postamble

Configure the Write Postamble for DRAM MRS MR8 OP[7].

tCCD_L/tDLLK

Configure the tCCD_L/tCCD_L_WR/tCCD_L_WR2/tDLLK for DRAM MRS MR13 OP[3:0].

Training Setting

Memory Training Algorithms

Select this to enable or disable Memory Training Algorithms.

Advanced Setting

Realtime Memory Timing

Configure the realtime memory timings.

[Enabled] The system will allow performing realtime memory timing changes after MRC_DONE.

Force Reset Type

Force Reset Type after F10 Save Changes and Exit.

Retrain on Fast Fail

Restart MRC in Cold mode if SW MemTest fails during Fast flow. The default value is [Enabled].

Retrain to Working Channel

Restart MRC in Cold mode after disabling failing channel. The default value is [Disabled].

Exit On Failure (MRC)

Exit On Failure for MRC training steps.

Force ColdReset

Force ColdReset OR Choose MrcColdBoot mode, when Coldboot is required during MRC execution. Note: If ME 5.0MB is present, ForceColdReset is required!

Reset for MRC Failed

Reset system after MRC training is failed.

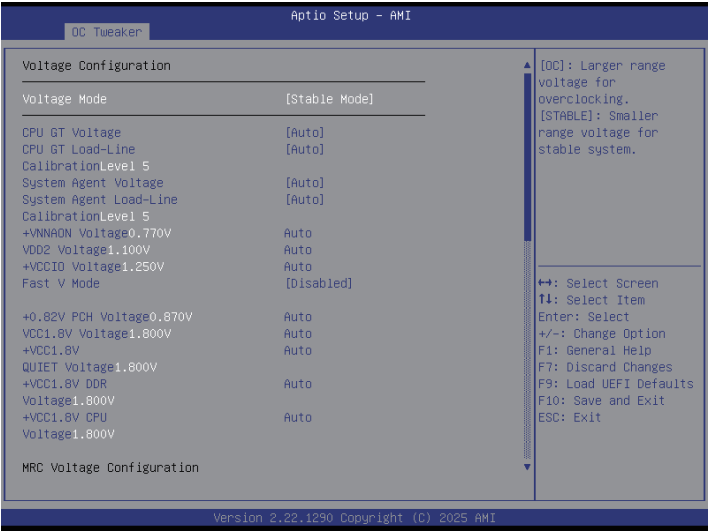
MRC Training on Warm Boot

When enabled, memory training will be excuted when warm boot.

MRC Fast Boot

When enabled, portions of memory reference code will be skipped when possibe to increase boot speed.

3.3.3 Voltage Configuration



Voltage Mode

- [OC] - Larger range voltage for overclocking.
- [Stable] - Smaller range voltage for stable system.

CPU GT Voltage

Input voltage for the processor by the external voltage regulator.

CPU GT Load-Line Calibration

CPU GT Load-Line Calibration helps prevent GT voltage droop when the system is under heavy loading.

System Agent Voltage

Input voltage for the processor by the external voltage regulator.

System Agent Load-Line Calibration

System Agent Load-Line Calibration helps prevent System Agent voltage droop when the system is under heavy load.

+VNNAON Voltage

Configure the voltage for the +VNNAON.

VDD2 Voltage

Configure the voltage for the VDD2.

+VCCIO Voltage

Configure the voltage for the +VCCIO.

Fast V Mode

The default for this item is [Disabled].

+0.82V PCH Voltage

Configure the voltage for the +0.82V PCH.

VCC1.8V Voltage

Configure the voltage for the VCC1.8V.

+VCC1.8V QUIET Voltage

Configure the voltage for the +VCC1.8V QUIET.

+VCC1.8V DDR Voltage

Configure the voltage for the +VCC1.8V DDR.

+VCC1.8V CPU Voltage

Configure the voltage for the +VCC1.8V CPU.

MRC Voltage Configuration

Vdd2Mv Voltage

The VR Rail tied to the DRAM. Usually, it equals to or lower than VDD2 voltage.

Vddq Voltage

Configure the CPU FIVR TX Vddq.

Vcclog Voltage

Configure the CPU FIVR VCC IOG.

VccClk Voltage

Configure the CPU FIVR VCC CLK.

DDR5 PMIC Configuration

PMIC Voltage Option

Choose separate to individually adjust DIMM PMIC.

VDD Voltage

Configure the VDD Voltage supported by PMIC at DRAM side.

VDD Voltage Range

JEDEC Standard is ranges from 0.800V to 1.435V. OC Demand is ranges from 0.800V to 2.070V. OC Demand may not be applied if PMIC OC CAP is JEDEC PMIC, user can check it via Memory Information tool.

VDDQ Voltage

Configure the VDDQ Voltage supported by PMIC at DRAM side.

VDDQ Voltage Range

JEDEC Standard is ranges from 0.800V to 1.435V. OC Demand is ranges from 0.800V to 2.070V. OC Demand may not be applied if PMIC OC CAP is JEDEC PMIC, user can check it via Memory Information tool.

VPP Voltage

Configure the memory VPP Voltage supported by PMIC at DRAM side.

PMIC Protection Unlock

Configure the PMIC Protection Unlock setting.

Current Limiter VDD

Output current limiter warning threshold setting.

Current Limiter VDDQ

Output current limiter warning threshold setting.

Current Limiter VPP

Output current limiter warning threshold setting.

AVX Configuration

AVX2 Voltage Guardband Scale Factor

AVX2 Voltage Guardband Scale Factor controls the voltage guardband applied to AVX2 workloads. A value > 1.00 will increase the voltage guardband, and < 1.00 will decrease the voltage guardband.

VR Configuration

IA AC Loadline

The nominal CPU VID voltage may be adjusted by AC Load Line. Higher AC loadline get higher VID, especially for high frequency or heavy loading.

IA DC Loadline

The power calculations done by the CPU may be adjusted by DC Load Line. DC Loadline in mOhms. Range is 0-20.00. 0=AUTO/HW default.

3.3.4 CPU DLVR Configuration



Voltage Mode

[OC] - Larger range voltage for overclocking.

[Stable] - Smaller range voltage for stable system.

Core Input Voltage

Input voltage for the processor by the external voltage regulator.

Core Input Voltage Load-Line Calibration

CPU Load-Line Calibration helps prevent CPU voltage droop when the system is under heavy loading.

CPU CORE SVID_VR_HOT

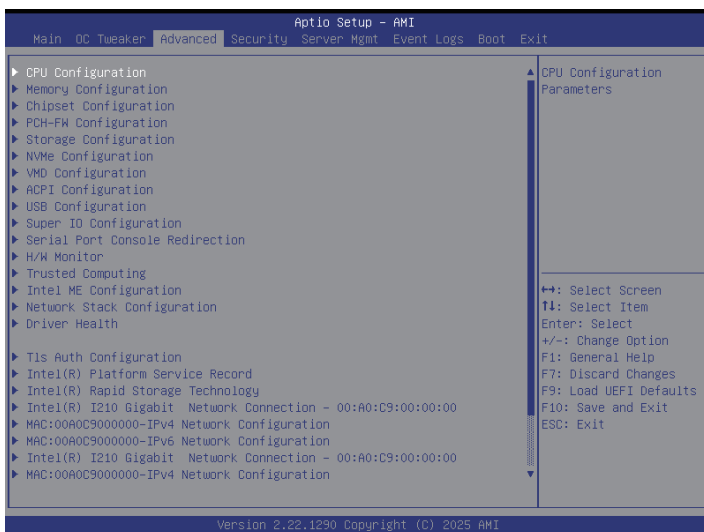
Configure CPU CORE SVID_VR_HOT.

CPU CORE OTP Temperature

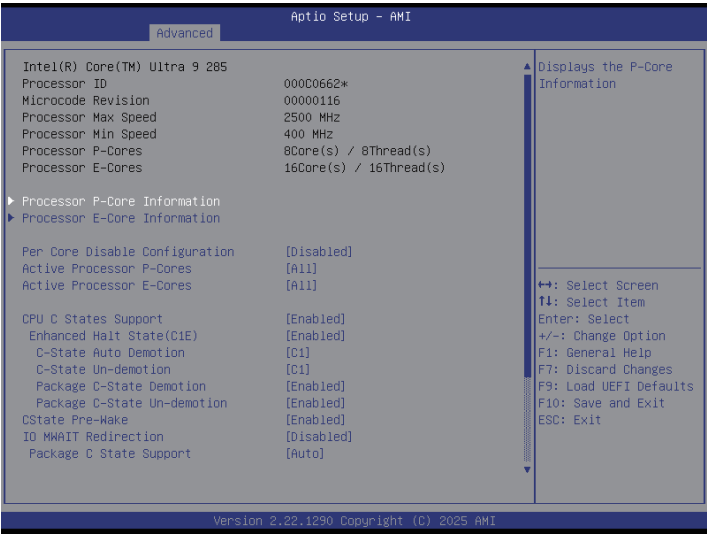
Configure CPU CORE OTP Temperature.

3.4 Advanced Screen

In this section, set the configurations for the following items: CPU Configuration, Memory Configuration, Chipset Configuration, PCH-FW Configuration, Storage Configuration, NVMe Configuration, VMD Configuration, ACPI Configuration, USB Configuration, Super IO Configuration, Serial Port Console Redirection, H/W Monitor, Trusted Computing, Intel ME Configuration, Network Stack Configuration, Driver Health and Tls Auth Configuration, Intel(R) Platform Service Record, Intel(R) Rapid Storage Technology, Intel(R) Network Connection and Insant Flash.



3.4.1 CPU Configuration



Processor P-Core Information

This item displays the P-Core Information.

Processor E-Core Information

This item displays the E-Core Information.

Pre Core Disable Configuration

Select this item to enable or disable Per Core Disable. When Per Core Disable Configuration is enabled, the Active Cores and Active Efficient-cores options will be disabled.

Active Processor P-Cores

Select the number of cores to enable in each processor package.

Active Processor E-Cores

Select the number of E-Cores to enable in each processor package.

CPU C States Support

Enable CPU C States Support for power saving. It is recommended to keep C6 and C7 enabled for better power saving.

Enhanced Halt State (C1E)

Enable Enhanced Halt State (C1E) for lower power consumption.

C-State Auto Demotion

Configure C-State Auto Demotion.

C-State Un-demotion

Configure C-State Un-demotion.

Package C-State Demotion

Select this item to enable or disable Package C-State Demotion.

Package C-State Un-Demotion

Select this item to enable or disable Package C-State Un-demotion.

CState Pre-Wake

Disable - to 1 to disable the Cstate Pre-Wake.

IO MWAIT Redirection

Set this to map IO_read instructions set to IO registers PMG_IO_BASE_ADDRBASE+offset to MWAIT (offset).

Package C State Support

Enable CPU, PCIe, Memory, Graphics C State Support for power saving.

DC6 Latency WA

Select this to enable or disable DC6 Latency WA.

CPU Thermal Throttling

Enable CPU internal thermal control mechanisms to keep the CPU from overheating.

Intel AVX/AVX2

Enable/Disable the Intel AVX and AVX2 Instructions. This is applicable for Big Core only.

Intel Virtualization Technology

Intel Virtualization Technology allows a platform to run multiple operating systems and applications in independent partitions, so that one computer system can function as multiple virtual systems.

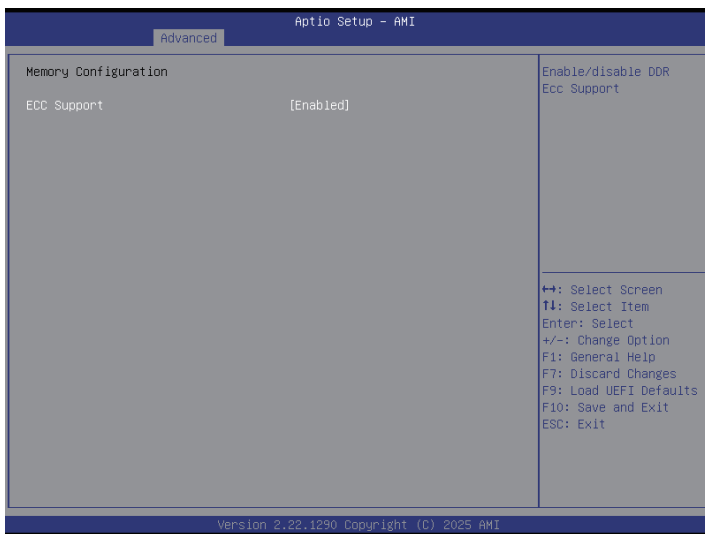
X2APIC Enable

Select this to enable or disable X2APIC Operating Mode. When this option is configured as 'Enabled', 'VT-d' option must be 'Enabled' and 'X2APIC Opt Out' option must be 'Disabled' as well.

Legacy Game Compatibility Mode

When enabled, pressing the scroll lock key will toggle the Efficient cores between being parked when Scroll Lock LED is on and un-parked when LED is off.

3.4.2 Memory Configuration



ECC Support

Use this item to enable or disable DDR ECC Support.

3.4.3 Chipset Configuration



SR-IOV Support

If system has SR-IOV capable PCIe Devices, this option Enables or Disables Single Root IO Virtualization Support. Only for PCH PCIe Devices.

Primary Graphics Adapter

If PCI Express graphics card is installed on the motherboard, use this option to select PCI Express or Onboard VGA as the primary graphics adapter.

**If no PCI Express graphics card is installed, [Onboard VGA] is the default graphics adapter. There is no screen on monitor even if a HDMI display is connected. Select [Onboard Hdmi] instead to use HDMI as output source.*

Intel IGFX

Select disable to disable the integrated graphics when an external graphics card is installed. Or select enable to keep the integrated graphics enabled at all times.

Re-Sized BAR Support

If system has Resizable BAR capable PCIe Devices, this option Enables or Disables Resizable BAR Support (Only if System Supports 64 bit PCI Decoding).

Onboard VGA (D-Sub)

Use this item to enable or disable the Onboard VGA function.

Onboard LAN

Use this to enable or disable the Onboard LAN function.

Onboard HDMI HD Audio

Enable audio for the onboard digital outputs.

VT-d

Intel® Virtualization Technology for Directed I/O helps the virtual machine monitor better utilize hardware by improving application compatibility and reliability, and providing additional levels of manageability, security, isolation, and I/O performance.

NPU Device (B0:D11:F0)

Use this to enable or disable NPU (Neural Processing Unit) Device.

OCU Mode Selection

Configure the OCU modes.

OCU1/OCU2 Mode Selection

Switch the OCU1/OCU2 link to PCIE or SATA.

PCIE Link Width

Select this item to configure PCIE Link Width.

PCIE6/MCIO1 Link Width

Use this item to select PCIE6/MCIO1 Link Width including x16, x8x8, x8x4x4 options.

PCIE Link Speed

Select this item to configure PCIE Link Speed.

PCIE4/PCIE6 Link Speed

Select Link Speed for PCIE4/PCIE6, the default value is [Auto]. Auto mode is optimizing for overclocking

M2_1/M2_2 Link Speed

Select Link Speed for M.2, the default value is [Auto]. Auto mode is optimizing for overclocking

MCIO2_1/MCIO2_2 Link Speed

Select Link Speed for MCIO, the default value is [Auto]. Auto mode is optimizing for overclocking

PCIE Hot Plug

Select this item to configure PCIE Hot Plug globally.

PCIE4/PCIE6 Hot Plug

Enable or disable PCIE Hot Plug.

MCIO2-1/MCIO2-2 Hot Plug

Enable or disable MCIO Hot Plug.

PCIE ASPM

Select this item to configure the PCIE ASPM.

PCI-E ASPM Support (Global)

Select this item to disable ASPM Support in all PCIe root ports.

PCIE4/PCIE6 ASPM Support

Enables or disables the ASPM support for PCIE.

M2_1/M2_2 ASPM Support

Enables or disables the ASPM support for M.2.

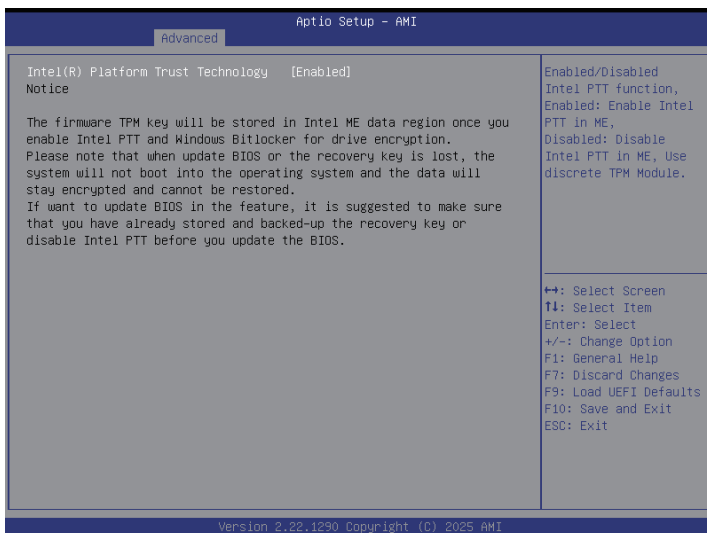
MCIO2_1/MCIO2_2 ASPM Support

Enables or disables the ASPM support for MCIO.

Onboard Debug Port LED

Enable or disable the onboard Dr. Debug LED.

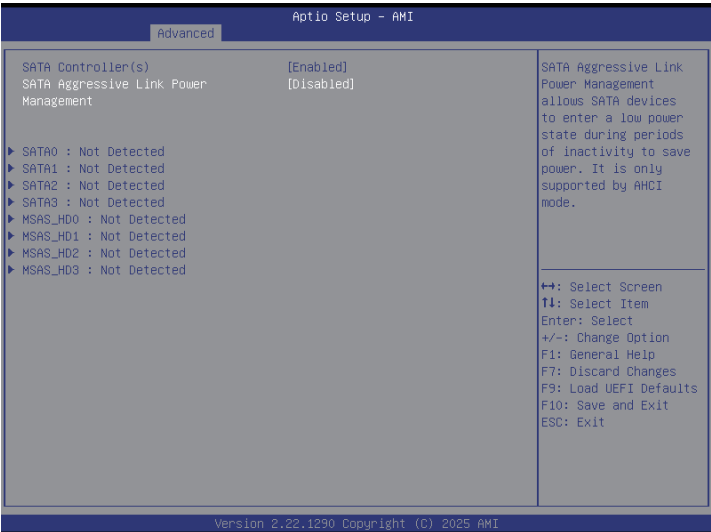
3.4.4 PCH-FW Configuration



Intel(R) Platform Trust Technology

Use this item to configure Intel PTT function. Select Enabled to use Intel PTT in ME. Disable this option to use discrete TPM Module.

3.4.5 Storage Configuration



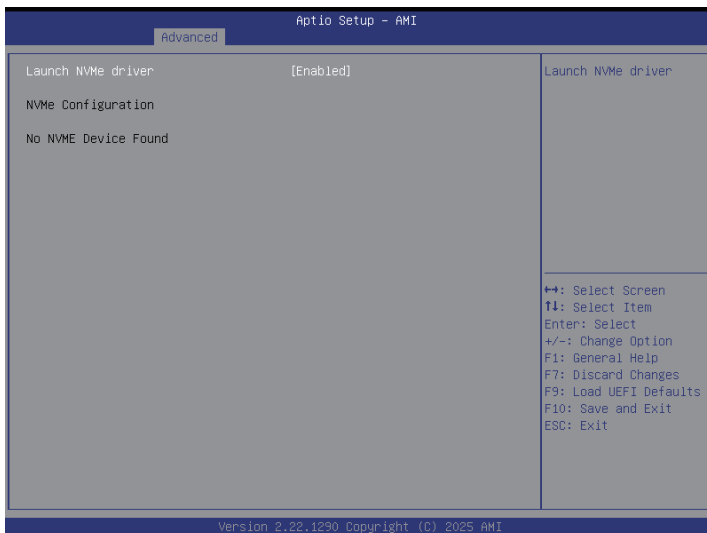
SATA Controller(s)

Enable/disable the SATA controllers.

SATA Aggressive Link Power Management

SATA Aggressive Link Power Management allows SATA devices to enter a low power state during periods of inactivity to save power. It is only supported by AHCI mode.

3.4.6 NVMe Configuration



The NVMe Configuration displays the NVMe controller and Drive information.

Launch NVMe driver

Select this item to enable or disable launch NVMe driver.

3.4.7 VMD Configuration



Enable VMD Controller

Use this item to enable or disable VMD Controller. When enabled, the options below appear.

Enable VMD Global Mapping

Use this item to enable or disable VMD Global Mapping.

Map this Root Port under VMD

Use this item to map or unmap Root Port to VMD.

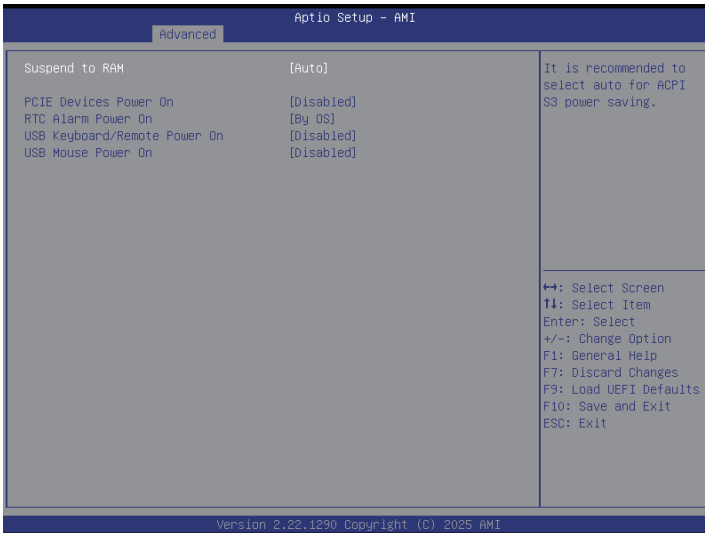
RAID0/1/5/10

Use this item to enable or disable RAID0/1/5/10 support.

ZPODD

Use this item to enable or disable ZPODD. The option is only needed to be enabled when ZPODD is connected in VMD mode.

3.4.8 ACPI Configuration



Suspend to RAM

Select disable for ACPI suspend type S1. It is recommended to select auto for ACPI S3 power saving.

PCIE Devices Power On

Allow the system to be waked up by a PCIE device and enable wake on LAN.

RTC Alarm Power On

Allow the system to be waked up by the real time clock alarm. Set it to By OS to let it be handled by the operating system.

RTC Alarm Date

Use this item to set Date of RTC power on feature.

RTC Alarm Hour

Use this item to set Hour of RTC power on feature.

RTC Alarm Minute

Use this item to set Minute of RTC power on feature.

RTC Alarm Second

Use this item to set Second of RTC power on feature.

USB Keyboard/Remote Power On

Allow the system to be waked up by an USB keyboard or remote controller.

USB Mouse Power On

Allow the system to be waked up by an USB mouse.

3.4.9 USB Configuration

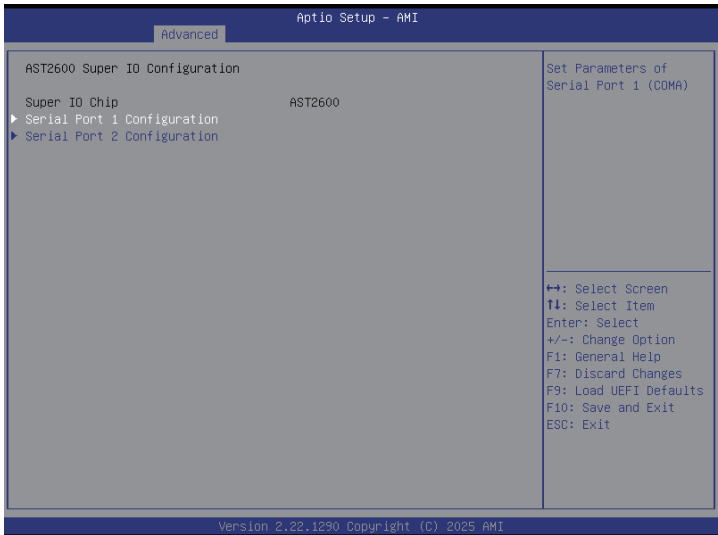


This page displays the information of the USB controllers and USB devices.

XHCI Hand-off

This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.

3.4.10 Super IO Configuration



Serial Port 1 Configuration / Serial Port 2 Configuration

Use this item to set parameters of Serial Port1 (COMA).

Serial Port

Use this item to enable or disable the serial port (COM).

Change Settings

Use this item to select an optimal setting for Super IO device.

Serial Port2 Configuration

Use this item to set parameters of Serial Port2 (COMB).

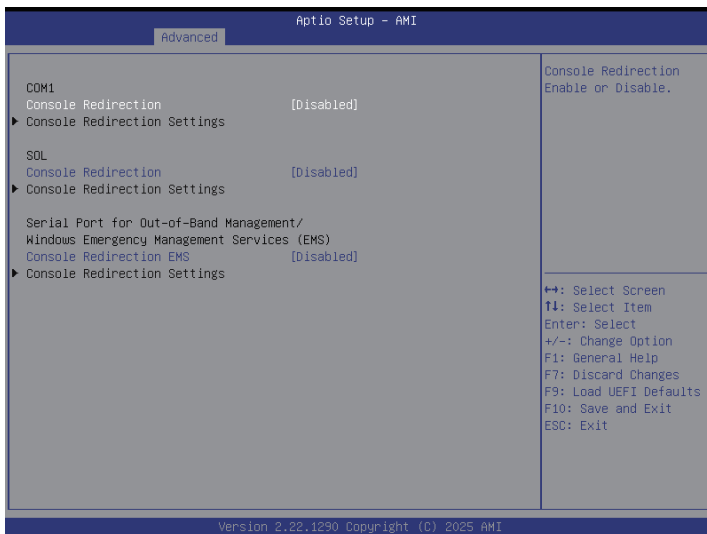
Serial Port

Use this item to enable or disable the SOL port.

Change Settings

Use this item to select an optimal setting for Super IO device.

3.4.11 Serial Port Console Redirection



COM1 / SOL

Console Redirection

Use this option to enable or disable Console Redirection. If this item is set to Enabled, select a COM Port to be used for Console Redirection.

Console Redirection Settings

Use this option to configure Console Redirection Settings, and specify how the computer and the host computer to exchange information.

Terminal Type

Use this item to select the preferred terminal emulation type for out-of-band management. It is recommended to select [VT-UTF8].

Option	Description
VT100	ASCII character set
VT100+	Extended VT100 that supports color and function keys
VT-UTF8	UTF8 encoding is used to map Unicode chars onto 1 or more bytes
ANSI	Extended ASCII character set

Bits Per Second

Use this item to select the serial port transmission speed. The speed used in the host computer and the client computer must be the same. Long or noisy lines may require lower transmission speed. The options include [9600], [19200], [38400], [57600] and [115200].

Data Bits

Use this item to set the data transmission size. The options include [7] and [8] (Bits).

Parity

Use this item to select the parity bit. The options include [None], [Even], [Odd], [Mark] and [Space]. A parity bit can be sent with the data bits to detect some transmission errors. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.

Even: parity bit is 0 if the num of 1's in the data bits is even.

Odd: parity bit is 0 if num of 1's in the data bits is odd.

Mark: parity bit is always 1.

Space: Parity bit is always 0.

Stop Bits

The item indicates the end of a serial data packet. The standard setting is [1] Stop Bit. Select [2] Stop Bits for slower devices.

Flow Control

Use this item to set the flow control to prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to restart the flow. Hardware flow uses two wires to send start/stop signals. The options include [None] and [Hardware RTS/CTS].

VT-UTF8 Combo Key Support

Use this item to enable or disable the VT-UTF8 Combo Key Support for ANSI/VT100 terminals.

Recorder Mode

Use this item to enable or disable Recorder Mode to capture terminal data and send it as text messages.

Resolution 100x31

Use this item to enable or disable extended terminal resolution support.

Putty Keypad

Use this item to select Function Key and Keypad on Putty.

Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

Console Redirection EMS

Use this option to enable or disable Console Redirection. If this item is set to Enabled, select a COM Port to be used for Console Redirection.

Console Redirection Settings

Use this option to configure Console Redirection Settings, and specify how the computer and the host computer to exchange information.

Out-of-Band Mgmt Port

Microsoft Windows Emergency Management Services (EMS) allows for remote management of a Windows Server OS through a serial port.

Terminal Type EMS

Use this item to select the preferred terminal emulation type for out-of-band management. It is recommended to select [VT-UTF8].

Option	Description
VT100	ASCII character set
VT100+	Extended VT100 that supports color and function keys
VT-UTF8	UTF8 encoding is used to map Unicode chars onto 1 or more bytes
ANSI	Extended ASCII character set

Bits Per Second EMS

Use this item to select the serial port transmission speed. The speed used in the host computer and the client computer must be the same. Long or noisy lines may require lower transmission speed. The options include [9600], [19200], [57600] and [115200].

Flow Control EMS

Use this item to set the flow control to prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to restart the flow. Hardware flow uses two wires to send start/stop signals. The options include [None], [Hardware RTS/CTS], and [Software Xon/Xoff].

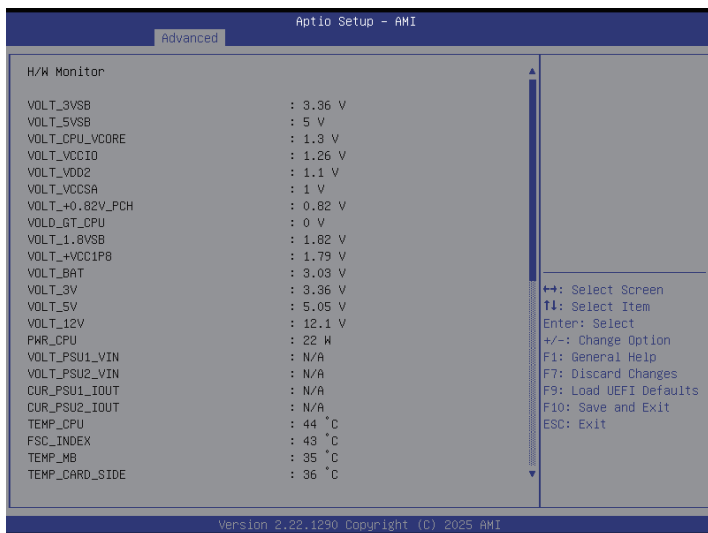
Data Bits EMS

Parity EMS

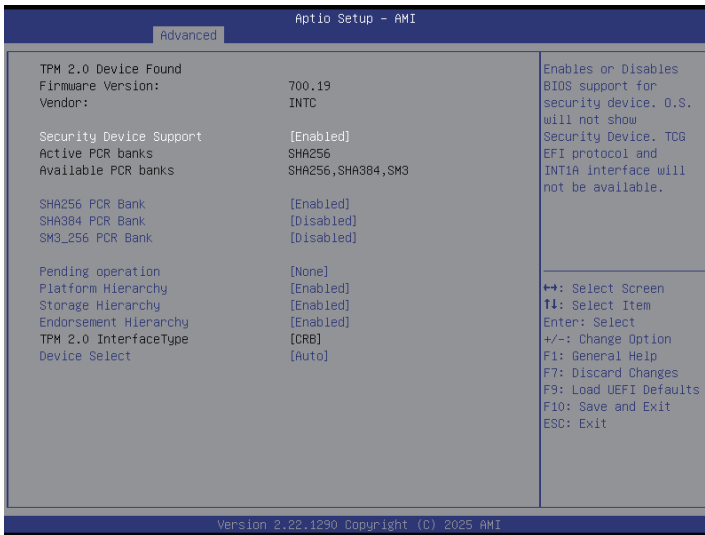
Stop Bits EMS

3.4.12 H/W Monitor

In this section, it allows user to monitor the status of the hardware on the system, including the parameters of the CPU temperature, motherboard temperature, CPU fan speed, chassis fan speed, and the critical voltage.



3.4.13 Trusted Computing



NOTE: Options vary depending on the version of the connected TPM module.

Security Device Support

Enable to activate Trusted Platform Module (TPM) security for the hard diskdrives.

Active PCR banks

This item displays active PCR Banks.

Available PCR Banks

This item displays available PCR Banks.

SHA256 PCR Bank

Use this item to enable or disable SHA256 PCR Bank

SHA384 PCR Bank

Use this item to enable or disable SHA384 PCR Bank.

SM3_256 PCR Bank

Use this item to enable or disable SM3_256 PCR Bank.

Pending Operation

Schedule an Operation for the Security Device.

NOTE: The computer will reboot during restart in order to change State of the Device.

Platform Hierarchy

Use this item to enable or disable Platform Hierarchy.

Storage Hierarchy

Use this item to enable or disable Storage Hierarchy.

Endorsement Hierarchy

Use this item to enable or disable Endorsement Hierarchy.

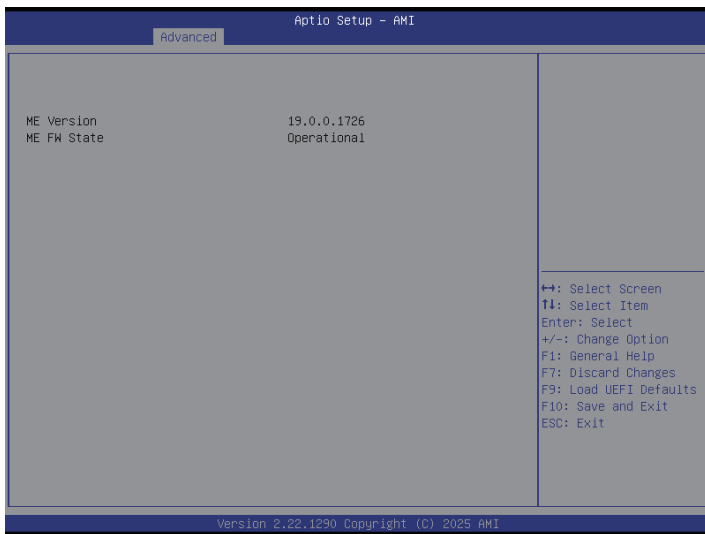
TPM 2.0 InterfaceType (CRB)

Select the Communication Interface to TPM 2.0 Device

Device Select

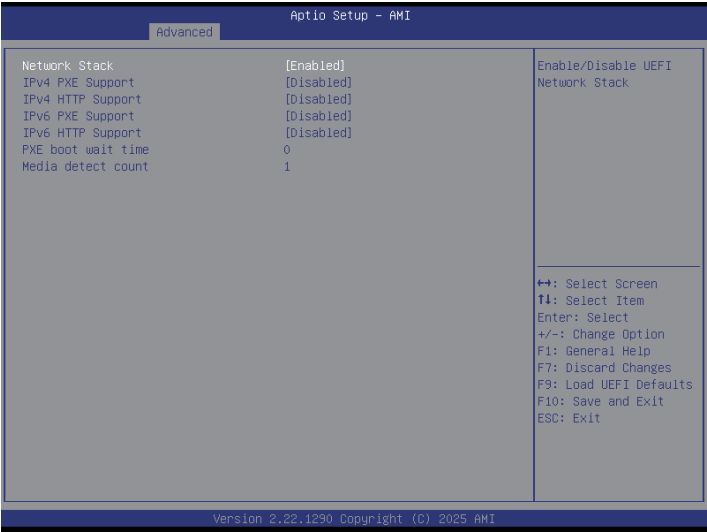
Use this item to select the TPM device to be supported. TPM 1.2 will restrict support to TPM 1.2 devices. TPM 2.0 will restrict support to TPM 2.0 devices. Auto will support both with the default set to TPM 2.0 devices. If TPM 2.0 devices are not found, TPM 1.2 devices will be enumerated.

3.4.14 Intel ME Configuration



ME Subsystem screen displays the Intel ME Subsystem Configuration information, such as ME Version and ME FW State.

3.4.15 Network Stack Configuration



Network Stack

Use this item to enable or disable UEFI Network Stack.

Ipv4 PXE Support

Use this item to enable or disable IPv4 PXE boot support. If disabled, IPv4 PXE boot support will not be available.

Ipv4 HTTP Support

Use this item to enable or disable IPv4 HTTP boot support. If disabled, IPv4 HTTP boot support will not be available.

Ipv6 PXE Support

Use this item to enable or disable IPv6 PXE boot support. If disabled, IPv6 PXE boot support will not be available.

Ipv6 HTTP Support

Use this item to enable or disable IPv6 HTTP boot support. If disabled, IPv6 HTTP boot support will not be available.

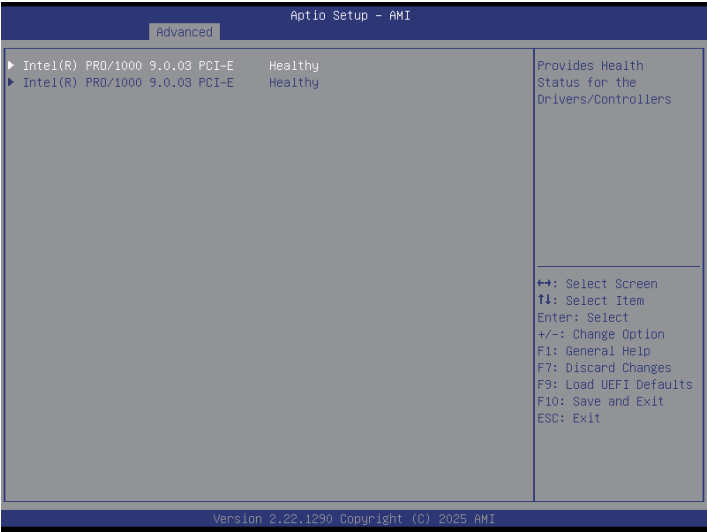
PXE Boot Wait Time

Specifies the wait time and press the ESC key to abort the PXE boot.

Media Detect Count

Specifies the number of times the presence of physical storage device are verified on a system reset or power cycle.

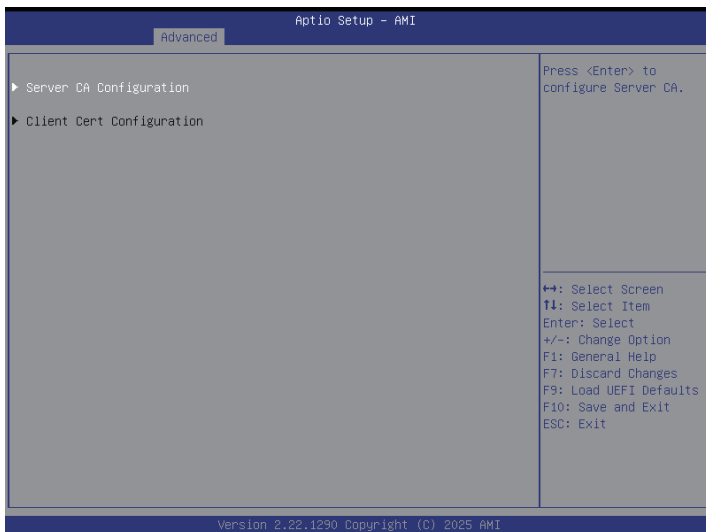
3.4.16 Driver Health



This page provides health status for the drivers/controllers.

Note: The screenshot here is for references only. The items on this page vary depending on models and devices used.

3.4.17 Tls Auth Configuration



Server CA Configuration

Press <Enter> to configure Server CA.

Client Cert Configuration

Enroll Cert

Press <Enter> to enroll cert.

Delete Cert

Press <Enter> to delete cert.

3.4.18 Intel(R) Platform Service Record

Advanced

Aptio Setup - AMI

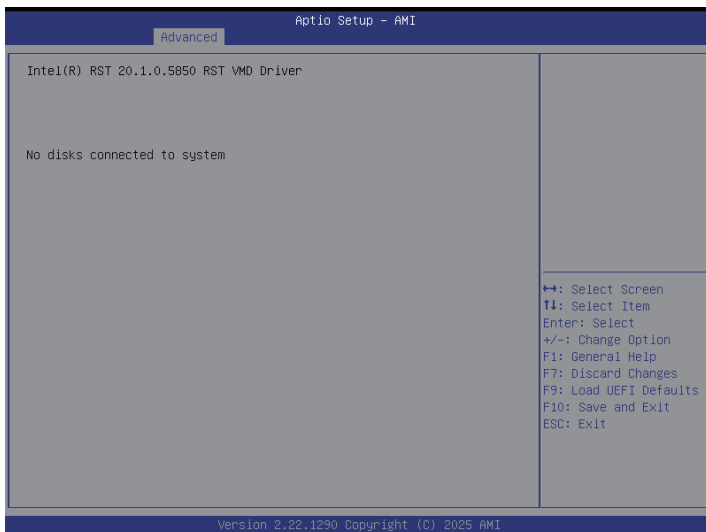
Intel(R) Unique Platform ID	0000000000000000-0000000000000000 0000000000000000-0000000000000000 0000000000000000-0000000000000000 0000000000000000-0000000000000000
Platform Service Record ID	00000000-0000-0000-0000-00000000 000
PSR Version	00.00
PSR State	Not Started
PSR Capabilities	
Chassis Intrusion	Not Supported
Excessive Operational Temperature	Not Supported
Excessive Shock	Not Supported
Remote Platform Erase	Not Supported
Local Platform Erase	Not Supported
CSME Firmware Version	0. 0. 0. 0
Genesis	
Log Start Date	1970/01/01 00:00:00
OEM Name	
OEM Make	

↑↓: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Option
F1: General Help
F7: Discard Changes
F9: Load UEFI Defaults
F10: Save and Exit
ESC: Exit

Version 2.22.1290 Copyright (C) 2025 AMI

This formset contains forms for displaying platform service record Zero values means either not started or failed to receive PSR data from CSME.

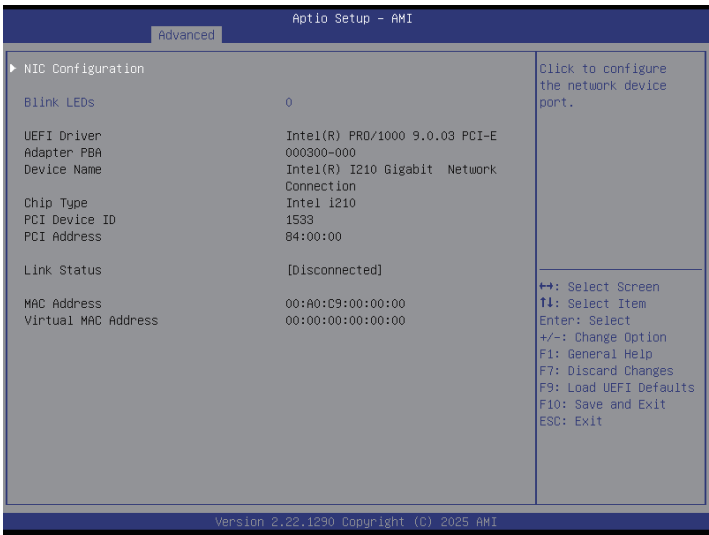
3.4.19 Intel(R) Rapid Storage Technology



This formset allows the user to manage RAID volumes on the Intel(R) RAID Controller.

3.4.20 Intel(R) Network Connection

Configure Gigabit Ethernet device parameters.



NIC Configuration

Click this item to configure the network device port.

Link Speed

Specifies the port speed used for the selected boot protocol.

Wake On LAN

Use this item to enable power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states.

Blink LEDs

Identify the physical network port by blinking the associated LED.

Port Configuration Information

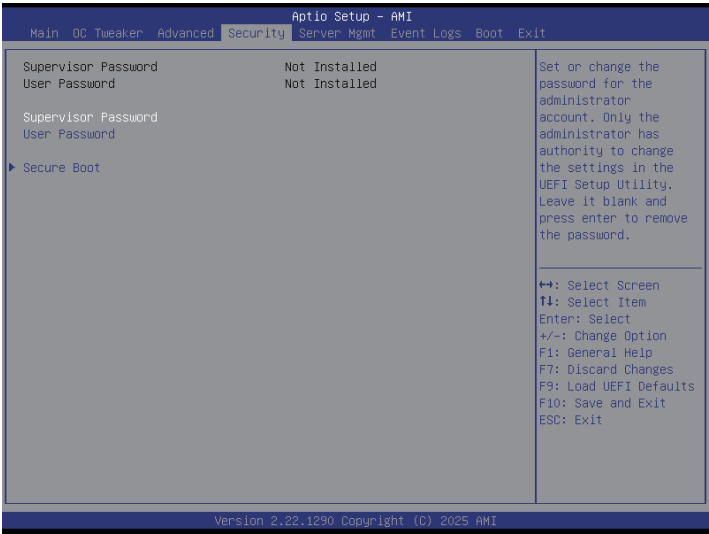
Displays port configuration information including UEFI Driver, Adapter PBA, Device Name, Chip Type, PCI Device ID, PCI Address, Link Status, MAC Address and Virtual MAC Address.

3.4.21 Instant Flash

Instant Flash is a UEFI flash utility embedded in Flash ROM. This convenient UEFI update tool allows user to update system UEFI without entering operating systems first like MS-DOS or Windows. Just save the new UEFI file to the USB flash drive, floppy disk or hard drive and launch this tool, then update the UEFI only in a few clicks without preparing an additional floppy diskette or other complicated flash utility. Please be noted that the USB flash drive or hard drive must use FAT32/16/12 file system. If executing Instant Flash utility, the utility will show the UEFI files and their respective information. Select the proper UEFI file to update the UEFI, and reboot the system after the UEFI update process is completed.

3.5 Security Screen

In this section, set or change the supervisor/user password for the system. It also allows user to clear the user password.



Supervisor Password

Set or change the password for the administrator account. Only the administrator has authority to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

User Password

Set or change the password for the user account. Users are unable to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

Secure Boot

Use this item to enable or disable support for Secure Boot.

Secure Boot Mode

Enable to support Windows 8 or later versions Secure Boot.

3.5.1 Expert Key Management

In this section, expert users can modify Secure Boot Policy variables without full authentication.



Factory Key Provision

Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode.

Install Default Secure Boot Keys

Please install default secure boot keys if it's the first time to use secure boot.

Clear Secure Boot keys

Force System to Setup Mode - clear all Secure Boot Variables. Change takes effect after re-boot.

Enroll Efi Image

Allow the image to run in Secure Boot mode. Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db).

Export Secure Boot variables

Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device.

Platform Key (PK)

Enroll Factory Defaults or load certificates from a file:

1. Public Key Certificate:

- a) EFI_SIGNATURE_LIST
- b) EFI_CERT_X509 (DER)
- c) EFI_CERT_RSA2048 (bin)
- d) EFI_CERT_SHAXXX

2. Authenticated UEFI Variable

3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

Key Exchange Keys (KEK)

Enroll Factory Defaults or load certificates from a file:

1. Public Key Certificate:

- a) EFI_SIGNATURE_LIST
- b) EFI_CERT_X509 (DER)
- c) EFI_CERT_RSA2048 (bin)
- d) EFI_CERT_SHAXXX

2. Authenticated UEFI Variable

3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

Authorized Signatures (db)

Enroll Factory Defaults or load certificates from a file:

1. Public Key Certificate:

- a) EFI_SIGNATURE_LIST
- b) EFI_CERT_X509 (DER)
- c) EFI_CERT_RSA2048 (bin)
- d) EFI_CERT_SHAXXX

2. Authenticated UEFI Variable

3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

Forbidden Signatures (dbx)

Enroll Factory Defaults or load certificates from a file:

1. Public Key Certificate:

- a) EFI_SIGNATURE_LIST
- b) EFI_CERT_X509 (DER)
- c) EFI_CERT_RSA2048 (bin)
- d) EFI_CERT_SHAXXX

2. Authenticated UEFI Variable

3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

Authorized TimeStamps (dbt)

Enroll Factory Defaults or load certificates from a file:

1. Public Key Certificate:

- a) EFI_SIGNATURE_LIST
- b) EFI_CERT_X509 (DER)
- c) EFI_CERT_RSA2048 (bin)
- d) EFI_CERT_SHAXXX

2. Authenticated UEFI Variable

3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

OsRecovery Signatures (dbr)

Enroll Factory Defaults or load certificates from a file:

1. Public Key Certificate:

- a) EFI_SIGNATURE_LIST
- b) EFI_CERT_X509 (DER)
- c) EFI_CERT_RSA2048 (bin)

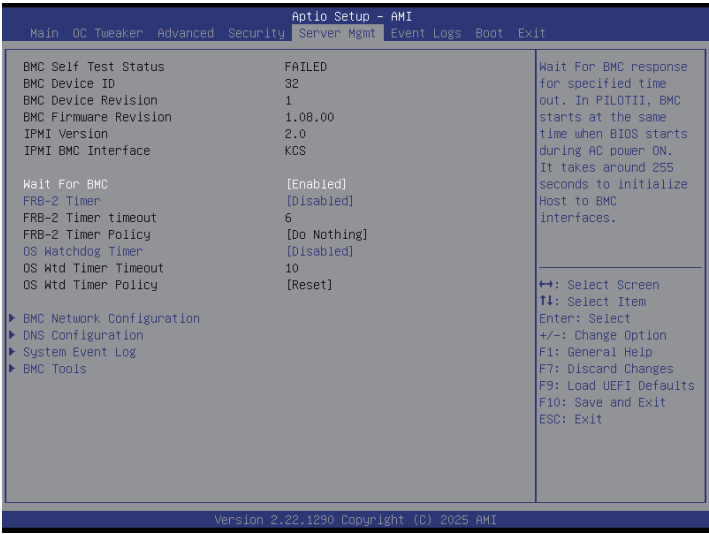
d) EFI_CERT_SHAXXX

2. Authenticated UEFI Variable

3. EFI PE/COFF Image(SHA256)

Key Source: Factory, Modified, Mixed

3.6 Server Mgmt



Wait For BMC

Wait For BMC response for specified time out. In PILOTII, BMC starts at the same time when BIOS starts during AC power ON. It takes around 90 seconds to initialize Host to BMC interfaces.

FRB-2 Timer

Use this item to enable or disable FRB-2 timer (POST timer).

FRB-2 Timer Timeout

Enter value between 1 to 30 min for FRB-2 Timer Expiration.

FRB-2 Timer Policy

Use this item to configure how the system should respond if the FRB-2 Timer expires. Not available if FRB-2 Timer is disabled.

OS Watchdog Timer

If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads. Helps determine that the OS successfully loaded or follows the OS Boot Watchdog Timer policy.

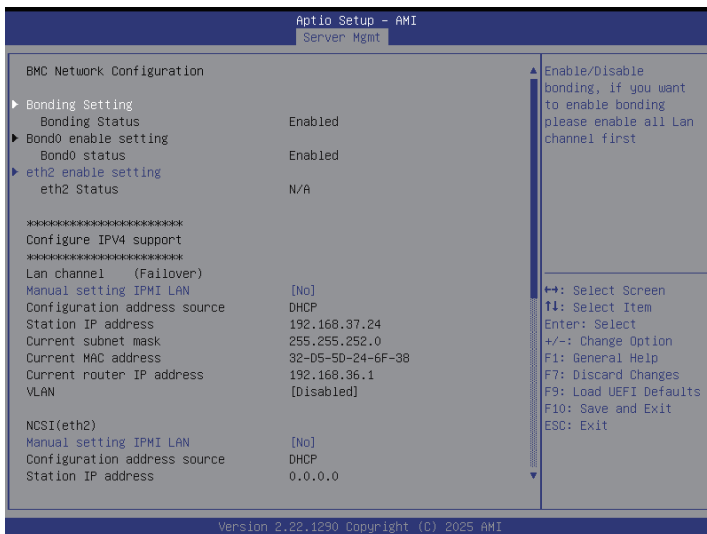
OS Wtd Timer Timeout

Enter the value between 1 to 30 min for OS Boot Watchdog Timer Expiration. This item is not available if OS Boot Watchdog Timer is disabled.

OS Wtd Timer Policy

Configure how the system should respond if the OS Boot Watchdog Timer expires. This item is not available if OS Boot Watchdog Timer is disabled.

3.6.1 BMC Network Configuration



Bonding Setting

Use this item to enable or disable bonding. Please enable all Lan channel first before enabling bonding.

Bond0 Enable Setting

Show the Bond0 status is enabled or disabled.

Lan Channel (Failover)

Manual Setting IPMI LAN

If [No] is selected, the IP address is assigned by DHCP. If using a static IP address, toggle to [Yes], and the changes take effect after the system reboots. The default value is [No].

Configuration Address Source

Select to configure BMC network parameters statically or dynamically (by BIOS or BMC). Configuration options: [Static] and [DHCP].



When [DHCP] or [Static] is selected, do NOT modify the BMC network settings on the IPMI web page.

Static: Manually enter the IP Address, Subnet Mask and Gateway Address in the BIOS for BMC LAN channel configuration.

DHCP: IP address, Subnet Mask and Gateway Address are automatically assigned by the network's DHCP server.



The default login information for the IPMI web interface is:

Username: admin

Password: admin

For more instructions on how to set up remote control environment and use the IPMI management platform, please refer to the IPMI Configuration User Guide or go to the Support website at: <http://www.asrockrack.com/support/faq.asp>

VLAN

Enabled/Disabled Virtual Local Area Network.

If [Enabled] is selected, configure the items below.

VLAN ID: Select this item to configure the VLAN ID setting, the Maximum value is 4094 and the Minimum value is 1.

VLAN Priority: Select this item to configure the VLAN Priority setting, the Maximum value is 7 and the Minimum value is 0.

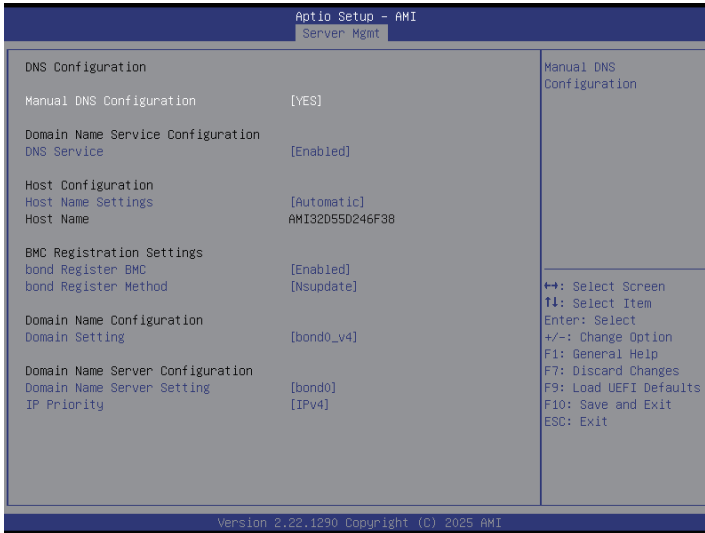
IPv6 Support

Enabled/Disable LAN1 IPv6 Support.

Manual Setting IPMI LAN(IPV6)

Select to configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase.

3.6.2 DNS Configuration



Manual DNS Configuration

Select this item to manual configure DNS.

If [YES] is selected, configure the items below.

DNS Service

Use this item to enable or disable DNS Service Configuration.

Host Name Settings

Use this item to automatic or manual Host Name Settings.

Bond Register BMC

Use this item to enable or disable Bond Register BMC.

Bond Register Method

Use this item to configure Bond Register Method with Nupdate or DHCP client FQDN/Hostname..

Domain Setting

This item supports Manual, Bond0_v4 and Bond0_v6 Domain Settings.

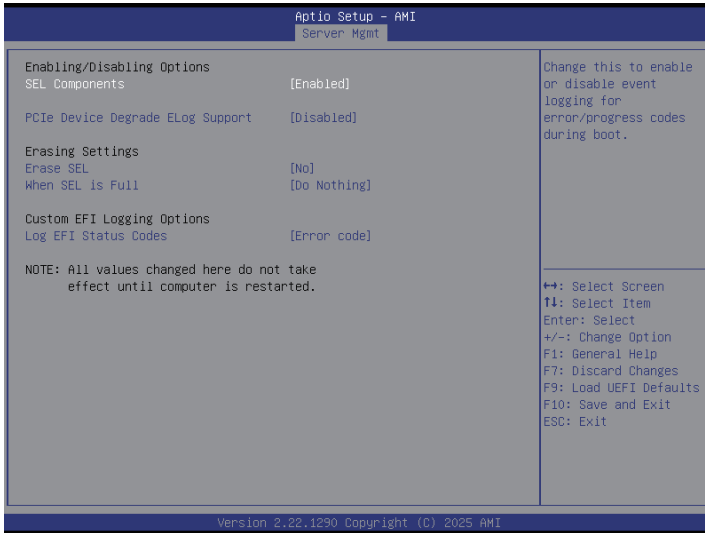
Domain Name Server Setting

This item supports Manual and Bond0 DNS Server Settings.

IP Priority

This item supports IPV4 and IPV6 IP Priority.

3.6.3 System Event Log



SEL Components

Change this to enable or disable all features of System Event Logging during boot.

PCIe Device Degrade ELog Support

Use this item to enable or disable PCIe Device Degrade Error Logging Support.

Erase SEL

Use this to choose options for erasing SEL.

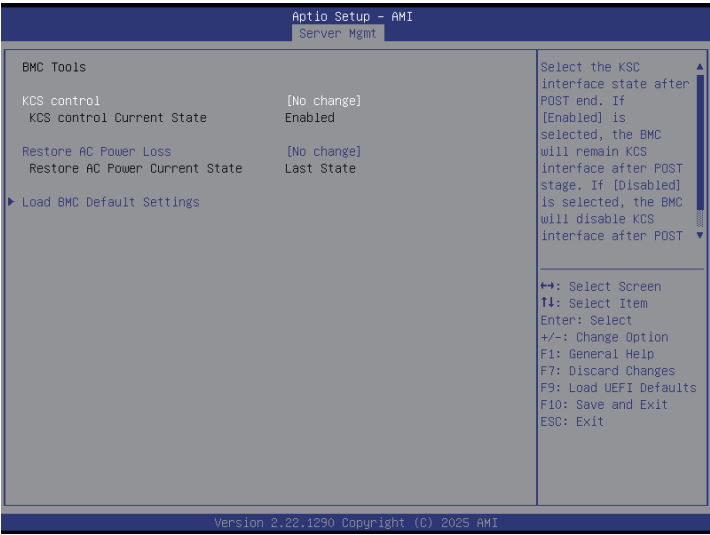
When SEL is Full

Use this to choose options for reactions to a full SEL.

Log EFI Status Codes

Use this item to disable the logging of EFI Status Codes or log only error code or only progress or both.

3.6.4 BMC Tools



KCS Control

Select this KCS interface state after POST end. If [Enabled] is selected, the BMC will remain KCS interface after POST stage. If [Disabled] is selected, the BMC will disable KCS interface after POST stage

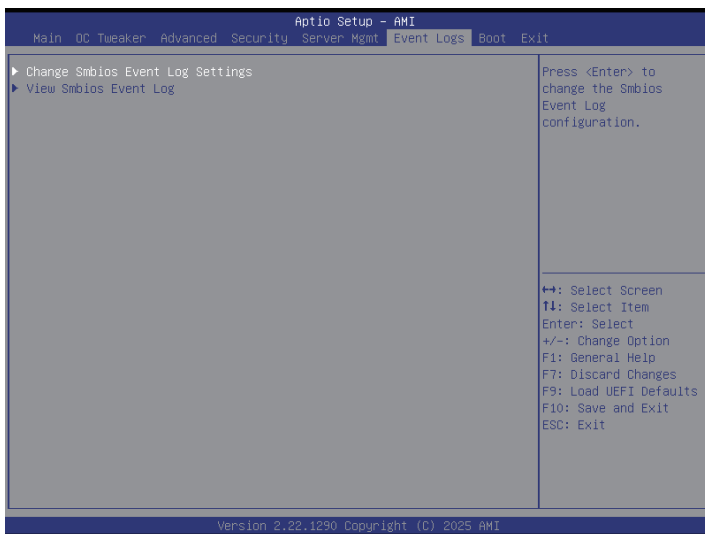
Restore AC Power Loss

This allows user to set the power state after an unexpected AC/power loss. If [Power Off] is selected, the AC/power remains off when the power recovers. If [Power On] is selected, the AC/power resumes and the system starts to boot up when the power recovers. If [Last State] is selected, it will recover to the state before AC/power loss.

Load BMC Default Settings

Use this item to Load BMC Default Settings

3.7 Event Logs



Change Smbios Event Log Settings

This allows user to configure the Smbios Event Log Settings.

When entering the item, the sub-item will displayed as below:

Smbios Event Log

Use this item to enable or disable all features of the SMBIOS Event Logging during system boot.

Erase Event Log

The options include [No], [Yes, Next reset] and [Yes, Every reset]. If Yes is selected, all logged events will be erased.

When Log is Full

Use this item to choose options for reactions to a full Smbios Event Log. The options include [Do Nothing] and [Erase Immediately].

Log System Boot Event

Choose option to enable/disable logging of System boot event.

View Smbios Event Log

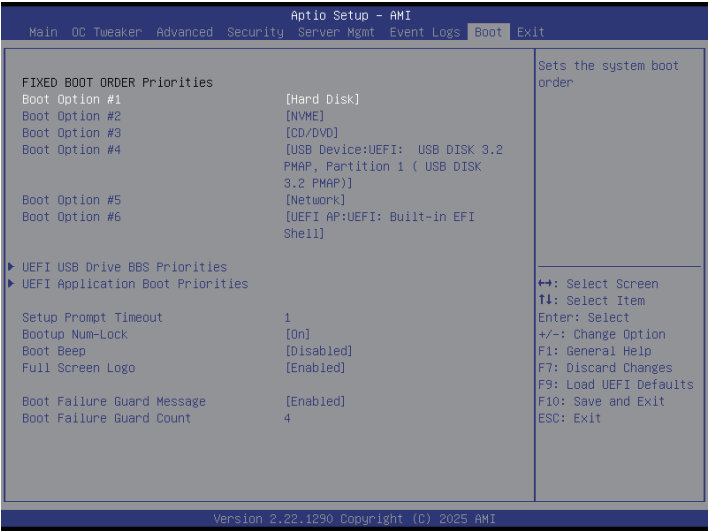
Press <Enter> to view the Smbios Event Log records.



All values changed here do not take effect until computer is restarted.

3.8 Boot Screen

In this section, it will display the available devices on the system for user to configure the boot settings and the boot priority.



Boot Option #1~#6

Use this item to set the system boot order.

UEFI USB Drive BBS Priorities

Specifies the Boot Device Priority sequence from available UEFI USB Drives.

UEFI Application Boot Priorities

Specifies the Boot Device Priority sequence from available UEFI Application.

Setup Prompt Timeout

Configure the number of seconds to wait for the UEFI setup utility.

Bootup Num-Lock

If this item is set to [On], it will automatically activate the Numeric Lock function after boot-up.

Boot Beep

Select whether the Boot Beep should be turned on or off when the system boots up. Please note that a buzzer is needed.

Full Screen Logo

Use this item to enable or disable OEM Logo. The default value is [Enabled].

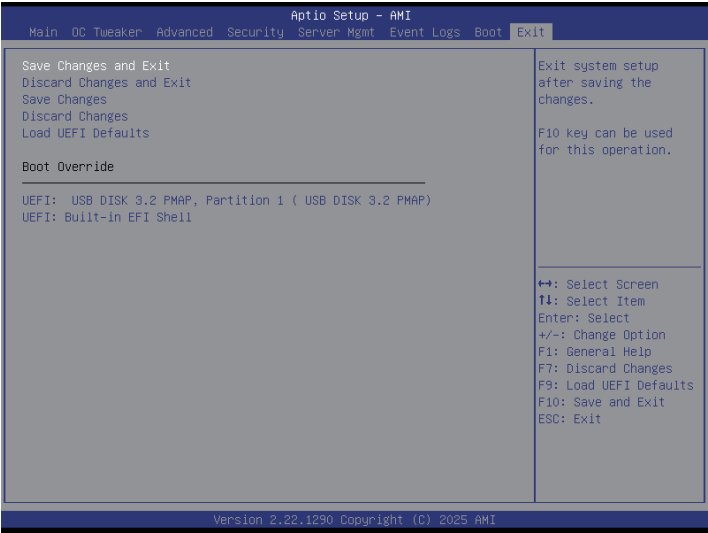
Boot Failure Guard Message

If the computer fails to boot for a number of times the system automatically restores the default settings.

Boot Failure Guard Count

Use this item to configure Boot Failure Guard Count.

3.9 Exit Screen



Save Changes and Exit

When selecting this option, the following message “Save configuration changes and exit setup?” will pop-out. Press <F10> key or select [Yes] to save the changes and exit the UEFI SETUP UTILITY.

Discard Changes and Exit

When selecting this option, the following message “Discard changes and exit setup?” will pop-out. Press <ESC> key or select [Yes] to exit the UEFI SETUP UTILITY without saving any changes.

Save Changes

When selecting this option, the following message “Save changes?” will pop-out. Select [Yes] to save all changes.

Discard Changes

When selecting this option, the following message “Discard changes?” will pop-out. Press <F7> key or select [Yes] to discard all changes.

Load UEFI Defaults

Load UEFI default values for all the setup questions. F9 key can be used for this operation.



All values changed here do not take effect until computer is restarted.

Chapter 4 Software Support

After all the hardware has been installed, it suggests to the official website at <http://www.ASRockRack.com> and make sure if there are any new updates of the BIOS / BMC firmware for the motherboard.

4.1 Download and Install Operating System

This motherboard supports various Microsoft® Windows® Server / Linux compliant operating systems. Please download the operating system from the OS manufacturer. Please refer to the OS documentation for more instructions.

**Please download the Intel® Rapid Storage Technology driver from the ASRock Rack's website (www.asrockrack.com) to the USB drive while installing OS in SATA RAID mode.*

4.2 Download and Install Software Drivers

This motherboard supports various Microsoft® Windows® compliant drivers. Please download the required drivers from the website at <http://www.ASRockRack.com>.

To download necessary drivers, go to the product page, click on the "Download" tab, choose the operating system, and select the driver to download.

Chapter 5 Troubleshooting

5.1 Troubleshooting Procedures

Follow the procedures below to troubleshoot the system.



Always unplug the power cord before adding, removing or changing any hardware components. Failure to do so may cause physical injuries and motherboard damages.

1. Disconnect the power cable and check whether the PWR LED is off.
2. Unplug all cables, connectors and remove all add-on cards from the motherboard.
Make sure that the jumpers are set to default settings.
3. Confirm that there are no short circuits between the motherboard and the chassis.
4. Install a CPU and fan on the motherboard, then connect the chassis speaker and power LED.

If there is no power...

1. Confirm that there are no short circuits between the motherboard and the chassis.
2. Make sure that the jumpers are set to default settings.
3. Check the settings of the 115V/230V switch on the power supply.
4. Verify if the battery on the motherboard provides ~3VDC. Install a new battery if it does not.

If there is no video...

1. Try replugging the monitor cables and power cord.
2. Check for memory errors.

If there are memory errors...

1. Verify that the DIMM modules are properly seated in the slots.
2. Use recommended DDR5 ECC/non-ECC UDIMM, CUDIMM.
3. If having installed more than one DIMM modules, they should be identical with the same brand, speed, size and chip-type.
4. Try inserting different DIMM modules into different slots to identify faulty ones.
5. Check the settings of the 115V/230V switch on the power supply.

Unable to save system setup configurations...

1. Verify if the battery on the motherboard provides ~3VDC. Install a new battery if it does not.
2. Confirm whether the power supply provides adequate and stable power.

Other problems...

1. Try searching keywords related to the problem on ASRock Rack's FAQ page:
<http://www.asrockrack.com/support>

5.2 Technical Support Procedures

If it has tried the troubleshooting procedures mentioned above and the problems are still unsolved, please contact ASRock Rack's technical support with the following information:

1. Contact information
2. Model name, BIOS version and problem type.
3. System configuration.
4. Problem description.

Contact ASRock Rack's technical support at:
<http://www.asrockrack.com/support/tsd.asp>

5.3 Returning Merchandise for Service

For warranty service, the receipt or a copy of the invoice marked with the date of purchase is required. By calling the vendor or going to the RMA website (<http://event.asrockrack.com/tsd.asp>) to obtain a Returned Merchandise Authorization (RMA) number.

The RMA number should be displayed on the outside of the shipping carton which is mailed prepaid or hand-carried when returning the motherboard to the manufacturer. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty does not cover damages incurred in shipping or from failure due to alteration, misuse, abuse or improper maintenance of products.

Contact the distributor first for any product related problems during the warranty period.

Contact Information

Contact ASRock Rack or want to know more about ASRock Rack, you're welcome to visit ASRock Rack's website at <http://www.asrockrack.com>; or contact the dealer for further information. For technical questions, please submit a support request form at <https://event.asrockrack.com/tsd.asp>

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