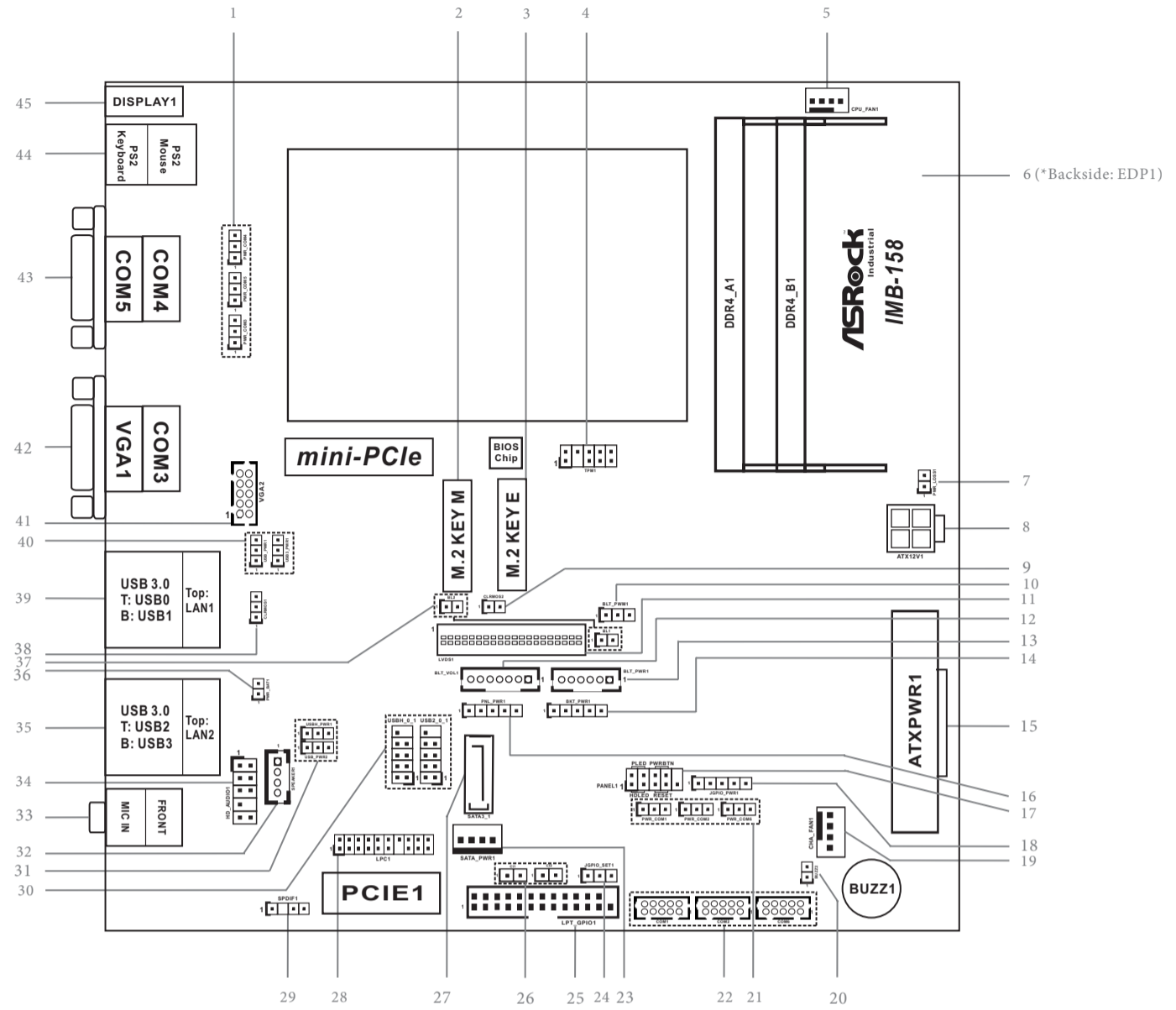


ASRock Jumpers and Headers Setting Guide

IMB-158



1 : COM Port Pin9 PWR Setting Jumpers
 PWR_COM3 ~ 5 (For COM Port3 ~ 5)
 1-2: +5V
 2-3: +12V

Pin#	Signal#	Signal#	Pin#
1#	GND#	+3.3V#	2#
3#	GND#	+3.3V#	4#
5#	NA#	NA#	6#
7#	NA#	NA#	8#
9#	GND#	NA	10#
11#	NA#	+3.3V#	12#
13#	NA#	+3.3V#	14#
15#	GND#	+3.3V#	16#
17#	NA#	+3.3V#	18#
19#	NA#	NA#	20#
21#	GND#	NA#	22#
23#	NA#	NA#	24#
25#	NA#	NA#	26#
27#	GND#	NA#	28#
29#	NA#	NA#	30#
31#	NA#	NA#	32#
33#	GND#	NA#	34#
35#	NA#	NA#	36#
37#	NA#	NA#	38#
39#	GND#	SMB_CLK#	40#
41#	SATA-B#	NA#	42#
43#	SATA-B#	NA#	44#
45#	GND#	NA#	46#
47#	SATA-A#	NA#	48#
49#	SATA-A#	PERST#	50#
51#	GND#	CLKREQ#	52#
53#	NA	WAKE#	54#
55#	NA	NA#	56#
57#	GND#	NA#	58#
59#	NA#	NA	60#
61#	GND	+3.3V#	70#
71#	GND#	+3.3V#	72#
73#	GND#	+3.3V#	74#
75#	GND#	#	#

2 : M.2 (Key-M)

6 : eDP Connector (BOM option with LVDS) (on the Backside of PCB)

PIN	Signal Name
40	NA
39	LCD_BLT_VCC
38	LCD_BLT_VCC
37	LCD_BLT_VCC
36	LCD_BLT_VCC
35	NA
34	NA
33	CON_LBKLT_CTL
32	CON_LBKLT_EN
31	GND
30	GND
29	GND
28	GND
27	eDP_HPD_CON
26	GND
25	GND
24	GND
23	GND
22	NA
21	LCD_VCC
20	LCD_VCC
19	LCD_VCC
18	LCD_VCC
17	GND
16	eDP_AUX#_CON
15	eDP_AUX#_CON
14	GND
13	eDP_TX0_CON
12	eDP_TX#0_CON
11	GND
10	eDP_TX1_CON
9	eDP_TX#1_CON
8	GND
7	eDP_TX2_CON
6	eDP_TX#2_CON
5	GND
4	eDP_TX3_CON
3	eDP_TX#3_CON
2	GND
1	NA

12 : Backlight Volume Control (BLT_VOL1)

PIN	Signal Name
1	GPIO_VOL_UP
2	GPIO_VOL_DW
3	PWRDN
4	LVDS1_BLUP
5	LVDS1_BLDW
6	GND
7	GND

7 : Power Loss Jumper
 Open: Power Loss
 Close: no Power Loss



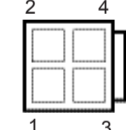
13 : Backlight Power Connector (BLT_PWR1)

PIN	Signal Name
1	GND
2	GND
3	CON_LBKLT_CTL
4	CON_LBKLT_EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC

3 : M.2 (Key-E)

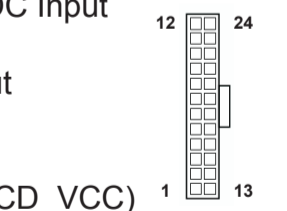
Pin#	Signal#	Signal#	Pin#
1#	GND#	+3.3V#	2#
3#	USB_D#	+3.3V#	4#
5#	USB_D#	NA#	6#
7#	GND#	NA#	8#
9#	CHV_WGR_D1-	CHV_RF_RESET	10#
11#	CHV_WGR_D1+	NA#	12#
13#	GND	XTAL_CLKREQ	14#
15#	CHV_WGR_D0-	NA#	16#
17#	CHV_WGR_D0+	GND#	18#
19#	GND	NA#	20#
21#	CHV_WGR_CLK-	CHV_BR#_RSP	22#
23#	CHV_WGR_CLK+	#	#
25#	GND#	CHV_BR#_DT	32#
33#	FETr#	CHV_BR#_RSP	34#
37#	FETr#	CHV_BR#_DT	36#
39#	GND#	NA#	38#
41#	PERP#	NA#	40#
43#	PERn#	NA#	42#
45#	GND#	NA#	44#
47#	PEFCUR#	NA#	46#
49#	PEFCUR#	NA#	48#
51#	GND#	SUSCP#	50#
53#	CLKREQ#	PERST0#	52#
55#	WAKE#	W_DISABLE1#	54#
57#	GND#	W_DISABLE2#	56#
59#	CHV_WT_D1-	SMB_DATA#	58#
61#	CHV_WT_D1+	SMB_CLK#	60#
63#	GND#	NA#	62#
65#	CHV_WT_D0-	CLKR_XTAL_LCP	64#
67#	CHV_WT_D0+	NA#	66#
69#	GND#	NA#	68#
71#	CHV_WT_CLK-	NA#	70#
73#	CHV_WT_CLK+	+3.3V#	72#
75#	GND#	+3.3V#	74#

8 : ATX Power Connector
 1-2: GND
 3-4: DC Input

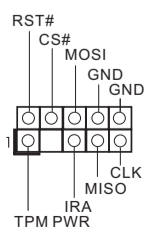


14 : Backlight Power Select (LCD_BLT_VCC) (BKT_PWR1)

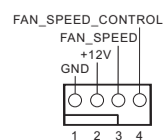
1-2: LCD_BLT_VCC: +5V
 2-3: LCD_BLT_VCC: +12V
 4-5: LCD_BLT_VCC: DC Input



4 : SPI TPM Header



5 : 4-Pin CPU FAN Connector (+12V)



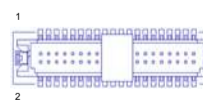
9 : Clear CMOS Jumper
 Open: Normal
 Short: Auto Clear CMOS When AC Power On



10 : Backlight Control Level (BLT_PWM1)

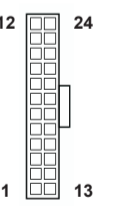
1-2: From eDP PWM to CON_LBKLT_CTL
 2-3: From LVDS PWM to CON_LBKLT_CTL

11 : LVDS Panel Connector (BOM option with eDP)



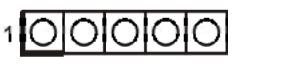
PIN	Signal Name	Signal Name	PIN
2	LCD_VCC	LCD_VCC	39
4	LDDC_CLK	+3.3V	40
6	LVDS_A_DATA0#	LDDC_DATA	
8	GND	LVDS_A_DATA1#	
10	LVDS_A_DATA1	LVDS_A_DATA2#	
12	LVDS_A_DATA2#	LVDS_A_DATA3#	
14	GND	LVDS_A_CLK#	
16	LVDS_A_DATA3	LVDS_B_DATA0#	
18	LVDS_A_CLK#	LVDS_B_DATA1#	
20	GND	LVDS_B_DATA2#	
22	LVDS_B_DATA0#	LVDS_B_DATA3	
24	LVDS_B_DATA1#	LVDS_B_CLK#	
26	GND	CON_LBKLT_EN	
28	LVDS_B_DATA2	CON_LBKLT_CTL	
30	LVDS_B_DATA3#	LCD_BLT_VCC	
32	GND	LCD_BLT_VCC	
34	LVDS_B_CLK	LCD_BLT_VCC	
36	CON_LBKLT_EN		
38	LCD_BLT_VCC		
40	LCD_BLT_VCC		

15 : 24-pin ATX Power Input Connector

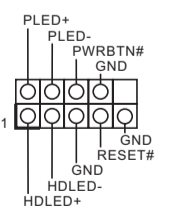


16 : Panel Power Select (LCD_VCC) (PNL_PWR1)

1-2: LCD_VCC: +3V
 2-3: LCD_VCC: +5V
 4-5: LCD_VCC: +12V

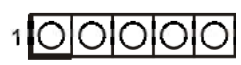


17 : System Panel Header

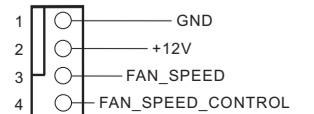


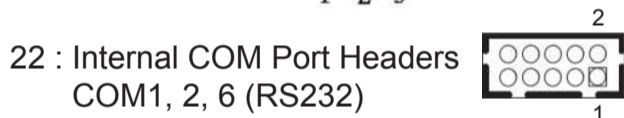
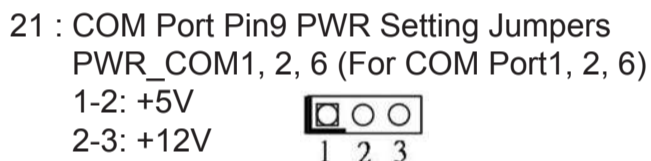
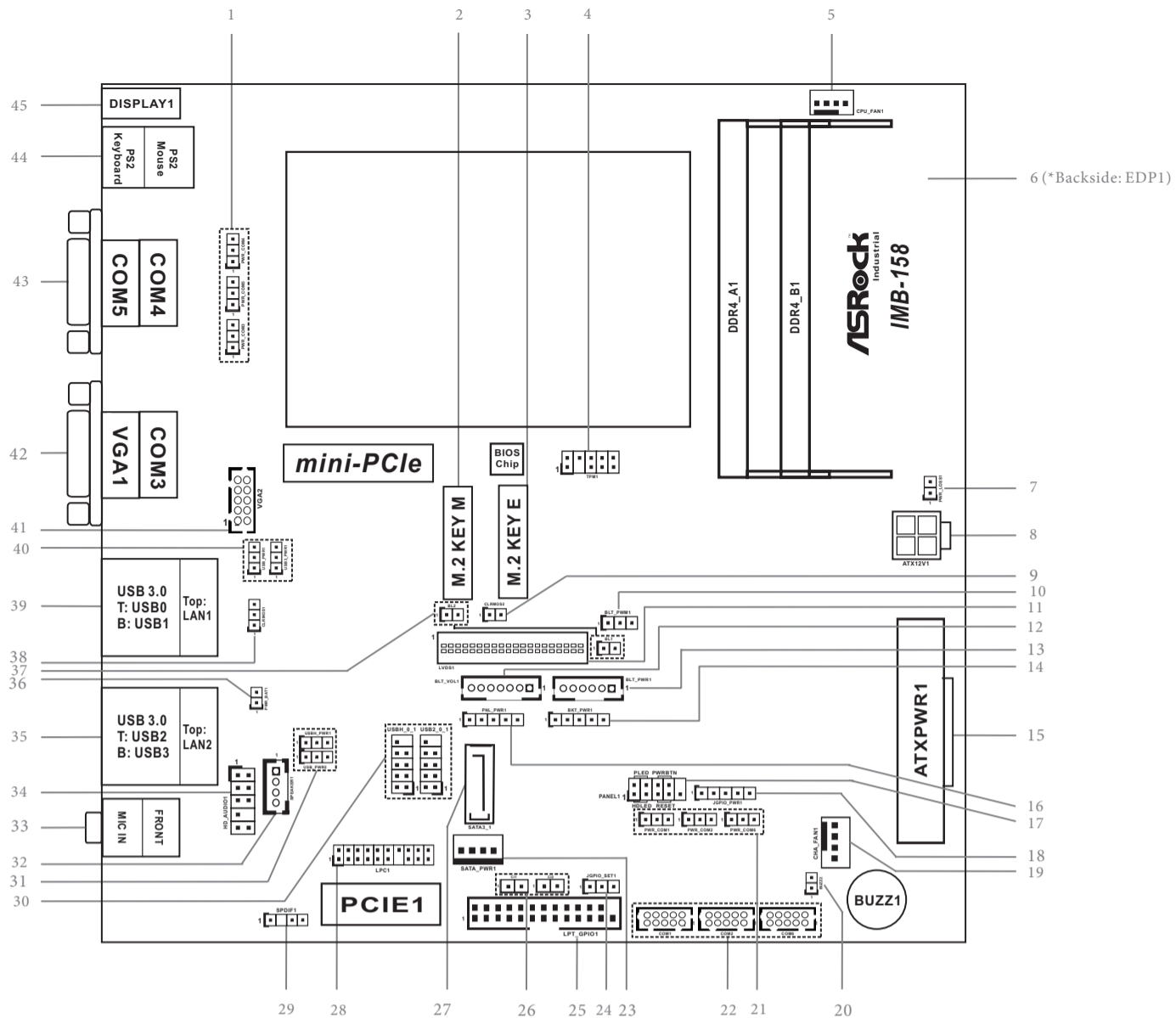
18 : Digital Input / Output Power Select (JGPIO_PWR) (JGPIO_PWR1)

1-2: +12V
 2-3: +5V
 4-5: GND

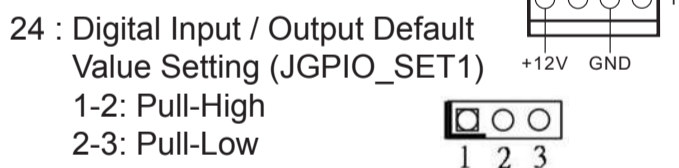


19 : 4-Pin Chassis FAN Connector (+12V)



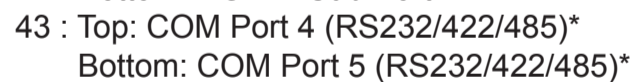
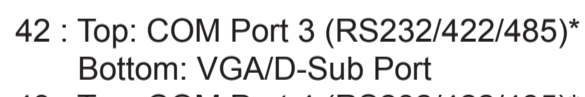
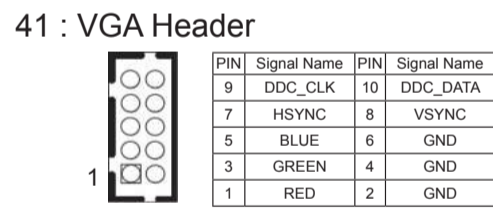
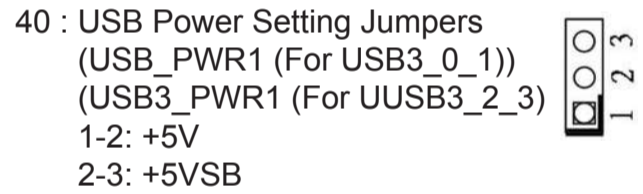
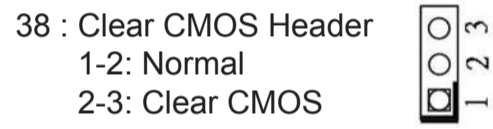
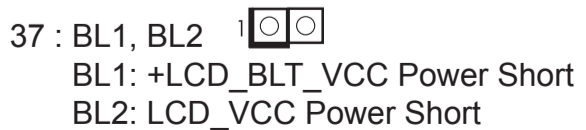
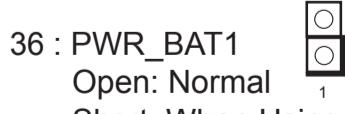
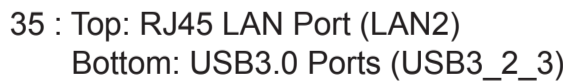
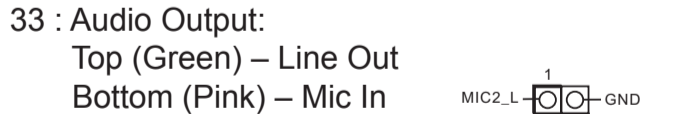
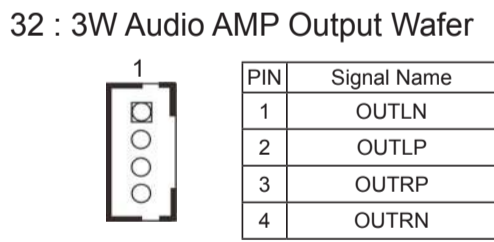
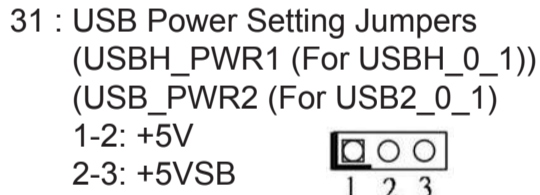
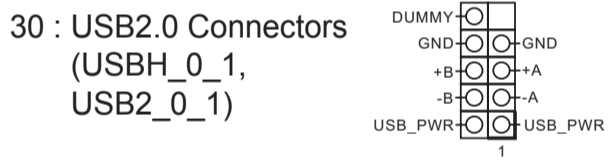
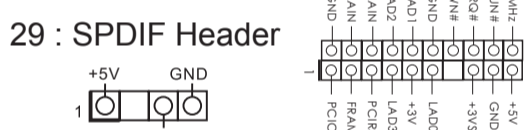
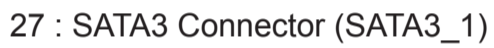
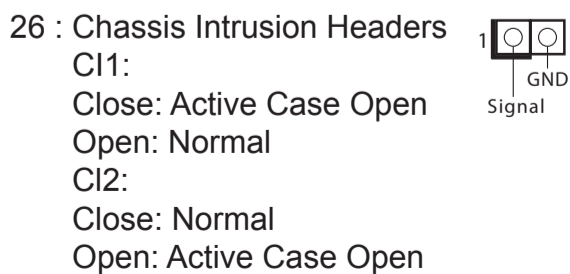


PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
10	N/A	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD
9	PWR	7	RRTS#	5	GND	3	TTXD	1	DDCD#



Printer Port:		GPIO:	
PIN	Signal Name	PIN	Signal Name
1	STB#	1	SIO_Gp47
2	SPD0	2	SIO_Gp46
3	SPD1	3	SIO_Gp45
4	SPD2	4	SIO_Gp44
5	SPD3	5	SIO_Gp41
6	SPD4	6	SIO_Gp44
7	SPD5	7	SIO_Gp40
8	SPD6	8	SIO_Gp43
9	SPD7	9	SIO_Gp37
10	SPD8	10	SIO_Gp36
11	SPD9	11	SIO_Gp36
12	SPD10	12	SIO_Gp36
13	SPD11	13	SIO_Gp35
14	SPD12	14	SIO_Gp35
15	SPD13	15	SIO_Gp34
16	SPD14	16	SIO_Gp34
17	SPD15	17	SIO_Gp33
18	SPD16	18	SIO_Gp33
19	SPD17	19	SIO_Gp32
20	SPD18	20	SIO_Gp32
21	SPD19	21	SIO_Gp31
22	SPD20	22	SIO_Gp31
23	SPD21	23	SIO_Gp30
24	SPD22	24	SIO_Gp30
25	SPD23	25	NA
26	SPD24	26	NC
27	SPD25	27	NC

* If you want to use the printer port function, please short pin4 and pin5 on Digital Input / Output Power Select (JGPIO_PWR1).



* This motherboard supports RS232/422/485 on COM3~5 ports. Please refer to below table for the pin definition. In addition, COM3~5 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM3~5 Ports Pin Definition			
PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	COM4: +5V/+12V/+5VSB COM3, 5: +5V/+12V	COM4: +5V/+12V/+5VSB COM3, 5: +5V/+12V	COM4: +5V/+12V/+5VSB COM3, 5: +5V/+12V

