

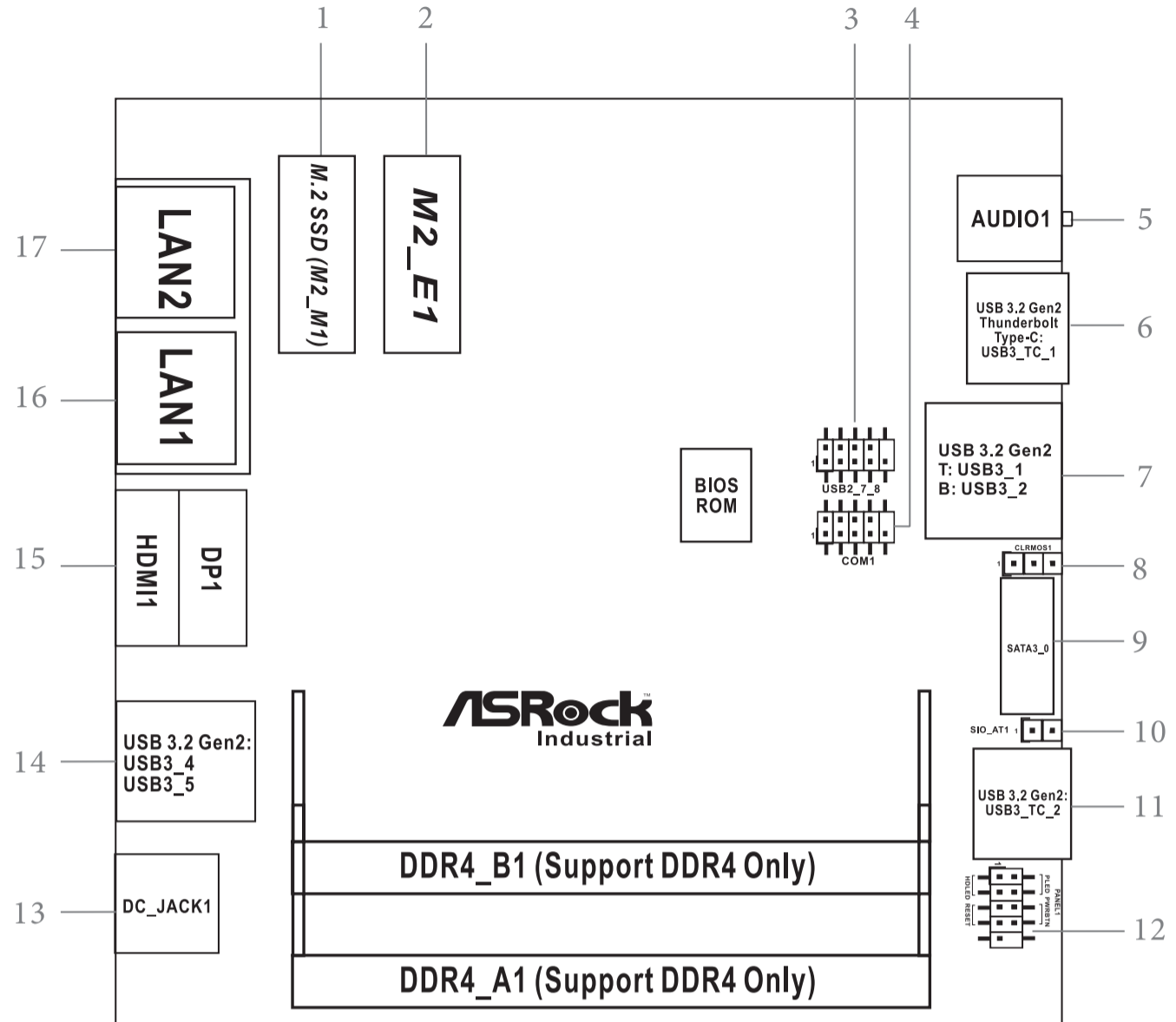
NUC-1165G7
NUC-1135G7
NUC-1115G4
NUC-1185G7E
NUC-1145G7E
NUC-1115G4E
NUC-6305E

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Revision History

Date	Description
October 27, 2020	First Release



1 : M.2 Key-M Socket (M2_M1)

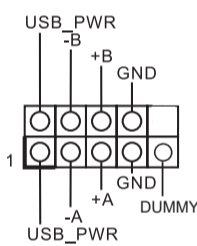
Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NC	6
7	PERp3	NC	8
9	GND	SATA_LED	10
11	PETn3	+3.3V	12
13	PETp3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	DEVSLP	38
39	GND	SMB_CLK	40
41	PERn0/SATA-B+	SMB_DATA	42
43	PERp0/SATA-B-	NA	44
45	GND	NA	46
47	PETn0/SATA-A-	NA	48
49	PETp0/SATA-A+	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		

* Pin6 and Pin8 are defined as USB2.0 signal to support Key-M to Key-B extension card.

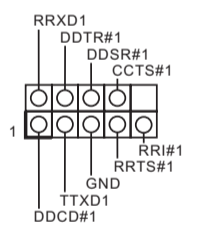
2 : M.2 Key-E Socket (M2_E1)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_RF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODEM_CLKREQ	14
15	CNV_WGR_DD-	NA	16
17	CNV_WGR_DD+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BRI_RSP	22
23	CNV_WGR_CLK+		
33	GND	CNV_BGI_DT	32
35	PETp	CNV_RGI_RSP	34
37	PETn	CNV_BBI_DT	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFCLKp	NA	46
49	PEFCLKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST#	52
55	WAKE#	W_DISABLE#	54
57	GND	W_DISABLE2#	56
59	CNV_WT_D1-	SMB_DATA	58
61	CNV_WT_D1+	SMB_CLK	60
63	GND	NA	62
65	CNV_WT_DD-	CLKIN_XTAL_LCP	64
67	CNV_WT_DD+	NA	66
69	GND	NA	68
71	CNV_WT_CLK-	NA	70
73	CNV_WT_CLK+	+3.3V	72
75	GND	+3.3V	74

3 : USB2.0 Connector (USB2_7_8)



4 : COM Port Header (RS232)



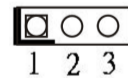
5 : Audio Jack (AUDIO1)

6 : USB 3.2 Gen2 Thunderbolt Type-C Port (USB3_TC_1)

7 : USB 3.2 Gen2 Ports (USB3_1_2)

8 : Clear CMOS Header (CLRCMOS1)

1-2 : Auto Clear (PWR OFF)
2-3 : Clear CMOS



9 : SATA3 Port (SATA3_0)

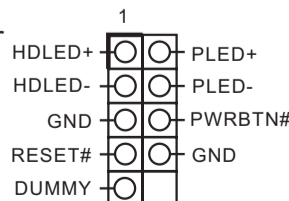
10 : SIO_AT1

Open : ATX Mode
Short : AT Mode



11 : USB 3.2 Gen2 Type-C Port (USB3_TC_2)

12 : System Panel Header (PANEL1)



13 : DC-In Jack (DC_IN1)

14 : USB 3.2 Gen2 Ports (USB3_4_5)

15 : Top : DisplayPort (DP1) Bottom : HDMI Port (HDMI1)

16 : RJ-45 LAN Port (LAN1)

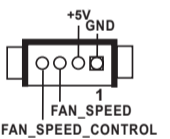
17 : RJ-45 LAN Port (LAN2)

Back Side :

Power Button (PWR_BTN1)



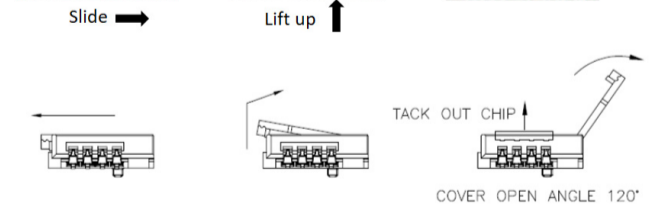
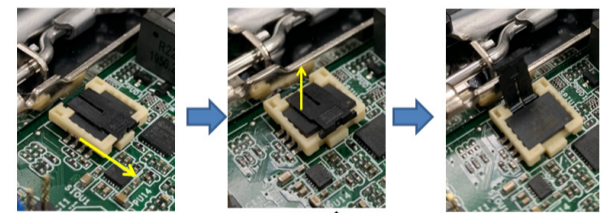
Fan Connector (FAN1)



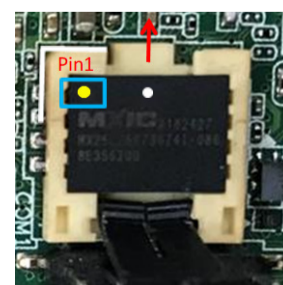
Battery Connector (BAT1)

ESPI Connector (ESPI)

Installation of ROM Socket



* Do not apply force to the actuator cover after ic inserted.
* Do not apply force to actuator cover when it is opening over 120 degree, Otherwise, the actuator cover may be broken.



* The yellow dot (Pin1) on the ROM must be installed at pin1 position of the socket.
* Make sure the white dot on the ROM is installed outwards of the socket.
* For further details of how to install ROM, please refer to ASRI website.
Warning: If the installation does not follow as the picture, then it may cause severe damage to chipset & MB.