

**IMB-1222**

**IMB-1222-WV**

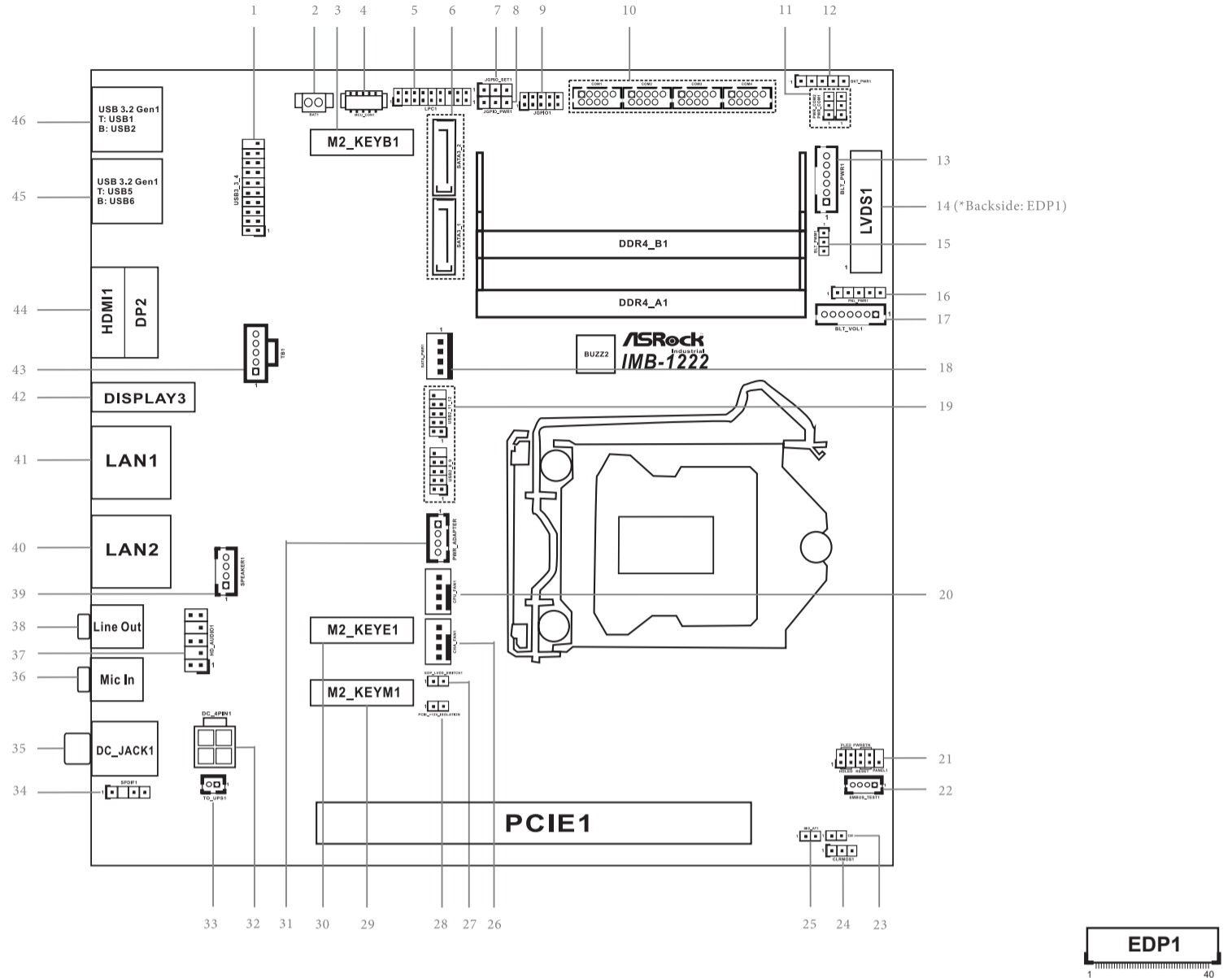
**IMB-X1222-WV**

The terms HDMI® and HDMI High-Definition Multimedia Interface, and the HDMI logo are trademarks or registered trademarks of HDMI Licensing LLC in the United States and other countries.

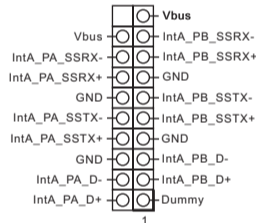


**Revision History**

Date	Description
August 12, 2021	First Release

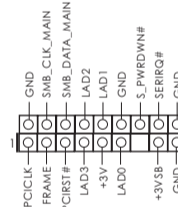


**1 : USB3.2 Gen1 Header (USB3\_3\_4)**



**2 : Battery Connector**  
**4 : MCU Connector**

**5 : LPC Header**



**6 : SATA3 Connectors (SATA3\_1, SATA3\_2)**

**7 : Digital Input / Output Default Value Setting (JGPIO\_SET1)**

1-2: Pull-High  
2-3: Pull-Low

**8 : Digital Input / Output Power Select (JGPIO\_PWR1)**

1-2: +12V  
2-3: +5V

**9 : Digital Input/Output Pin Header (JGPIO1)**

PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
2	SIO_GP71	4	SIO_GP72	6	SIO_GP73	8	SIO_GP74	10	GND
1	SIO_GP75	3	SIO_GP76	5	SIO_GP77	7	SIO_GP80	9	JGPIO_PWR

**10 : COM Port Headers (COM1, 2, 3, 4)**

PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	DDCD#	3	TTXD	5	GND	7	RRTS#
2	RRXD	4	DDTR#	6	DDSR#	8	CCTS#

**COM1, 2 Port Pin Definition**

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	TX+	RTX+
3	TXD	RX+	N/A
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	PWR	PWR	PWR

**11 : COM Port PWR Setting Jumpers**

PWR\_COM1 (For COM Port1)  
PWR\_COM2 (For COM Port2)

1-2: +5V  
2-3: +12V

**12 : Backlight Power Select (LCD\_BLT\_VCC) (BKT\_PWR1)**

1-2: LCD\_BLT\_VCC: +5V  
2-3: LCD\_BLT\_VCC: +12V  
4-5: LCD\_BLT\_VCC: DC\_IN

**13 : Inverter Power Control Wafer (BLT\_PWR1)**

PIN	Signal Name
6	LCD_BLT_VCC
5	LCD_BLT_VCC
4	CON_LBKLT_EN
3	CON_LBKLT_CTL
2	GND
1	GND

**14\* : LVDS Panel Connector**

PIN	Signal Name	PIN	Signal Name
39	LCD_BLT_VCC	40	LCD_BLT_VCC
37	CON_LBKLT_CTL	38	LCD_BLT_VCC
35	GND	36	CON_LBKLT_EN
33	LVDS_B_CLK#	34	LVDS_B_CLK
31	LVDS_B_DATA3	32	GND
29	DPLVDD_EN	30	LVDS_B_DATA3#
27	LVDS_B_DATA2#	28	LVDS_B_DATA2
25	LVDS_B_DATA1	26	GND
23	GND	24	LVDS_B_DATA1#
21	LVDS_B_DATA0#	22	LVDS_B_DATA0
19	LVDS_A_CLK	20	GND
17	GND	18	LVDS_A_CLK#
15	LVDS_A_DATA3#	16	LVDS_A_DATA3
13	LVDS_A_DATA2	14	GND
11	GND	12	LVDS_A_DATA2#
9	LVDS_A_DATA1#	10	LVDS_A_DATA1
7	LVDS_A_DATA0	8	PD (Panel Detection)
5	LDDC_DATA	6	LVDS_A_DATA0#
3	+3.3V	4	LDDC_CLK
1	LCD_VCC	2	LCD_VCC

\* PD (Panel Detection): Connect this pin to LVDS Panel's Ground pin to detect Panel detection.

**\* eDP Connector (on the Backside of PCB)**

PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	NA	2	NA	3	eDP_TX6L_CON	4	eDP_TX3_CON	5	GND	6	eDP_TX2E_CON	7	eDP_TX2_CON
8	GND	9	eDP_TX1E_CON	10	eDP_TX1_CON	11	GND	12	eDP_TX6L_CON	13	eDP_TX3_CON	14	GND
15	eDP_AUX_CON	16	eDP_AUX_CON	17	GND	18	LCD_VCC	19	LCD_VCC	20	LCD_VCC	21	LCD_VCC
22	NA	23	NA	24	GND	25	GND	26	GND	27	eDP_HPD_CON	28	GND
29	GND	30	GND	31	GND	32	CON_LBKLT_EN	33	CON_LBKLT_CTL	34	SMB_DATA	35	SMB_CLK
36	LCD_BLT_VCC	37	LCD_BLT_VCC	38	LCD_BLT_VCC	39	LCD_BLT_VCC	40	NA				

**15 : Backlight Control Level (CON\_LBKLT\_CTL) (BLT\_PWM1)**

1-2: From eDP PWM to CON\_LBKLT\_CTL  
2-3: From LVDS PWM to CON\_LBKLT\_CTL

**16 : Panel Power Select (LCD\_VCC) (PNL\_PWR1)**

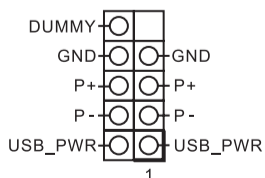
1-2: LCD\_VCC: +3V  
2-3: LCD\_VCC: +5V  
4-5: LCD\_VCC: +12V

**17 : Backlight Volume Control (BLT\_VOL1)**

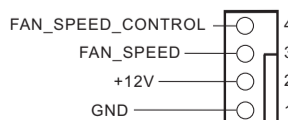
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
7	GND	6	GND	5	BLT_DW	4	BLT_UP	3	PWRDN	2	GPIO_VOL_DW
1	GPIO_VOL_UP										

**18 : SATA Power Output Connector**

**19 : USB2.0 Headers (USB2\_11\_12, USB2\_8\_9)**



**20 : CPU FAN Connector (+12V)**



\* This motherboard supports RS232/422/485 on COM1, 2 ports. Please refer to below table for the pin definition. In addition, COM1, 2 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

