

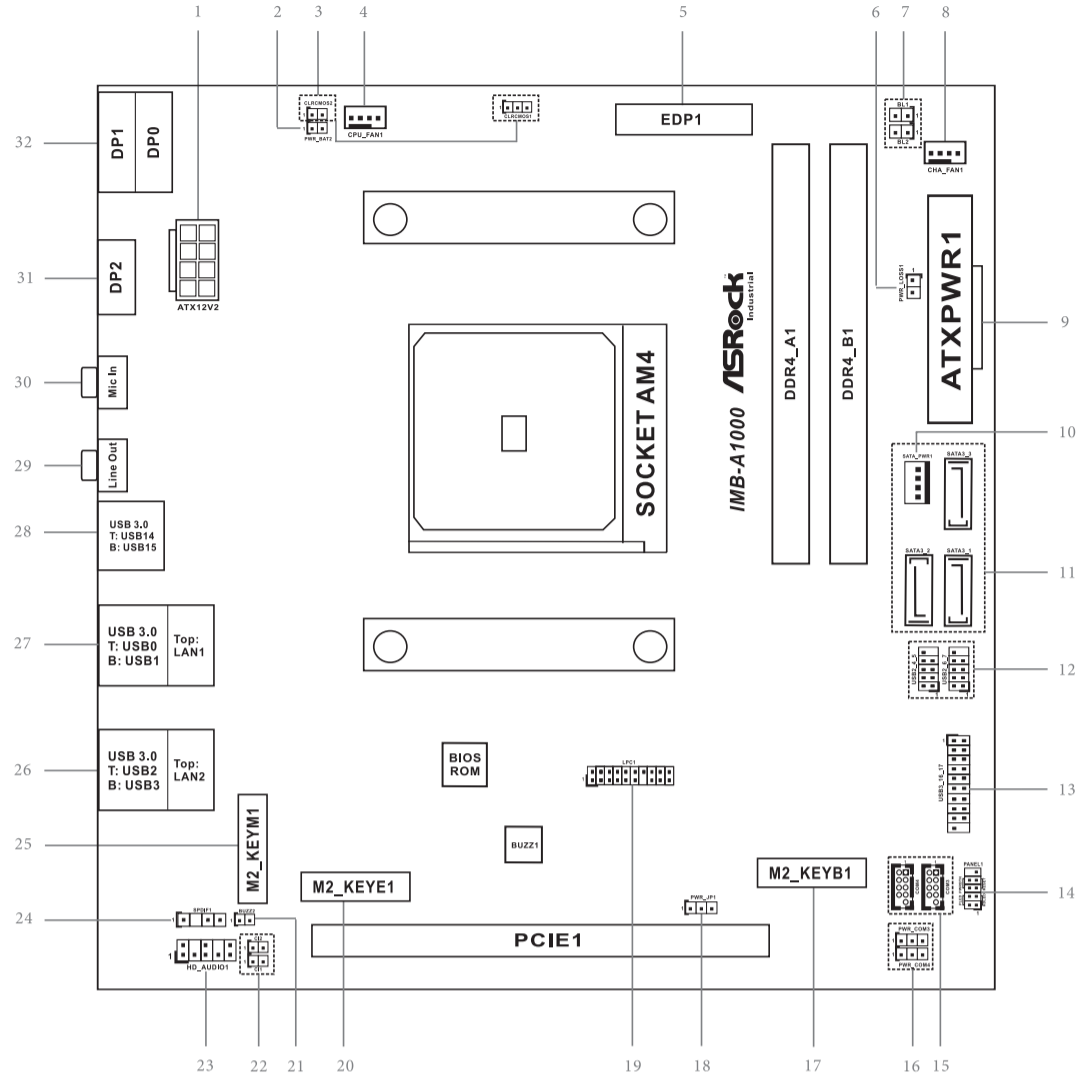


P/N: 15G06M086000AK V1.0

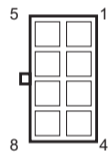
ASRock Industrial

# Jumpers and Headers Setting Guide

## IMB-A1000



1 : ATX 12V Power Connector

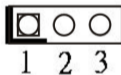


2 : PWR\_BAT2  
Open: Normal  
Short: Charge Battery

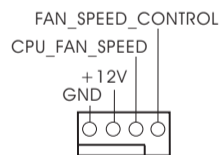


3 : Clear CMOS Headers

CLRMOSS2 :  
Open : Normal  
Short : Auto Clear CMOS (Power Off)  
CLRCMOS1 :  
1-2 : Normal  
2-3 : Clear CMOS



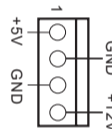
4 : CPU FAN Connector (+12V)



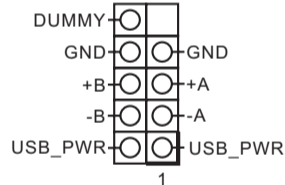
5 : eDP Connector



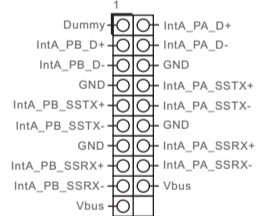
10 : SATA Power Output Connector



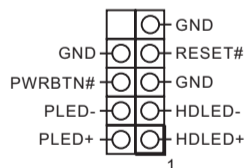
11 : SATA3 Connectors (SATA3\_1~3)



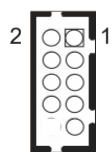
13 : USB 3.0 Header (USB3\_16\_17)



14 : System Panel Header



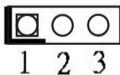
15 : COM Port Headers (COM3, 4) (RS232)



PIN	Signal Name	PIN	Signal Name
2	RRXD	1	DDCD#
4	DDTR#	3	TTXD
6	DDSR#	5	GND
8	CCTS#	7	RRTS#
10	N/A	9	+5V

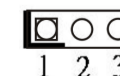
16 : COM Port PWR Setting Jumpers  
PWR\_COM3 (For COM Port3)  
PWR\_COM4 (For COM Port4)

1-2 : +5V  
2-3 : +12V

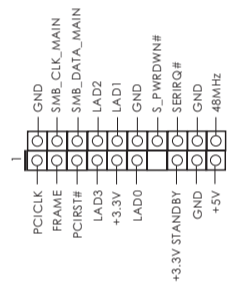


18 : ATX/AT Mode Select (PWR\_JP1)

1-2 : AT Mode  
2-3 : ATX Mode



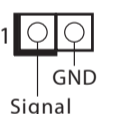
19 : LPC Header



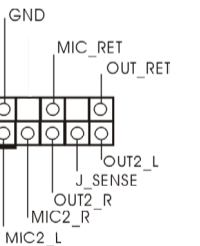
21 : Buzzer (BUZZ2)



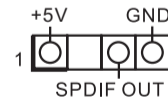
22 : Chassis Intrusion Headers  
CI1 :  
Close : Active Case Open  
Open : Normal  
CI2 :  
Close : Normal  
Open : Active Case Open



23 : Front Panel Audio Header



24 : SPDIF Header



6 : PWR LOSS Header (PWR\_LOSS1)

Open : no Power Loss  
Short : Power Loss

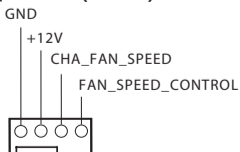


7 : BL1, BL2

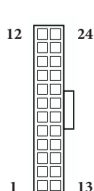


BL1 :  
Open : Protect LCD\_BLT\_VCC (+12V)  
Short : No Protect LCD\_BLT\_VCC (+12V)  
BL2 :  
Open : Protect LCD\_VCC (+3V)  
Short : No Protect LCD\_VCC (+3V)

8 : Chassis FAN Connector (+12V)



9 : 24-pin ATX Power Input Connector



26 : Top : RJ45 LAN Port (LAN2)

Bottom : USB 3.0 Ports (USB3\_2\_3)

27 : Top : RJ45 LAN Port (LAN1)

Bottom : USB 3.0 Ports (USB3\_0\_1)

28 : USB 3.0 Ports (USB3\_14\_15)

29 : Audio Jack (Line Out)

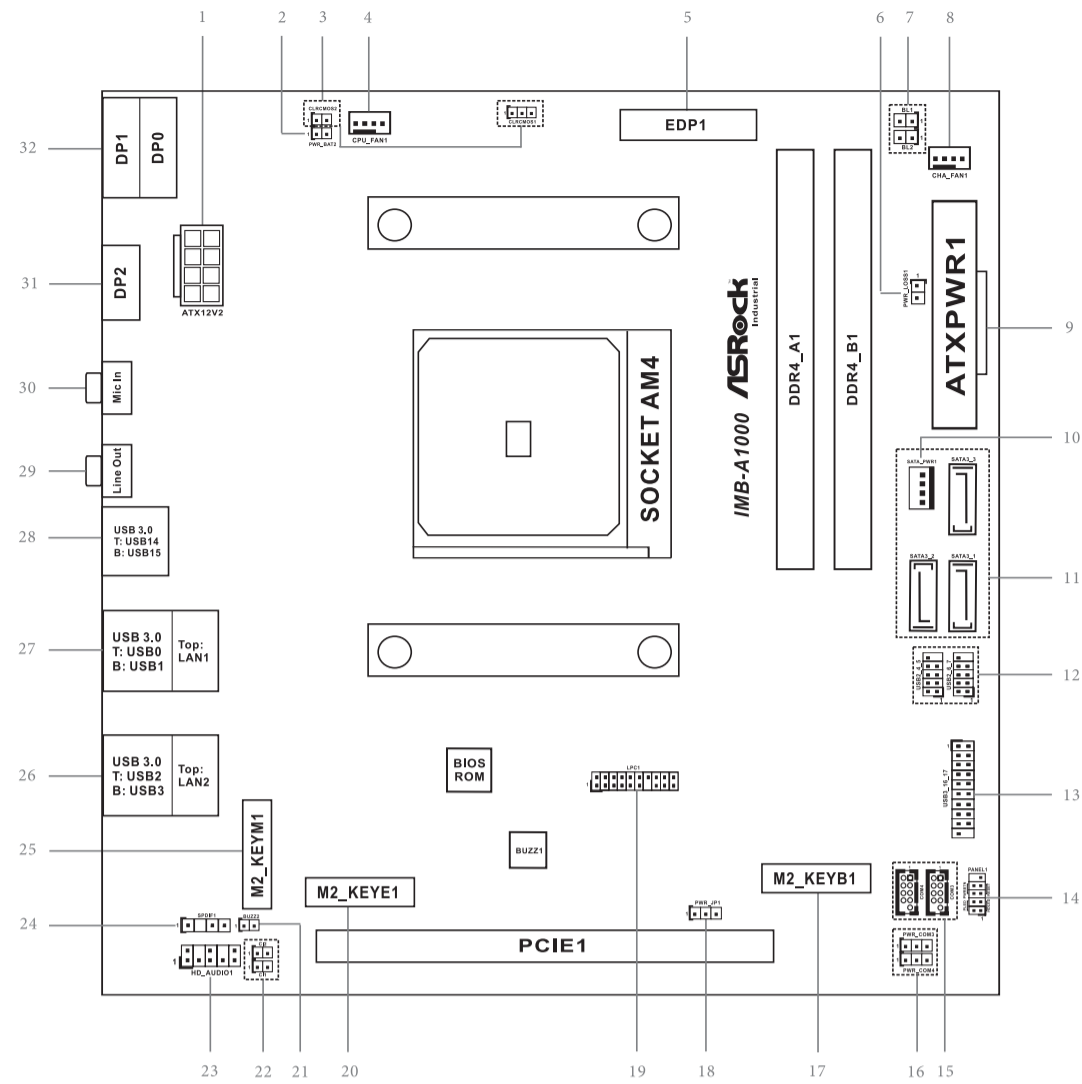
30 : Audio Jack (Mic In)

31 : DisplayPort (DP2)

32 : Top : DisplayPort (DP0)

Bottom : DisplayPort (DP1)

PIN	Signal Name
40	NA
39	LCD_BLT_VCC(+12V)
38	LCD_BLT_VCC(+12V)
37	LCD_BLT_VCC(+12V)
36	LCD_BLT_VCC(+12V)
35	LCD_BLT_VCC(+12V)
34	NA
33	CON_LINDET_CTL
32	CON_LINDET_EN
31	GND
30	GND
29	GND
28	qDP_HPH_CON
27	qDP_HPH_CON
26	GND
25	GND
24	GND
23	NA
22	LCD_VCC(+3V)
21	LCD_VCC(+3V)
20	LCD_VCC(+3V)
19	LCD_VCC(+3V)
18	LCD_VCC(+3V)
17	GND
16	qDP_AUX_CON
15	qDP_AUX_CON
14	GND
13	qDP_TX0_CON
12	qDP_TX0_CON
11	GND
10	qDP_TX1_CON
9	qDP_TX1_CON
8	GND
7	qDP_TX2_CON
6	qDP_TX2_CON
5	GND
4	qDP_TX3_CON
3	qDP_TX3_CON
2	GND
1	NA



## M.2 Socket Pin Definition

### 17 : M.2 Key-B Socket (M2\_KEYB1)

Pin	Signal	Signal	Pin
1	NA	+3.3V	2
3	GND	+3.3V	4
5	GND	FULL_Card_Power-off	6
7	USB_D+	W_DISABLE#	8
9	USB_D-	WWAN_LED#	10
11	GND		
21	GND	NA	20
23	NA	NA	22
25	NA	NA	24
27	GND	NA	26
29	USB3_RX-	NA	28
31	USB3_RX+	UIM_RESET	30
33	GND	UIM_CLK	32
35	USB3_TX-	UIM_DATA	34
37	USB3_TX+	UIM_PWR	36
39	GND	NA	38
41	PERP0	NA	40
43	PERP0	NA	42
45	GND	NA	44
47	PETN0	NA	46
49	PETP0	NA	48
51	GND	PERST#	50
53	PEFLCKn	CLKREQ#	52
55	PEFLCKp	WAKE#	54
57	GND	NA	56
59	NA	NA	58
61	NA	NA	60
63	NA	NA	62
65	NA	NA	64
67	NA	NA	66
69	NA	NA	68
71	GND	+3.3V	70
73	GND	+3.3V	72
75	NA	+3.3V	74

## Back Side :

### M.2 Key-M Socket (M2\_KEYM2)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERP3	NA	6
7	PERP3	NA	8
9	GND	SATA_LED	10
11	PETP3	+3.3V	12
13	PETP3	+3.3V	14
15	GND	+3.3V	16
17	PERP2	+3.3V	18
19	PERP2	NA	20
21	GND	NA	22
23	PETP2	NA	24
25	PETP2	NA	26
27	GND	NA	28
29	PERP1	NA	30
31	PERP1	NA	32
33	GND	NA	34
35	PETP1	NA	36
37	PETP1	DEVSLP	38
39	GND	SMB_CLK	40
41	PERP0/SATA-B+	SMB_DATA	42
43	PERP0/SATA-B-	NA	44
45	GND	NA	46
47	PETP0/SATA-a-	NA	48
49	PETP0/SATA-a+	PERST#	50
51	GND	CLKREQ#	52
53	PEFLCKn	WAKE#	54
55	PEFLCKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		

### 20 : M.2 Key-E Socket (M2\_KEYE1)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_RF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODEM_CLKREQ	14
15	CNV_WGR_D0-	NA	16
17	CNV_WGR_D0+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BR1_RSP	22
23	CNV_WGR_CLK+		
33	GND	CNV_BR1_DT	32
35	PETP	CNV_BR1_RSP	34
37	PETP	CNV_BR1_DT	36
39	GND	NA	38
41	PERP	NA	40
43	PERP	NA	42
45	GND	NA	44
47	PEFLCKp	NA	46
49	PEFLCKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST#	52
55	WAKE#	W_DISABLE#	54
57	GND	W_DISABLE2#	56
59	CNV_WT_D1-	SMB_DATA	58
61	CNV_WT_D1+	SMB_CLK	60
63	GND	NA	62
65	CNV_WT_D0-	CLKIN_XTAL_LCP	64
67	CNV_WT_D0+	NA	66
69	GND	NA	68
71	CNV_WT_CLK-	NA	70
73	CNV_WT_CLK+	+3.3V	72
75	GND	+3.3V	74

## SIM Card Socket (SIM1)

### 25 : M.2 Key-M Socket (M2\_KEYM1)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERP3	NA	6
7	PERP3	NA	8
9	GND	SATA_LED	10
11	PETP3	+3.3V	12
13	PETP3	+3.3V	14
15	GND	+3.3V	16
17	PERP2	+3.3V	18
19	PERP2	NA	20
21	GND	NA	22
23	PETP2	NA	24
25	PETP2	NA	26
27	GND	NA	28
29	PERP1	NA	30
31	PERP1	NA	32
33	GND	NA	34
35	PETP1	NA	36
37	PETP1	DEVSLP	38
39	GND	SMB_CLK	40
41	PERP0/SATA-B+	SMB_DATA	42
43	PERP0/SATA-B-	NA	44
45	GND	NA	46
47	PETP0/SATA-a-	NA	48
49	PETP0/SATA-a+	PERST#	50
51	GND	CLKREQ#	52
53	PEFLCKn	WAKE#	54
55	PEFLCKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		