



P/N: 15G06M095001AK V1.1

ASRock Industrial

Jumpers and Headers Setting Guide

IMB-1711

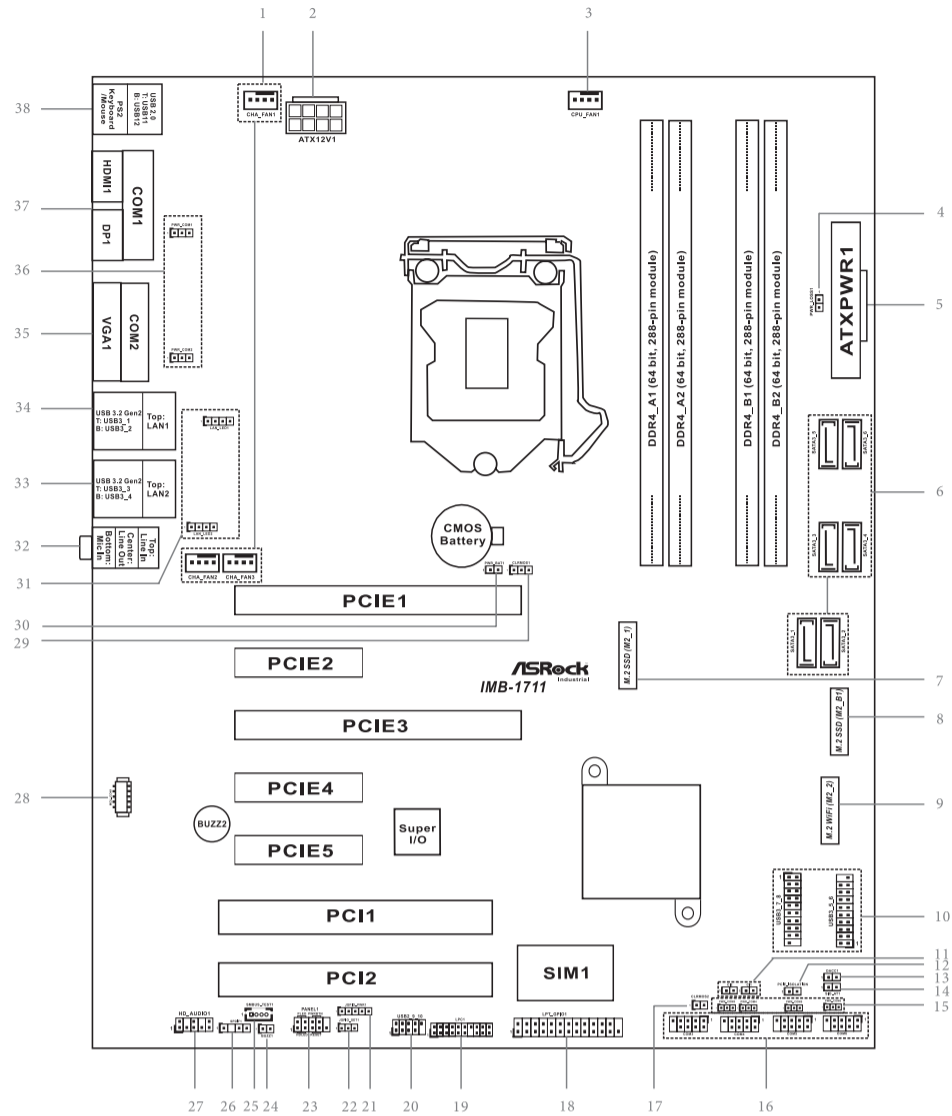
IMB-X1711

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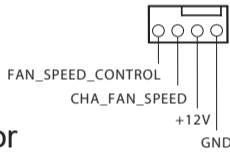


Revision History

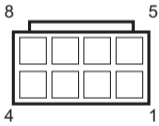
Date	Description
June 20, 2022	First Release
February 7, 2024	Second Release



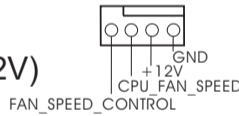
1 : Chassis FAN Connectors (+12V) (CHA_FAN1~3)



2 : ATX 12V Power Connector



3 : CPU FAN Connector (+12V)

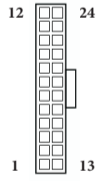


4 : PWR LOSS Jumper (PWR_LOSS1)

Short : Power Loss
Open : no Power Loss



5 : 24-pin ATX Power Input Connector



6 : SATA3 Connectors (SATA3_1 ~ SATA3_6)

7 : M.2 Key-M Socket (M2_1)

Pin	Signal Name	Signal Name	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NA	6
7	PERp3	NA	8
9	GND	SATA_LED	10
11	PETn3	+3.3V	12
13	PETp3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	DEVSLP	38
39	GND	NA	40
41	PERn0/SATA-B+	NA	42
43	PERp0/SATA-B-	NA	44
45	GND	NA	46
47	PETn0/SATA-A+	NA	48
49	PETp0/SATA-A-	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
59	CNV WT D1-	NA	60
61	CNV WT D1+	NA	62
63	GND	NA	64
65	CNV WT D0-	CLKIN_XTAL_LCP	66
67	CNV WT D0+	NA	68
69	GND	NA	70
71	CNV WT CLK-	NA	72
73	CNV WT CLK+	+3.3V	74
75	GND	+3.3V	74

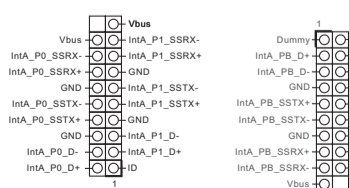
8 : M.2 Key-B Socket (M2_B1)

Pin	Signal Name	Signal Name	Pin
1	NA	+3.3V	2
3	GND	+3.3V	4
5	GND	Full_Card_Power_off	6
7	USB_D+	W_DISABLE	8
9	USB_D-	WWAN_LED#	10
11	GND		
21	GND	NA	20
23	NA	NA	22
25	NA	NA	24
27	GND	NA	26
29	USB3_RX-	NA	28
31	USB3_RX+	UIM_RESET	30
33	GND	UIM_CLK	32
35	USB3_TX-	UIM_DATA	34
37	USB3_TX+	UIM_PWR	36
39	GND	NA	38
41	PERn0	NA	40
43	PERp0	NA	42
45	GND	NA	44
47	PETn0	NA	46
49	PETp0	NA	48
51	GND	PERST#	50
53	PEFCLKn	CLKREQ#	52
55	PEFCLKp	WAKE#	54
57	GND	NA	56
59	NA	NA	58
61	NA	NA	60
63	NA	NA	62
65	NA	NA	64
67	NA	NA	66
69	NA	NA	68
71	GND	+3.3V	70
73	GND	+3.3V	72
75	NA	+3.3V	74

9 : M.2 Key-E Socket (M2_2)

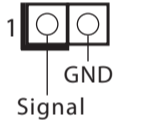
Pin	Signal Name	Signal Name	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_RF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODEM_CLKREQ	14
15	CNV_WGR_D0-	NA	16
17	CNV_WGR_D0+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BRI_RSP	22
23	CNV_WGR_CLK+		
33	GND	CNV_BCI_DT	32
35	PETp	CNV_RGI_RSP	34
37	PETn	CNV_BRI_DT	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFCLKp	NA	46
49	PEFCLKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST0#	52
55	WAKE#	W_DISABLE1#	54
57	GND	W_DISABLE2#	56
59	CNV_WT D1-	NA	58
61	CNV_WT D1+	NA	60
63	GND	NA	62
65	CNV_WT D0-	CLKIN_XTAL_LCP	64
67	CNV_WT D0+	NA	66
69	GND	NA	68
71	CNV_WT CLK-	NA	70
73	CNV_WT CLK+	+3.3V	72
75	GND	+3.3V	74

10 : USB 3.2 Gen1 Headers (USB3_5_6, USB3_7_8)



11 : Chassis Intrusion Headers (CI1, CI2)

CI1 :
Close : Active Case Open



Open : Normal

CI2 :

Close : Normal

Open : Active Case Open



12 : PCIe Isolation Jumper (PCIE_ISOLATION)

13 : DACC1

Short : ACC



Open : no ACC

14 : ATX/AT Mode Jumper (SIO_AT1)

Open : ATX Mode

Short : AT Mode



COM Port Pin9 PWR Setting Jumpers

15 : PWR_COM3 (For COM Port3)

PWR_COM4 (For COM Port4)

PWR_COM5 (For COM Port5)

PWR_COM6 (For COM Port6)

36 : PWR_COM1 (For COM Port1)

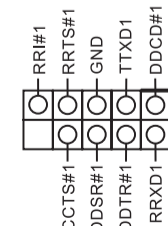
PWR_COM2 (For COM Port2)

1-2 : +5V

2-3 : +12V



16 : COM Port Headers (COM3~6) (RS232)

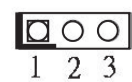


Clear CMOS Headers

29 : CLRMO1 :

1-2 : Normal

2-3 : Clear CMOS

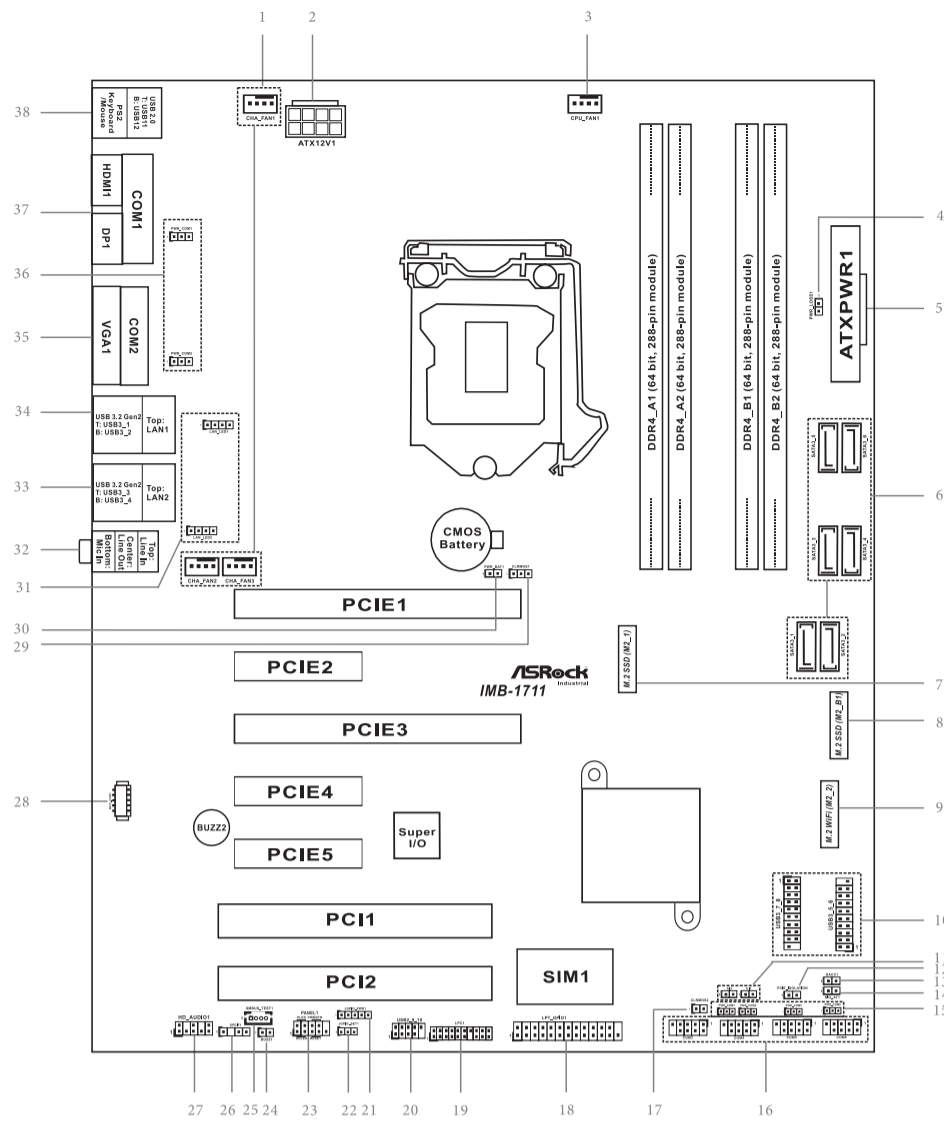


17 : CLRMO2 :

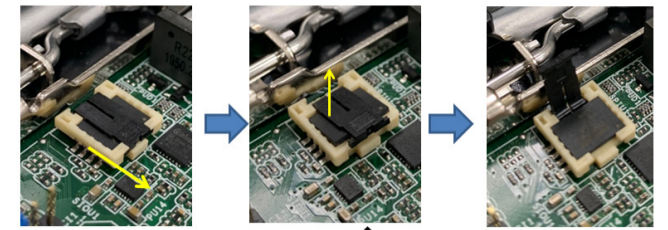
Open : Normal

Short : Auto Clear CMOS (Power Off)



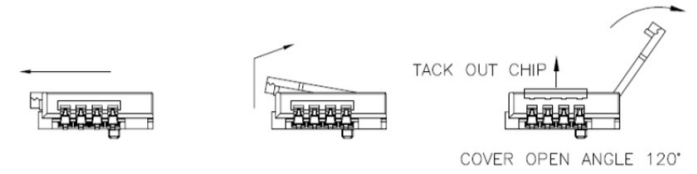


Installation of ROM Socket

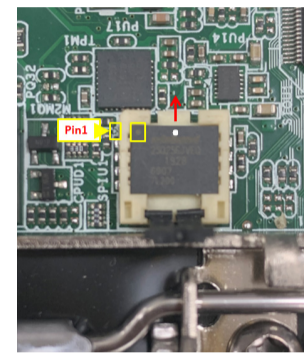


Slide →

Lift up ↑



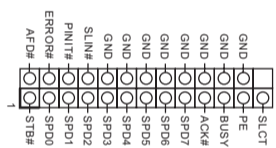
* Do not apply force to the actuator cover after ic inserted.
 * Do not apply force to actuator cover when it is opening over 120 degree, Otherwise, the actuator cover may be broken.



* The yellow dot (Pin1) on the ROM must be installed at pin1 position of the socket (white arrow area).
 * Make sure the white dot on the ROM is installed outwards of the socket.
 * For further details of how to install ROM, please refer to ASRock website.
Warning: If the installation does not follow as the picture, then it may cause severe damage to chipset & MB.

18 : Printer Port / GPIO Header (LPT_GPIO1)

Printer Port:

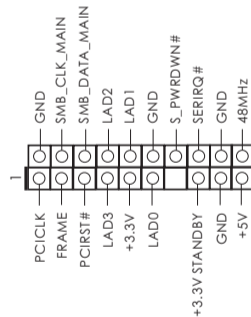


GPIO:

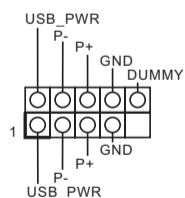
PIN	Signal Name	PIN	Signal Name
26	NC	25	NA
24	GND	23	SIO_GP70
22	GND	21	SIO_GP71
20	GND	19	SIO_GP72
18	GND	17	SIO_GP87
16	GND	15	SIO_GP86
14	GND	13	SIO_GP85
12	JGPIO_PWR	11	SIO_GP84
10	JGPIO_PWR	9	SIO_GP83
8	SIO_GP73	7	SIO_GP82
6	SIO_GP74	5	SIO_GP81
4	SIO_GP75	3	SIO_GP80
2	SIO_GP76	1	SIO_GP77

* If you want to use the printer port function, please short pin4 and pin5 on Digital Input / Output Power Select (JGPIO_PWR1).

19 : LPC Header



20 : USB 2.0 Header (USB2_9_10)



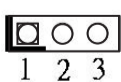
21 : Digital Input / Output Power Select (JGPIO_PWR) (JGPIO_PWR1)

1-2 : +12V
 2-3 : +5V
 3-4 : +5V
 4-5 : GND

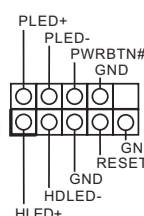


22 : Digital Input / Output Default Value Setting (JGPIO_SET1)

1-2 : Pull-High
 2-3 : Pull-Low



23 : System Panel Header



24 : Buzzer (BUZZ1)

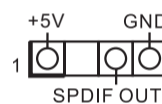


25 : SMBUS_TEST1

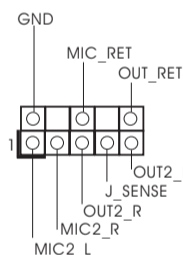


PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	+3V	2	SMB_CLK	3	SMB_DATA	4	GND

26 : SPDIF Header



27 : Front Panel Audio Header



28 : MCU Connector (MCU_CON1)

30 : PWR_BAT1



Open : Normal
 Short : Charge Battery

31 : LAN LED Headers :



LAN_LED1 (For LAN1 Port)
 LAN_LED2 (For LAN2 Port)

32 : Audio Jacks

Blue - Line In
 Green - Line Out
 Pink - Mic In

33 : Top : RJ45 LAN Port (LAN2)

Bottom : USB 3.2 Gen2 Ports (USB3_3_4)

34 : Top : RJ45 LAN Port (LAN1)

Bottom : USB 3.2 Gen2 Ports (USB3_1_2)

35 : Top : COM Port (COM2) (RS232/422/485)*
 Bottom : D-Sub Port (VGA1)

37 : Top : COM Port (COM1) (RS232/422/485)*
 Bottom Right : DisplayPort (DP1)
 Bottom Left: HDMI Port (HDMI1)

* This motherboard supports RS232/422/485 on COM1, 2 ports. Please refer to below table for the pin definition. In addition, COM1, 2 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM1, 2 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD, Data Carrier Detect	TX-	RTX-
2	RXD, Receive Data	RX+	N/A
3	TXD, Transmit Data	TX+	RTX+
4	DTR, Data Terminal Ready	RX-	N/A
5	GND	GND	GND
6	DSR, Data Set Ready	N/A	N/A
7	RTS, Request To Send	N/A	N/A
8	CTS, Clear To Send	N/A	N/A
9	No Power/5V/12V	N/A	N/A

38 : Top : USB 2.0 Ports (USB11_12)

Bottom : PS/2 Keyboard/Mouse Port