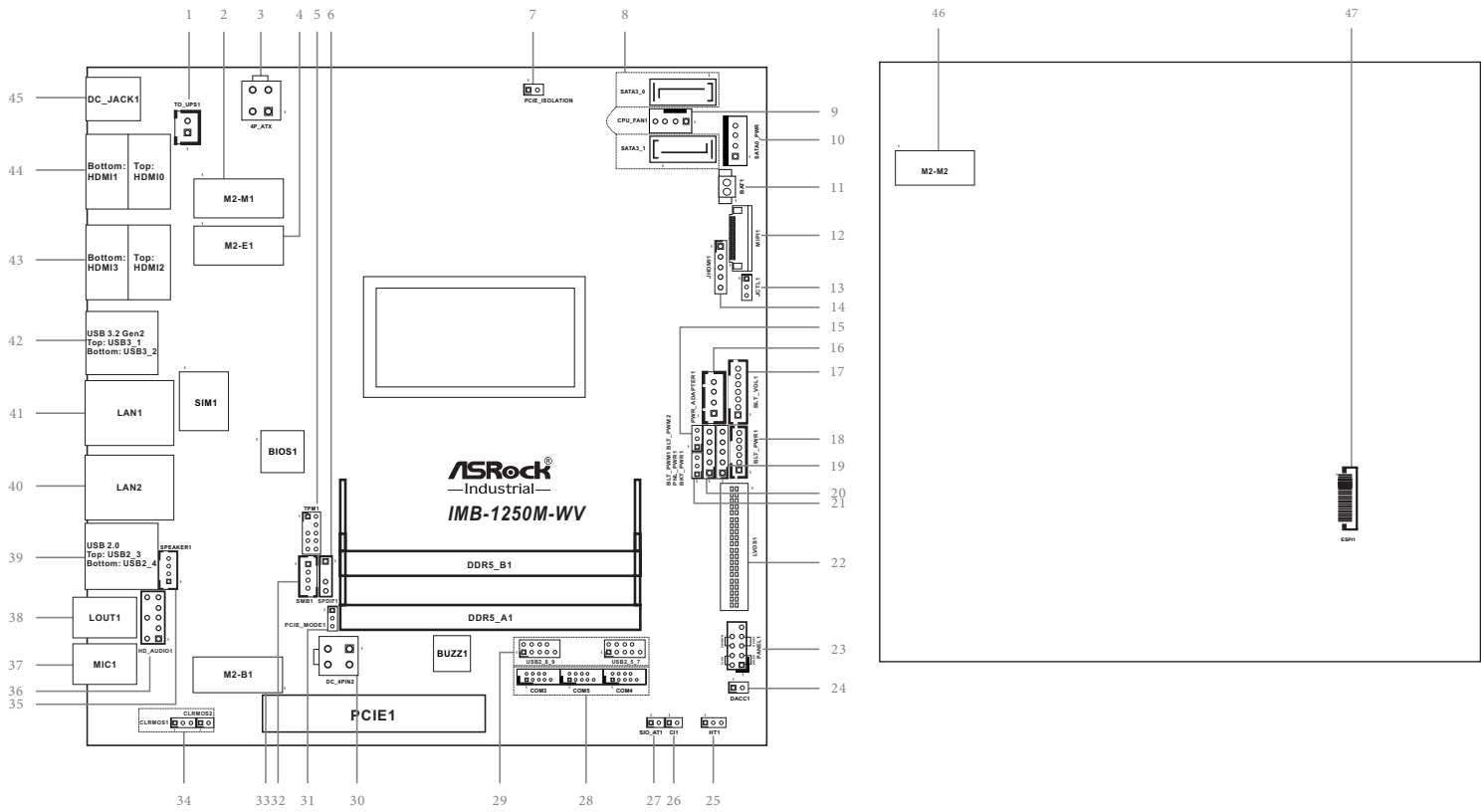
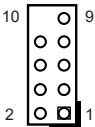




23 : System Panel Header (PANEL1)

Pin	Signal Name	Signal Name	Pin
1	HDLED+	PLED+	2
3	HDLED-	PLED-	4
5	GND	PWRBTN#	6
7	RESET#	GND	8
9	GND		10



24 : DACC1

Open: no ACC
Short: ACC (Default)

* Auto clear CMOS when system boot improperly.



25 : Heater header (HT1)

Pin	Signal Name
1	Heater_PWR (5V/1A)
2	GND
3	NTC (Negative Temperature Coefficient) thermistors



* The 10k Ohm NTC thermistors is suggested.

* Deep mode is not supported when the preheat function is enabled.

26 : Chassis Intrusion Header (CI1)

Open : Normal (Default)
Short : Active Case Open



27 : ATX/AT Mode Jumper (SIO_AT1)

Open: ATX Mode (Default)
Short: AT Mode



28 : COM Port Headers (COM3~5)
(RS232/422/485) *



* This motherboard supports RS232/422/485 on COM3, 4 and 5 ports. Please refer to the table below for the pin definition. In addition, COM3, 4 and 5 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM3, 4 and 5 Ports Pin Definition

Pin	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	TX+	RTX+
3	TXD	RX+	NA
4	DTR	RX-	NA
5	GND	GND	GND
6	DSR	NA	NA
7	RTS	NA	NA
8	CTS	NA	NA
9	PWR	PWR	PWR

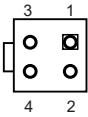
29 : USB 2.0 Headers (USB2_5_7, USB2_8_9)

Pin	Signal Name	Signal Name	Pin
1	USB_PWR	USB_PWR	2
3	P-	P-	4
5	P+	P+	6
7	GND	GND	8
9	DUMMY		10



30 : 4-pin ATX PWR Connector (DC_4PIN2)

Pin	Signal Name
1	GND
2	GND
3	PCIE_EXT +12V
4	PCIE_EXT +12V



* Only use with VGA-PWR card

31 : PCIE_PWR_MODE (PCIE_MODE1)

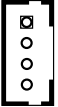
1-2: LOW_PWR_MODE (Default)
2-3: HIGH_PWR_MODE

* Only set to HIGH_PWR_Mode (pin 2-3) when using VGA-PWR CARD..



32 : SMB1

Pin	Signal Name
1	GPP_D03
2	SMB_CLK_MAIIN
3	SMB_DATA_MAIN
4	GND



33 : M.2 Key-B Socket (M2_B1)

Pin	Signal Name	Signal Name	Pin
1	NA	+3.3V	2
3	GND	+3.3V	4
5	GND	FuLL_Card_Power_off	6
7	USB_D+	DISABLE1_N	8
9	USB_D-	NA	10
11	GND		
		NA	20
21	GND	NA	22
23	NA	NA	24
25	NA	DISABLE2_N	26
27	GND	NA	28
29	USB3_RX_N	UIM_RESET	30
31	USB3_RX_P	UIM_CLK	32
33	GND	UIM_DATA	34
35	USB3_TX_N	UIM_PWR	36
37	USB3_TX_P	NA	38
39	GND	NA	40
41	PERn0	NA	42
43	PERp0	NA	44
45	GND	NA	46
47	PETn0	NA	48
49	PETP0	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	NA	54
55	PEFCLKp	NA	56
57	GND	NA	58
59	NA	NA	60
61	NA	NA	62
63	NA	NA	64
65	NA	SIM_DETECT	66
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	NA		

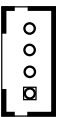
34 : Clear CMOS Headers

CLRMO1
1-2 : Normal (Default)
2-3 : Clear CMOS
CLRMO2
Open : Normal (Default)
Short : Auto Clear CMOS
When AC Power On



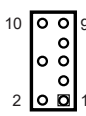
35 : 3W Audio AMP Output Wafer (SPEAKER1)

Pin	Signal Name
1	OUTLN
2	OUTLP
3	OUTRP
4	OUTRN

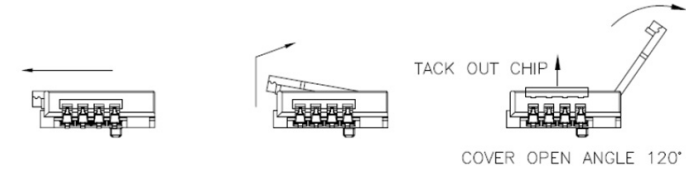
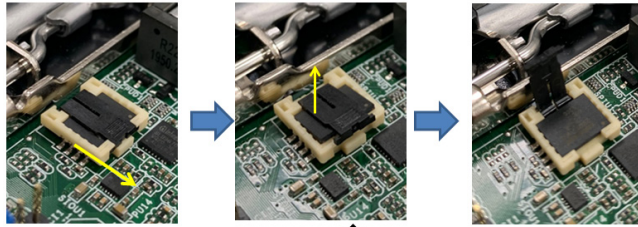


36 : Front Panel Audio Header (HD_AUDIO1)

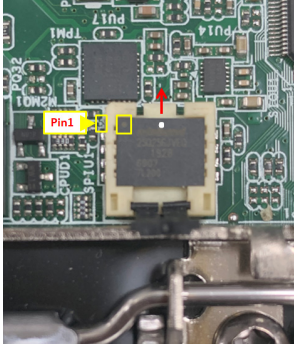
Pin	Signal Name	Signal Name	Pin
1	MIC2_L	GND	2
3	MIC2_R		4
5	OUT2_R	MIC_RET	6
7	J_SENSE		8
9	OUT2_L	OUT_RET	10



Installation of ROM Socket



- Do not apply force to the actuator cover after ic inserted.
- Do not apply force to actuator cover when it is opening over 120 degree, Otherwise, the actuator cover may be broken.



- The yellow dot (Pin1) on the ROM must be installed at pin1 position of the socket (white arrow area).
 - Make sure the white dot on the ROM is installed outwards of the socket.
 - For further details of how to install ROM, please refer to ASRI website.
- Warning: If the installation does not follow as the picture, then it may cause severe damage to chipset & MB.**

37 : Mic In (MIC1)

38 : Line Out (LOUT1)

39 : USB 2.0 Ports (USB2_3_4)

40 : RJ45 LAN Port (LAN2)

41 : RJ45 LAN Port (LAN1)

42 : USB 3.2 Gen2 Ports (USB3_1_2)

43 : HDMI Ports (HDMI2_3)

44 : HDMI Ports (HDMI0_1)

45 : DC_JACK1

Back Side :

46 : ESPI Header (ESPI1)

Pin	Signal Name
1	GND
2	ESPI_CLK
3	GND
4	ESPI_CS0#
5	ESPI_RESET#
6	GND
7	+3V
8	ESPI_CS1#
9	SMBus_CLK
10	SMBus_DATA
11	ESPI_IO0
12	ESPI_IO1
13	ESPI_IO2
14	ESPI_IO3
15	ESPI_ALERT1#
16	+3VSB
17	Internal Use
18	+5VSB
19	ESPI_ALERT0#
20	GND



47 : M.2 Key-M Socket (M2_M2)

Pin	Signal Name	Signal Name	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NA	6
7	PERp3	NA	8
9	GND	LED#	10
11	PETn3	+3.3V	12
13	PETp3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	NA	38
39	GND	NA	40
41	PERn0	NA	42
43	PERp0	NA	44
45	GND	NA	46
47	PETn0	NA	48
49	PETP0	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	NA	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		