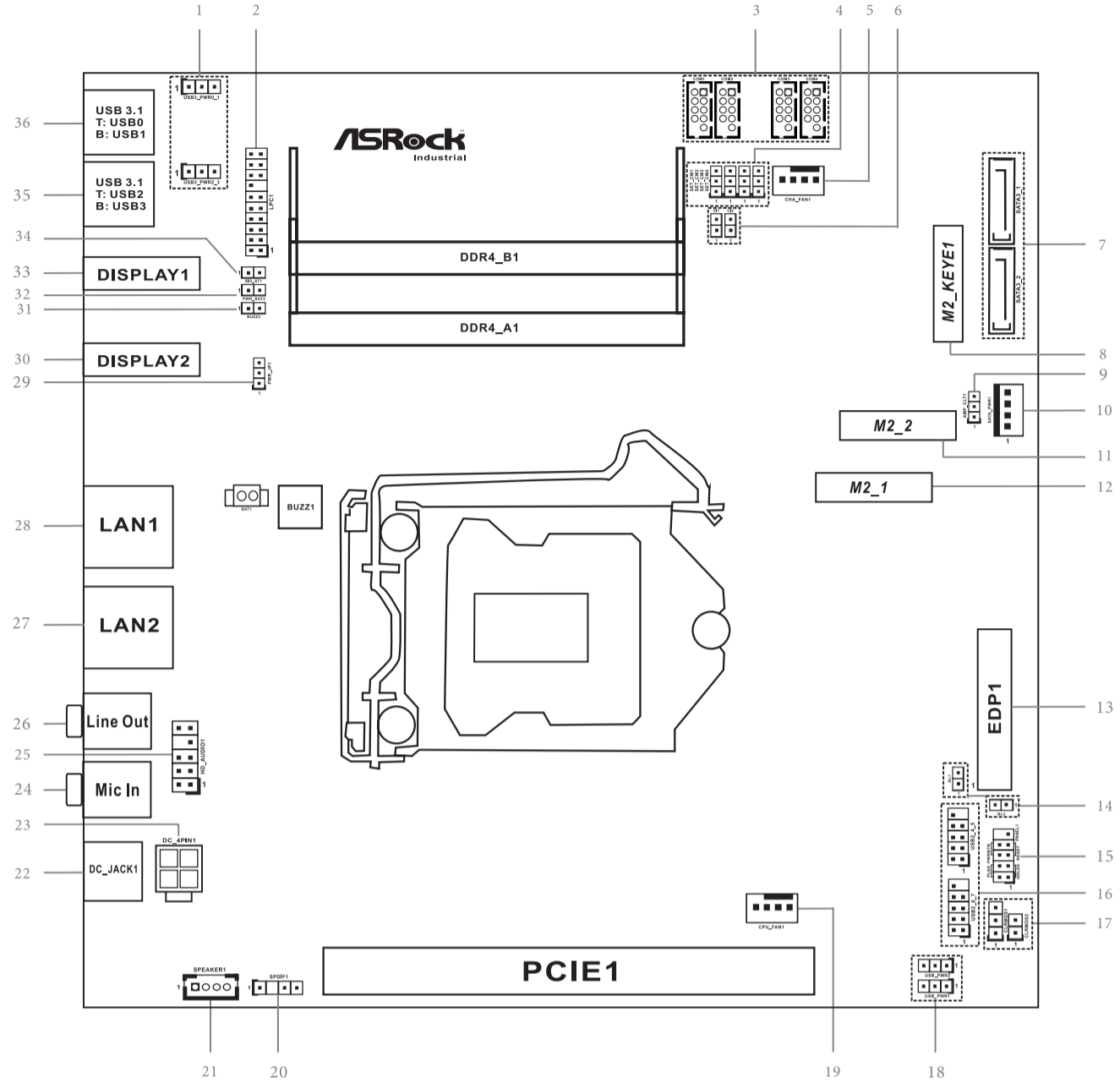


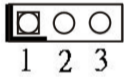


Jumpers and Headers Setting Guide

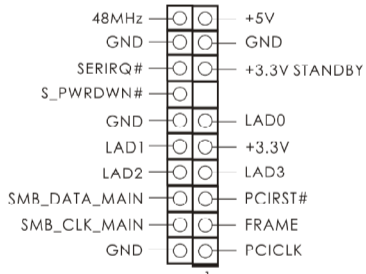


1 : USB Power Setting Jumpers
USB3_PWR0_1 (For USB3_0_1)
USB3_PWR2_3 (For USB3_2_3)

1-2: +5V
2-3: +5VSB



2 : LPC Header



3 : COM Port Headers (COM1, 2, 3, 4)

PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	DDCD#	3	TTXD	5	GND	7	RRTS#	9	PWR
2	RRXD	4	DDTR#	6	DDSR#	8	CCTS#		

* This motherboard supports RS232/422/485 on COM3 port.
Please refer to below table for the pin definition. In addition, COM3 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

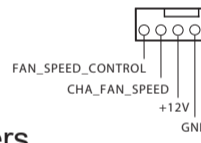
COM3 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	PWR	PWR	PWR

4 : COM Port PWR Setting Jumpers
(SET_CM1, 2, 3, 4)
1-2: +5V
2-3: +12V



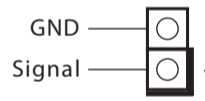
5 : Chassis FAN Connector (+12V)



6 : Chassis Intrusion Headers

CI1 :
Close: Active Case Open
Open: Normal

CI2 :
Close: Normal
Open: Active Case Open

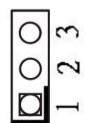


7 : SATA3 Connectors (SATA3_1, SATA3_2)

8 : M.2 Socket (Key-E) (With CNV1)

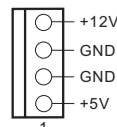
Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_RF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODERN_CLKREQ	14
15	CNV_WGR_D0-	NA	16
17	CNV_WGR_D0+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BRI_RST	22
23	CNV_WGR_CLK+		
33	GND	CNV_BGI_DT	32
35	PETp	CNV_RIG_RSP	34
37	PETn	CNV_BRI_DT	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFLKp	NA	46
49	PEFLKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST#	52
55	WAKE#	W_DISABLE#	54
57	GND	W_DISABLE#	56
59	CNV_WT_D1-	SMB_DATA	58
61	CNV_WT_D1+	SMB_CLK	60
63	GND	NA	62
65	CNV_WT_D0-	CLKIN_XTAL_LCP	64
67	CNV_WT_D0+	NA	66
69	GND	NA	68
71	CNV_WT_CLK-	NA	70
73	CNV_WT_CLK+	+3.3V	72
75	GND	+3.3V	74

9 : AMP_CTL1



PIN	Signal Name
1	GPIO_VOL_DW
2	GND
3	GPIO_VOL_UP

10 : SATA Power Output Connector



11 : M.2 Socket (Key-M) (M2_2)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NA	6
7	PERp3	NA	8
9	GND	SATA_LED	10
11	PETn3	+3.3V	12
13	PETp3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	DEVSLP	38
39	GND	SMB_CLK	40
41	PERn0/SATA-B+	SMB_DATA	42
43	PERp0/SATA-B-	NA	44
45	GND	NA	46
47	PETn0/SATA-A+	NA	48
49	PETp0/SATA-A-	PERST#	50
51	GND	CLKREQ#	52
53	PEFLKn	WAKE#	54
55	PEFLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND	+3.3V	74

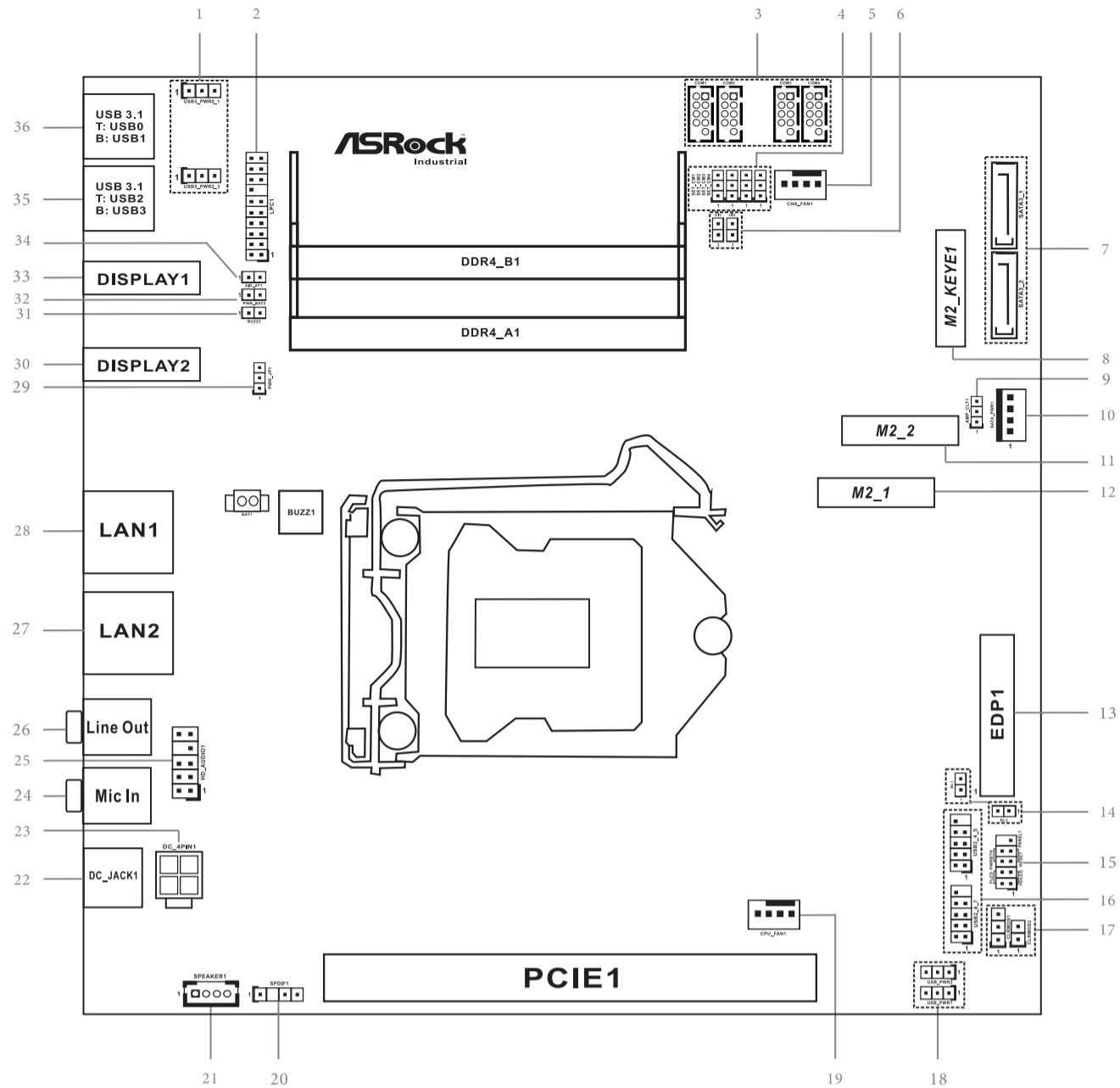
12 : M.2 Socket (Key-M) (M2_1)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NA	6
7	PERp3	NA	8
9	GND	SATA_LED	10
11	PETn3	+3.3V	12
13	PETp3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	DEVSLP	38
39	GND	SMB_CLK	40
41	PERn0/SATA-B+	SMB_DATA	42
43	PERp0/SATA-B-	NA	44
45	GND	NA	46
47	PETn0/SATA-A+	NA	48
49	PETp0/SATA-A-	PERST#	50
51	GND	CLKREQ#	52
53	PEFLKn	WAKE#	54
55	PEFLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND	+3.3V	74

13 : eDP Connector



PIN	Signal Name
1	NA
2	GND
3	eDP_TX#3_CON
4	eDP_TX3_CON
5	GND
6	eDP_TX#2_CON
7	eDP_TX2_CON
8	GND
9	eDP_TX#1_CON
10	eDP_TX1_CON
11	GND
12	eDP_TX#0_CON
13	eDP_TX0_CON
14	GND
15	eDP_AUX#_CON
16	eDP_AUX#_CON
17	GND
18	LCD_VCC
19	LCD_VCC
20	LCD_VCC
21	LCD_VCC
22	NA
23	GND
24	GND
25	GND
26	GND
27	eDP_HPD_CON
28	GND
29	GND
30	GND
31	GND
32	CON_LBKL_T_EN
33	CON_LBKL_T_CTL
34	SMB_DATA
35	SMB_CLK
36	LCD_BLT_VCC
37	LCD_BLT_VCC
38	LCD_BLT_VCC
39	LCD_BLT_VCC
40	NA



14 : BL1

Open: Protect LCD_BLT_VCC
 Short: No Protect LCD_BLT_VCC
 BL2
 Open: Protect LCD_VCC
 Short: No Protect LCD_VCC



21 : 3W Audio AMP Output Wafer



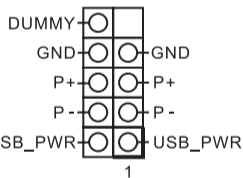
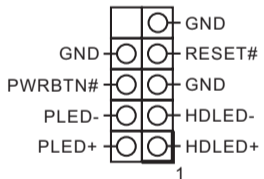
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	OUTLN	2	OUTLP	3	OUTRP	4	OUTRN

34 : SIO_AT1
 Open: ATX Mode
 Short: AT Mode



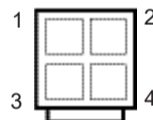
USB3.1 Ports
 35 : USB3_2_3
 36 : USB3_0_1

15 : System Panel Header



16 : USB2.0 Headers
 (USB2_4_5, USB2_6_7)

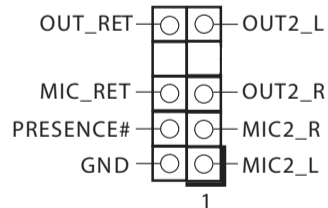
23 : 4-pin ATX PWR Connector
 (Input +19V)
 1-2: GND
 3-4: DC Input



Audio Jacks

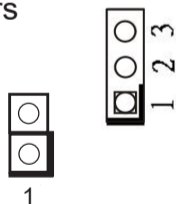
24 : Pink - Mic In
 26 : Green - Line Out

25 : Front Panel Audio Header



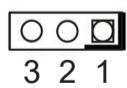
17 : Clear CMOS Headers

CLRMO1 :
 1-2: Normal
 2-3: Clear CMOS
 CLRMO2 :
 Open: Normal
 Short: Auto Clear CMOS (Power Off)



18 : USB Power Setting Jumpers
 USB_PWR1 (For USB2_4_5)
 USB_PWR2 (For USB2_6_7)

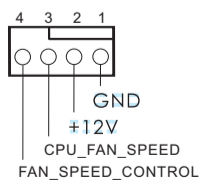
1-2: +5V
 2-3: +5VSB



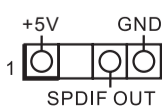
RJ45 LAN Ports
 27 : LAN2
 28 : LAN1

DisplayPorts
 30 : DISPLAY2
 33 : DISPLAY1

19 : CPU FAN Connector
 (+12V)



20 : SPDIF Header



29 : ATX/AT Mode Jumper (PWR_JP1)
 1-2: AT Mode
 2-3: ATX Mode



31 : Buzzer



32 : PWR_BAT2
 Open: Normal
 Short: Charge Battery

