



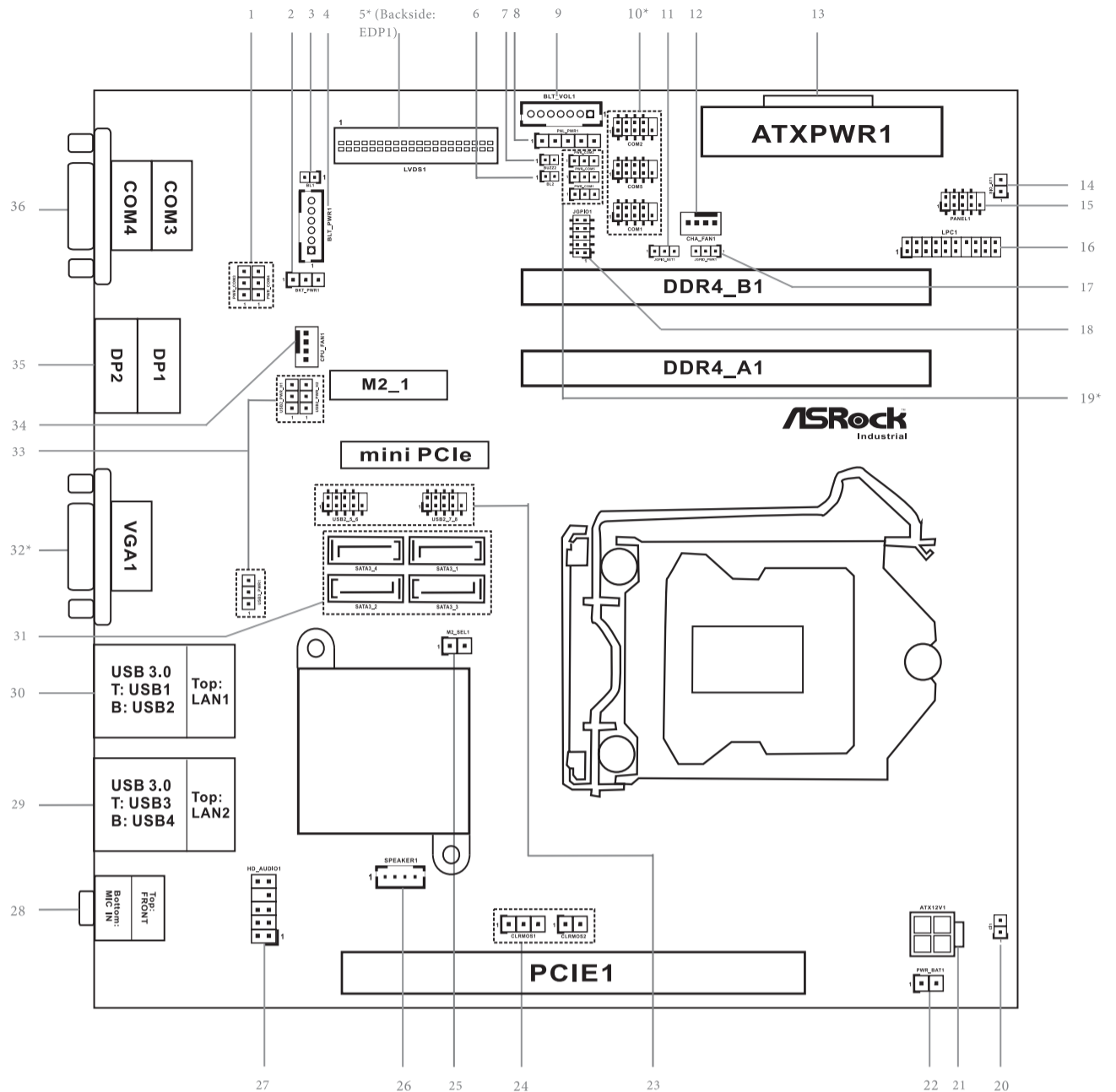
ASRock Jumpers and Headers Setting Guide

IMB-1211

IMB-1210

Revision History

Date	Description
May 15, 2020	First Release



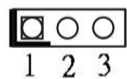
1 : COM Port PWR Setting Jumpers
PWR_COM3 (For COM Port3)
PWR_COM4 (For COM Port4)



1-2 : +5V
2-3 : +12V

2 : Backlight Power Select
(LCD_BLT_VCC) (BKT_PWR1)

1-2 : LCD_BLT_VCC : +5V
2-3 : LCD_BLT_VCC : +12V



3 : BL1

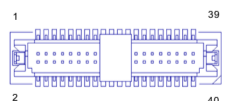
Open : Protect LCD_BLT_VCC
Short : No Protect LCD_BLT_VCC

4 : Inverter Power Control Wafer
(BLT_PWR1)



PIN	Signal Name
6	LCD_BLT_VCC
5	LCD_BLT_VCC
4	CON_LBKLT_EN
3	CON_LBKLT_CTL
2	GND
1	GND

5* : LVDS Panel Connector
(For IMB-1210-L / IMB-1211-L only)



PIN	Signal Name	PIN	Signal Name
1	LCD_VCC	18	LVDS_A_CLK#
2	LCD_VCC	19	LVDS_A_CLK
3	LDDC_CLK	20	LVDS_B_DATA0
4	LVDS_A_DATA0#	21	LVDS_B_DATA0#
5	LDDC_DATA	22	LVDS_B_DATA1
6	LVDS_A_DATA1#	23	LVDS_B_DATA1#
7	PD (Panel Detection)	24	LVDS_B_DATA2
8	LVDS_A_DATA1	25	LVDS_B_DATA2#
9	LVDS_A_DATA2#	26	LVDS_B_DATA3
10	LVDS_A_DATA2	27	LVDS_B_DATA3#
11	GND	28	DPLVDD_EN
12	LVDS_A_DATA3#	29	LVDS_B_DATA3
13	LVDS_A_DATA3	30	LVDS_B_DATA3#
14	LVDS_A_CLK#	31	LVDS_B_CLK#
15	LVDS_A_CLK	32	LVDS_B_CLK
16	LVDS_B_DATA0	33	CON_LBKLT_EN
17	LVDS_B_DATA0#	34	CON_LBKLT_CTL
18	LVDS_B_DATA1	35	GND
19	LVDS_B_DATA1#	36	LCD_BLT_VCC
20	LVDS_B_DATA2	37	LCD_BLT_VCC
21	LVDS_B_DATA2#	38	LCD_BLT_VCC
22	LVDS_B_DATA3	39	LCD_BLT_VCC
23	LVDS_B_DATA3#		
24	LVDS_B_DATA4		
25	LVDS_B_DATA4#		
26	LVDS_B_DATA5		
27	LVDS_B_DATA5#		
28	DPLVDD_EN		
29	LVDS_B_DATA3		
30	LVDS_B_DATA3#		
31	LVDS_B_CLK#		
32	LVDS_B_CLK		
33	CON_LBKLT_EN		
34	CON_LBKLT_CTL		
35	GND		
36	LCD_BLT_VCC		
37	LCD_BLT_VCC		
38	LCD_BLT_VCC		
39	LCD_BLT_VCC		

* eDP Connector (For IMB-1210-D / IMB-1211-D only)
(on the Backside of PCB)



PIN	Signal Name
1	NA
2	NA
3	NA
4	EDP_TX0_CON
5	EDP_TX0_CON
6	EDP_TX0_CON
7	EDP_TX0_CON
8	EDP_TX0_CON
9	EDP_TX0_CON
10	EDP_TX0_CON
11	EDP_TX0_CON
12	EDP_TX0_CON
13	EDP_TX0_CON
14	EDP_TX0_CON
15	EDP_TX0_CON
16	EDP_TX0_CON
17	EDP_TX0_CON
18	EDP_TX0_CON
19	EDP_TX0_CON
20	EDP_TX0_CON
21	EDP_TX0_CON
22	EDP_TX0_CON
23	EDP_TX0_CON
24	EDP_TX0_CON
25	EDP_TX0_CON
26	EDP_TX0_CON
27	EDP_TX0_CON
28	EDP_TX0_CON
29	EDP_TX0_CON
30	EDP_TX0_CON
31	EDP_TX0_CON
32	EDP_TX0_CON
33	EDP_TX0_CON
34	EDP_TX0_CON
35	EDP_TX0_CON
36	EDP_TX0_CON
37	EDP_TX0_CON
38	EDP_TX0_CON
39	EDP_TX0_CON
40	EDP_TX0_CON

6 : BL2
Open : Protect LCD_VCC
Short : No Protect LCD_VCC

7 : Buzzer

8 : Panel Power Select (LCD_VCC)
(PNL_PWR1)
1-2 : LCD_VCC : +3V
2-3 : LCD_VCC : +5V
4-5 : LCD_VCC : +12V

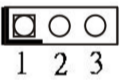
9 : Backlight Volume Control (BLT_VOL1)

PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name		
7	GND	6	GND	5	BLT_DW	4	BLT_UP	3	PWRDN	2	GPIO_VOL_DW	1	GPIO_VOL_UP

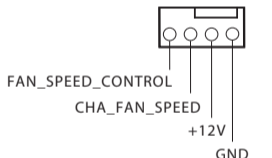
10* : COM Port Headers (COM1, 2, 5)
(COM5 is for IMB-1211-D / IMB-1211-L only)
(COM1, COM2, COM5 support RS232 only)

PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
2	RRXD#	4	DDTR#	6	DDSR#	8	CCTS#	10	N/A
1	DDCD#	3	TTXD	5	GND	7	RRTS#	9	PWR

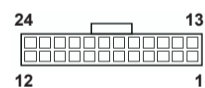
11 : Digital Input / Output Default Value Setting (JGPIO_SET1)
1-2 : Pull-High
2-3 : Pull-Low



12 : 4-Pin Chassis FAN Connector (+12V)



13 : 24-pin ATX Power Input Connector

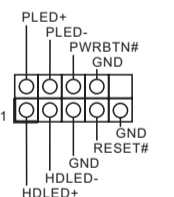


14 : ATX/AT Mode Jumper (SIO_AT1)

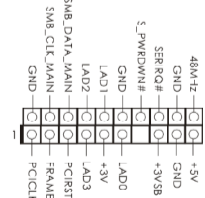
Open : ATX Mode
Short : AT Mode



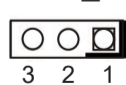
15 : System Panel Header



16 : LPC Header

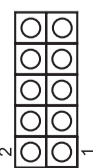


17 : Digital Input / Output Power Select (JGPIO_PWR)
1-2 : +12V
2-3 : +5V

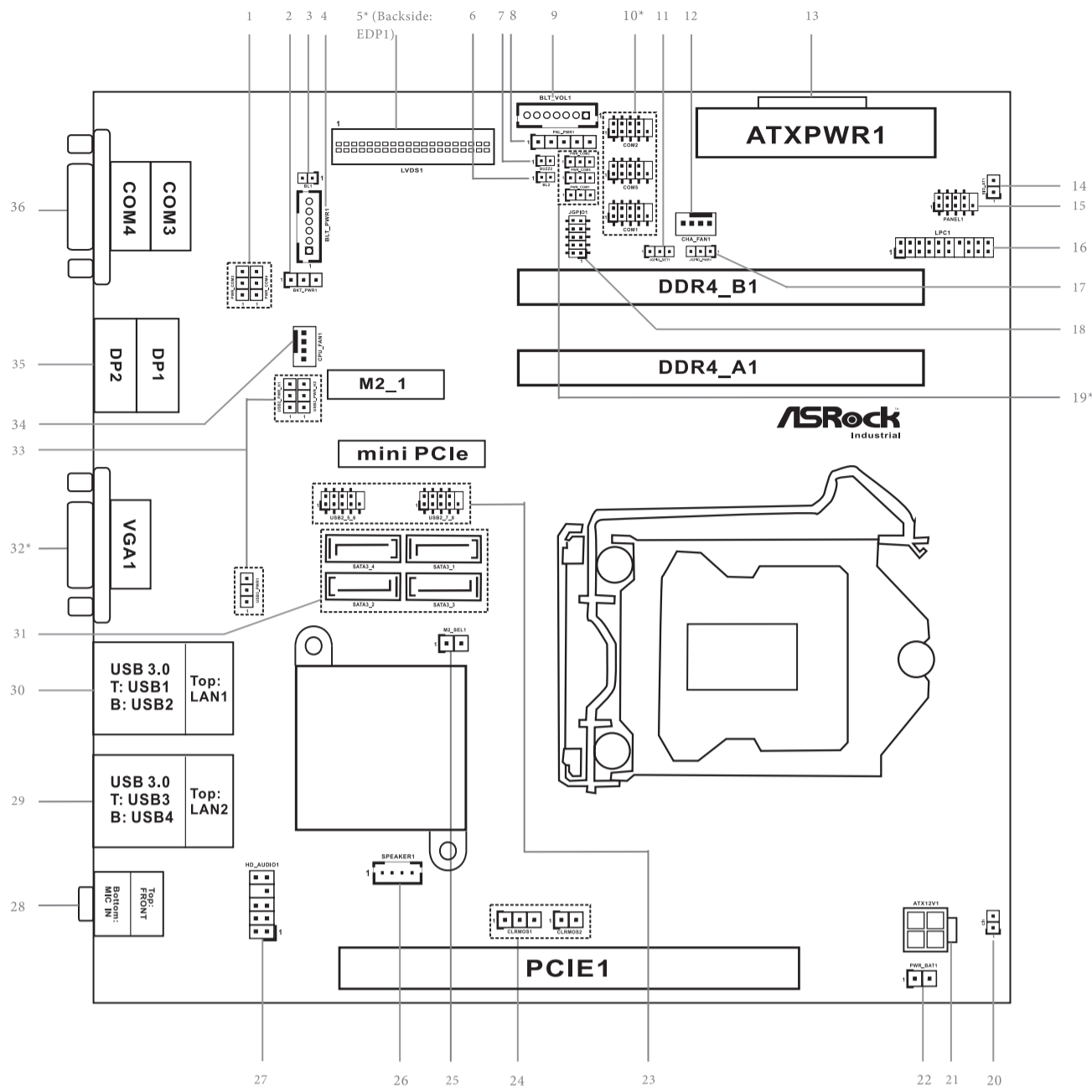


18 : Digital Input/Output Pin Header (JGPIO1)

PIN	Signal Name	PIN	Signal Name
10	GND	9	JGPIO_PWR
8	SIO_GP23	7	SIO_GP27
6	SIO_GP22	5	SIO_GP26
4	SIO_GP21	3	SIO_GP25
2	SIO_GP20	1	SIO_GP24



* PD (Panel Detection): Connect this pin to LVDS Panel's Ground pin to detect Panel detection.



M.2 Key-E Socket:

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_BF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODEM_CLKREQ	14
15	CNV_WGR_D0-	NA	16
17	CNV_WGR_D0+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BRI_RSP	22
23	CNV_WGR_CLK+		
33	GND	CNV_BGI_DT	32
35	PETp	CNV_RGI_RSP	34
37	PETn	CNV_BRI_DT	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFCLKp	NA	46
49	PEFCLKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST#	52
55	WAKE#	W_DISABLE#	54
57	GND	W_DISABLE2#	56
59	CNV_WT_D1-	SMB_DATA	58
61	CNV_WT_D1+	SMB_CLK	60
63	GND	NA	62
65	CNV_WT_D0-	CLKIN_XTAL_LCP	64
67	CNV_WT_D0+	NA	66
69	GND	NA	68
71	CNV_WT_CLK-	NA	70
73	CNV_WT_CLK+	+3.3V	72
75	GND	+3.3V	74

M.2 Key-M Socket:

SATA Mode:

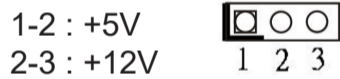
Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	NA	NA	6
7	NA	NA	8
9	GND	SATA_LED	10
11	NA	+3.3V	12
13	NA	+3.3V	14
15	GND	+3.3V	16
17	NA	+3.3V	18
19	NA	NA	20
21	GND	NA	22
23	NA	NA	24
25	NA	NA	26
27	GND	NA	28
29	NA	NA	30
31	NA	NA	32
33	GND	NA	34
35	NA	NA	36
37	NA	DEVSLP	38
39	GND	SMB_CLK	40
41	SATA-B+	SMB_DATA	42
43	SATA-B-	NA	44
45	GND	NA	46
47	SATA-A-	NA	48
49	SATA-A+	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND	+3.3V	74

PCIe Mode:

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NA	6
7	PERB3	NA	8
9	GND	SATA_LED	10
11	PETn3	+3.3V	12
13	PETp3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	DEVSLP	38
39	GND	SMB_CLK	40
41	PERn0/SATA-B+	SMB_DATA	42
43	PERp0/SATA-B-	NA	44
45	GND	NA	46
47	PETn0/SATA-A-	NA	48
49	PETp0/SATA-A+	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND	+3.3V	74

IMB-1211-D: Switch between SATA and PCIe mode.
IMB-1210-L: SATA mode only.

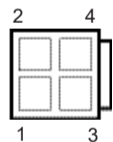
19* : COM Port PWR Setting Jumpers
(PWR_COM5 is for IMB-1211-D / IMB-1211-L only)
PWR_COM1 (For COM Port1)
PWR_COM2 (For COM Port2)
PWR_COM5 (For COM Port5)



20 : Chassis Intrusion Header (C11)
Close : Active Case Open
Open : Normal



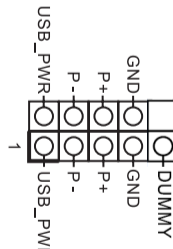
21 : 4-pin ATX Power Input/Output Connector



22 : PWR_BAT1
Open : Normal
Short : Charge Battery



23 : USB2.0 Headers
(USB2_5_6, USB2_7_8)



24 : Clear CMOS Headers
CLRMOSS1 :
1-2 : Normal
2-3 : Clear CMOS
CLRMOSS2 :
Open : Normal
Short : Auto Clear CMOS (Power Off)



25 : M.2 Select (M2_SEL1)
(For IMB-1210-D / IMB-1210-L only)
Open : For SATA3_1
Short : For M.2



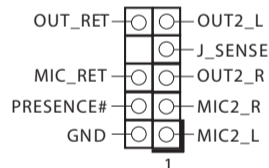
* M2_2 and SATA3_1 share lanes. If either one of them is in use, the other one will be disabled.

26 : 3W Audio AMP Output Wafer



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	OUTLN	2	OUTLP	3	OUTRP	4	OUTRN

27 : Front Panel Audio Header



28 : Audio Output
Green - Line Out
Pink - Mic In

29 : Top : RJ45 LAN Port (LAN2)
Bottom : USB3.0 Ports (USB3_3_4)
(Only +5VSB)

30 : Top : RJ45 LAN Port (LAN1)
Bottom : USB3.0 Ports (USB3_1_2)

31 : SATA3 Connectors (SATA3_1~4)

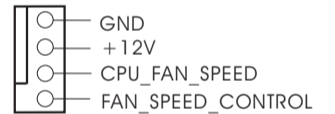


32* : D-Sub Port (VGA1) (For IMB-1211-L / IMB-1210-L only)
DisplayPort (DP3) (For IMB-1211-D / IMB-1210-D only)

33 : USB Power Setting Jumpers
(USB2_PWR_H1 (For USB2_5_6))
(USB2_PWR_H2 (For USB2_7_8))
(USB3_PWR1 (For USB3_1_2))



34 : 4-Pin CPU FAN Connector (+12V) (CPU_FAN1)



35 : Top : DisplayPort (DP1)
Bottom : DisplayPort (DP2)

36 : IMB-1210-L / IMB-1210-D :
Top : COM Port (COM3) (RS232/422/485)*
Bottom : COM Port (COM4) (RS232)
IMB-1211-D / IMB-1211-L :
Top : COM Port (COM3) (RS232/422/485)*
Bottom : COM Port (COM4) (RS232/422/485)*

* Please refer to below table for the pin definition. In addition, they can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	+5V / +12V	N/A	N/A

Back Side :

M.2 Socket (Key-M) (M2_2) :

* M2_2 supports either PCIe or SATA mode for IMB-1211-D. M2_2 supports SATA mode only for IMB-1210-L.

* M2_2 and SATA3_1 share lanes. If either one of them is in use, the other one will be disabled.