



P/ N: 15G06M093000AK V1.0

ASRock
—Industrial—

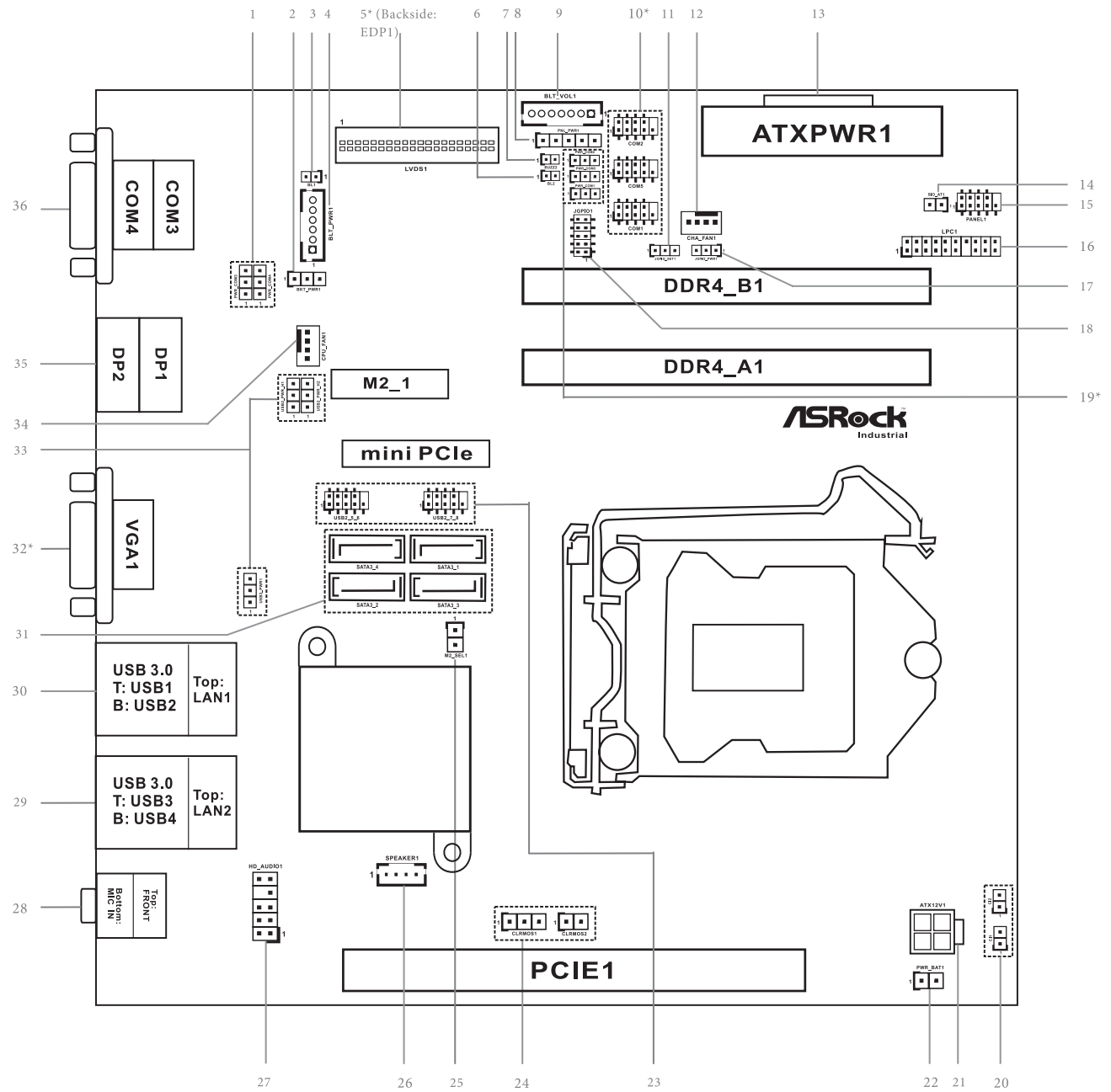
Jumpers and Headers Setting Guide

IMB-1210-L R2

IMB-1210-D R2

IMB-1211-L R2

IMB-1211-D R2

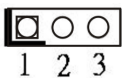


1 : COM Port PWR Setting Jumpers
PWR_COM3 (For COM Port3)
PWR_COM4 (For COM Port4)



1-2 : +5V
2-3 : +12V

2 : Backlight Power Select
(LCD_BLT_VCC) (BKT_PWR1)
1-2 : LCD_BLT_VCC : +5V
2-3 : LCD_BLT_VCC : +12V



3 : BL1
Open : Protect LCD_BLT_VCC
Short : No Protect LCD_BLT_VCC

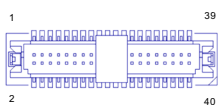


4 : Inverter Power Control Wafer
(BLT_PWR1)



PIN	Signal Name
6	LCD_BLT_VCC
5	LCD_BLT_VCC
4	CON_LBKLT_EN
3	CON_LBKLT_CTL
2	GND
1	GND

5* : LVDS Panel Connector
(For IMB-1210-L R2 / IMB-1211-L R2 only)



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
2	LCD_VCC	1	LCD_VCC	5	LDDC_DATA	7	LVD3_A_DATA0
4	LDDC_CLK	3	+3.3V	9	LVD3_A_DATA1#	11	GND
6	LVD3_A_DATA0#	5	LDDC_DATA	13	LVD3_A_DATA2	15	LVD3_A_DATA3#
8	PD (Panel Detection)	7	LVD3_A_DATA0	17	GND	19	LVD3_A_CLK
10	LVD3_A_DATA1	9	LVD3_A_DATA1#	21	LVD3_B_DATA0#	23	GND
12	LVD3_A_DATA2#	11	GND	25	LVD3_B_DATA1	27	LVD3_B_DATA2#
14	GND	13	LVD3_A_DATA2	29	DPLVDD_EN	31	LVD3_B_DATA3
16	LVD3_A_DATA3	15	LVD3_A_DATA3#	33	LVD3_B_CLK#	35	GND
18	LVD3_A_CLK#	17	GND	37	CON_LBKLT_CTL	39	LCD_BLT_VCC
20	GND	19	LVD3_A_CLK	40	LCD_BLT_VCC	38	LCD_BLT_VCC
22	LVD3_B_DATA0	21	LVD3_B_DATA0#				
24	LVD3_B_DATA1#	23	GND				
26	GND	25	LVD3_B_DATA1				
28	LVD3_B_DATA2	27	LVD3_B_DATA2#				
30	LVD3_B_DATA3#	29	DPLVDD_EN				
32	GND	31	LVD3_B_DATA3				
34	LVD3_B_CLK	33	LVD3_B_CLK#				
36	CON_LBKLT_EN	35	GND				
38	LCD_BLT_VCC	37	CON_LBKLT_CTL				
40	LCD_BLT_VCC	39	LCD_BLT_VCC				

* eDP Connector (For IMB-1210-D R2 / IMB-1211-D R2 only)
(on the Backside of PCB)



39	LCD_BLT_VCC	NA
40	LCD_BLT_VCC	NA
41	LCD_BLT_VCC	NA
42	LCD_BLT_VCC	NA
43	LCD_BLT_VCC	NA
44	LCD_BLT_VCC	NA
45	LCD_BLT_VCC	NA
46	LCD_BLT_VCC	NA
47	LCD_BLT_VCC	NA
48	LCD_BLT_VCC	NA
49	LCD_BLT_VCC	NA
50	LCD_BLT_VCC	NA
51	LCD_BLT_VCC	NA
52	LCD_BLT_VCC	NA
53	LCD_BLT_VCC	NA
54	LCD_BLT_VCC	NA
55	LCD_BLT_VCC	NA
56	LCD_BLT_VCC	NA
57	LCD_BLT_VCC	NA
58	LCD_BLT_VCC	NA
59	LCD_BLT_VCC	NA
60	LCD_BLT_VCC	NA
61	LCD_BLT_VCC	NA
62	LCD_BLT_VCC	NA
63	LCD_BLT_VCC	NA
64	LCD_BLT_VCC	NA
65	LCD_BLT_VCC	NA
66	LCD_BLT_VCC	NA
67	LCD_BLT_VCC	NA
68	LCD_BLT_VCC	NA
69	LCD_BLT_VCC	NA
70	LCD_BLT_VCC	NA
71	LCD_BLT_VCC	NA
72	LCD_BLT_VCC	NA
73	LCD_BLT_VCC	NA
74	LCD_BLT_VCC	NA
75	LCD_BLT_VCC	NA
76	LCD_BLT_VCC	NA
77	LCD_BLT_VCC	NA
78	LCD_BLT_VCC	NA
79	LCD_BLT_VCC	NA
80	LCD_BLT_VCC	NA

6 : BL2
Open : Protect LCD_VCC
Short : No Protect LCD_VCC



7 : Buzzer



8 : Panel Power Select (LCD_VCC)
(PNL_PWR1)
1-2 : LCD_VCC : +3V
2-3 : LCD_VCC : +5V
4-5 : LCD_VCC : +12V



9 : Backlight Volume Control (BLT_VOL1)



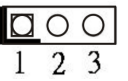
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
7	GND	6	GND	5	BLT_DW	4	BLT_UP	3	PWRDN
2	GND	1	GND						

10* : COM Port Headers (COM1, 2, 5)
(COM5 is for IMB-1211-D R2 / IMB-1211-L R2 only)
(COM1, COM2, COM5 support RS232 only)

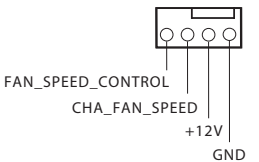


PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
2	RRXD#	4	DDTR#	6	DDSR#	8	CCTS#	10	N/A
1	DDCD#	3	TTXD	5	GND	7	RRTS#	9	PWR

11 : Digital Input / Output Default Value
Setting (JGPIO_SET1)
1-2 : Pull-High
2-3 : Pull-Low



12 : 4-Pin Chassis FAN
Connector (+12V)



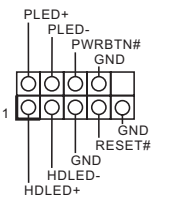
13 : 24-pin ATX Power Input Connector



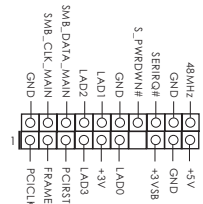
14 : ATX/AT Mode Jumper (SIO_AT1)
Open : ATX Mode
Short : AT Mode



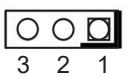
15 : System Panel Header



16 : LPC Header

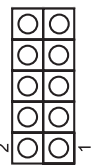


17 : Digital Input / Output Power Select
(JGPIO_PWR) (JGPIO_PWR1)
1-2 : +12V
2-3 : +5V

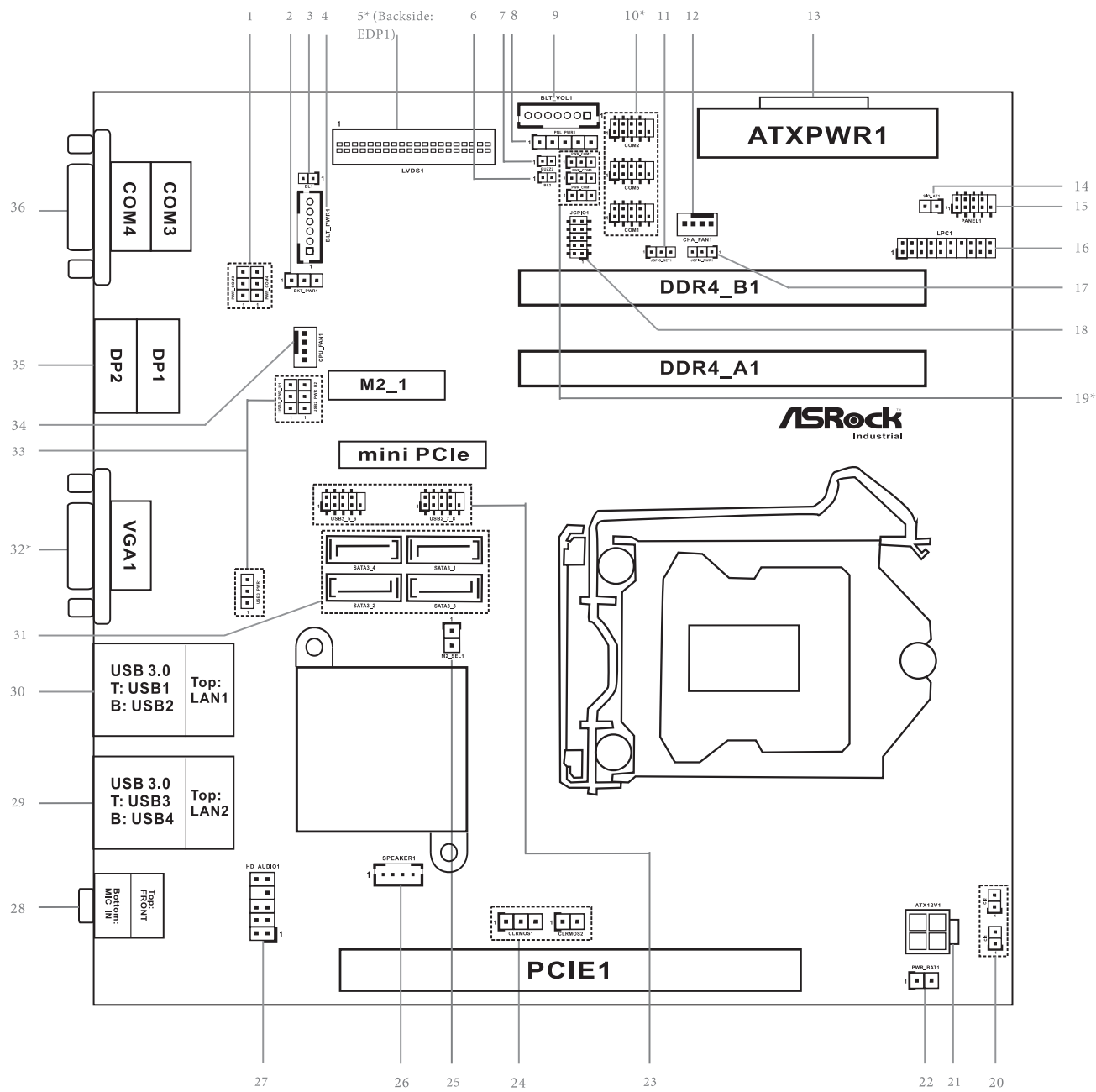


18 : Digital Input/Output Pin Header (JGPIO1)

PIN	Signal Name	PIN	Signal Name
10	GND	9	JGPIO_PWR
8	SIO_GP23	7	SIO_GP27
6	SIO_GP22	5	SIO_GP26
4	SIO_GP21	3	SIO_GP25
2	SIO_GP20	1	SIO_GP24



* PD (Panel Detection): Connect this pin to LVDS Panel's Ground pin to detect Panel detection.



M.2 Key-E Socket:

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_RF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODEM_CLKREQ	14
15	CNV_WGR_D0-	NA	16
17	CNV_WGR_D0+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BRI_RSP	22
23	CNV_WGR_CLK+		
33	GND	CNV_BG1_DT	32
35	PETp	CNV_RGI_RSP	34
37	PETn	CNV_BRI_DT	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFCLKp	NA	46
49	PEFCLKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST#	52
55	WAKE#	W_DISABLE1#	54
57	GND	W_DISABLE2#	56
59	CNV_WT_D1-	SMB_DATA	58
61	CNV_WT_D1+	SMB_CLK	60
63	GND	NA	62
65	CNV_WT_D0-	CLKIN_XTAL_LCP	64
67	CNV_WT_D0+	NA	66
69	GND	NA	68
71	CNV_WT_CLK-	NA	70
73	CNV_WT_CLK+	+3.3V	72
75	GND	+3.3V	74

M.2 Key-M Socket:

SATA Mode:

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	NA	NA	6
7	NA	NA	8
9	GND	SATA_LED	10
11	NA	+3.3V	12
13	NA	+3.3V	14
15	GND	+3.3V	16
17	NA	+3.3V	18
19	NA	NA	20
21	GND	NA	22
23	NA	NA	24
25	NA	NA	26
27	GND	NA	28
29	NA	NA	30
31	NA	NA	32
33	GND	NA	34
35	NA	NA	36
37	NA	DEVSIP	38
39	GND	SMB_CLK	40
41	SATA-B+	SMB_DATA	42
43	SATA-B-	NA	44
45	GND	NA	46
47	SATA-A-	NA	48
49	SATA-A+	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		

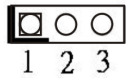
PCIe Mode:

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NA	6
7	PERn3	NA	8
9	GND	SATA_LED	10
11	PETn3	+3.3V	12
13	PETn3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	DEVSIP	38
39	GND	SMB_CLK	40
41	PERn0/SATA-B+	SMB_DATA	42
43	PERp0/SATA-B-	NA	44
45	GND	NA	46
47	PETn0/SATA-A-	NA	48
49	PETp0/SATA-A+	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		

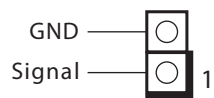
IMB-1211-D R2: Switch between SATA and PCIe mode.
IMB-1210-L R2: SATA mode only.

19* : COM Port PWR Setting Jumpers
(PWR_COM5 is for IMB-1211-D R2 / IMB-1211-L R2 only)
PWR_COM1 (For COM Port1)
PWR_COM2 (For COM Port2)
PWR_COM5 (For COM Port5)

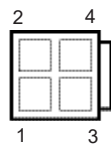
1-2 : +5V
2-3 : +12V



20 : Chassis Intrusion Headers (CI1, CI2)
CI1 :
Close : Active Case Open
Open : Normal
CI2 :
Close : Normal
Open : Active Case Open



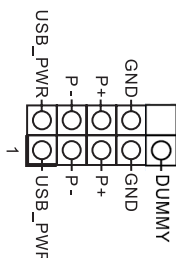
21 : 4-pin ATX Power Input/Output Connector



22 : PWR_BAT1
Open : Normal
Short : Charge Battery



23 : USB2.0 Headers
(USB2_5_6, USB2_7_8)



24 : Clear CMOS Headers
CLRMOS1 :
1-2 : Normal
2-3 : Clear CMOS
CLRMOS2 :
Open : Normal
Short : Auto Clear CMOS (Power Off)



25 : M.2 Select (M2_SEL1)
(For IMB-1210-D R2 / IMB-1210-L R2 only)
Open : For SATA3_1
Short : For M.2



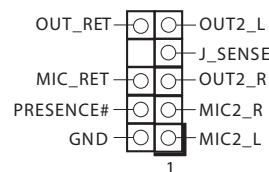
* M2_2 and SATA3_1 share lanes. If either one of them is in use, the other one will be disabled.

26 : 3W Audio AMP Output Wafer



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	OUTLN	2	OUTLP	3	OUTRP	4	OUTRN

27 : Front Panel Audio Header

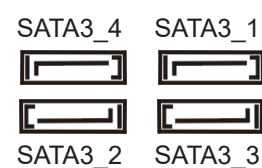


28 : Audio Output
Green - Line Out
Pink - Mic In

29 : Top : RJ45 LAN Port (LAN2)
Bottom : USB3.0 Ports (USB3_3_4)
(Only +5VSB)

30 : Top : RJ45 LAN Port (LAN1)
Bottom : USB3.0 Ports (USB3_1_2)

31 : SATA3 Connectors (SATA3_1~4)



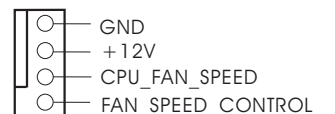
32* : D-Sub Port (VGA1) (For IMB-1211-L R2 / IMB-1210-L R2 only)
DisplayPort (DP3) (For IMB-1211-D R2 / IMB-1210-D R2 only)

33 : USB Power Setting Jumpers
(USB2_PWR_H1 (For USB2_5_6))
(USB2_PWR_H2 (For USB2_7_8))
(USB3_PWR1 (For USB3_1_2))

1-2 : +5V
2-3 : +5VSB



34 : 4-Pin CPU FAN Connector (+12V)
(CPU_FAN1)



35 : Top : DisplayPort (DP1)
Bottom : DisplayPort (DP2)

36 : IMB-1210-L R2 / IMB-1210-D R2 :
Top : COM Port (COM3) (RS232/422/485)*
Bottom : COM Port (COM4) (RS232)
IMB-1211-D R2 / IMB-1211-L R2 :
Top : COM Port (COM3) (RS232/422/485)*
Bottom : COM Port (COM4) (RS232/422/485)*

* Please refer to below table for the pin definition. In addition, they can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	+5V / +12V	N/A	N/A

Back Side :

M.2 Socket (Key-M) (M2_2) :

* M2_2 supports either PCIe or SATA mode for IMB-1211-D R2. M2_2 supports SATA mode only for IMB-1210-L R2.

* M2_2 and SATA3_1 share lanes. If either one of them is in use, the other one will be disabled.