

IMB-1236

User Manual

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This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) this device may not cause harmful interference, and
- (2) this device must accept any interference received, including interference that may cause undesired operation.

The terms HDMI* and HDMI High-Definition Multimedia Interface, and the HDMI logo are trademarks or registered trademarks of HDMI Licensing LLC in the United States and other countries.



WARNING

THIS PRODUCT CONTAINS A BUTTON BATTERY

If swallowed, a button battery can cause serious injury or death. Please keep batteries out of sight or reach of children.

CALIFORNIA, USA ONLY

The Lithium battery adopted on this motherboard contains Perchlorate, a toxic substance controlled in Perchlorate Best Management Practices (BMP) regulations passed by the California Legislature. When you discard the Lithium battery in California, USA, please follow the related regulations in advance.

"Perchlorate Material-special handling may apply, see www.dtsc.ca.gov/hazardouswaste/perchlorate"

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Our goods come with guarantees that cannot be excluded under the Australian Consumer Law. You are entitled to a replacement or refund for a major failure and compensation for any other reasonably foreseeable loss or damage caused by our goods. You are also entitled to have the goods repaired or replaced if the goods fail to be of acceptable quality and the failure does not amount to a major failure. If you require assistance please call ASRockInd Tel: +886-2-28965588 ext.123 (Standard International call charges apply)



ASRockInd follows the green design concept to design and manufacture our products, and makes sure that each stage of the product life cycle of ASRockInd product is in line with global environmental regulations. In addition, ASRockInd disclose the relevant information based on regulation requirements.



DO NOT throw the motherboard in municipal waste. This product has been designed to enable proper reuse of parts and recycling. This symbol of the crossed out wheeled bin indicates that the product (electrical and electronic equipment) should not be placed in municipal waste. Check local regulations for disposal of electronic products.

AWARNING

- INGESTION HAZARD: This product contains a button cell or coin battery.
- **DEATH** or serious injury can occur if ingested.
- A swallowed button cell or coin battery can cause Internal Chemical Burns in as little as 2 hours.
- KEEP new and used batteries OUT OF REACH of CHILDREN
- Seek immediate medical attention if a battery is suspected to be swallowed or inserted inside any part of the body.



- Remove and immediately recycle or dispose of used batteries according to local regulations and keep away from children. Do NOT dispose of batteries in household trash or incinerate.
- Even used batteries may cause severe injury or death.
- Call a local poison control center for treatment information.
- Battery type: CR2032
- Battery voltage: 3V
- Non-rechargeable batteries are not to be recharged.
- Do not force discharge, recharge, disassemble, heat above (manufacturer's specified temperature rating) or incinerate. Doing so may result in injury due to venting, leakage or explosion resulting in chemical burns.
- This product contains an irreplaceable battery.
- This icon indicates that a swallowed button battery can cause serious injury or death. Please keep batteries out of sight or reach of children.

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Chapter 1 Introduction

Thank you for purchasing ASRockInd *IMB-1236* motherboard, a reliable motherboard produced under ASRockInd's consistently stringent quality control. It delivers excellent performance with robust design conforming to ASRockInd's commitment to quality and endurance.

In this manual, chapter 1 and 2 contain introduction of the motherboard and step-by-step guide to the hardware installation. Chapter 3 contains the configuration guide to BIOS setup.



Because the motherboard specifications and the BIOS software might be updated, the content of this manual will be subject to change without notice. In case any modifications of this manual occur, the updated version will be available on ASRockInd website without further notice. You may find the latest CPU support lists on ASRockInd website as well.

ASRockInd website: https://www.asrockind.com/IMB-1236

If you require technical support related to this motherboard, please visit our website for specific information about the model you are using.

https://www.asrockind.com/technical-support

1.1 Package Contents

ASRockInd *IMB-1236* Motherboard (Mini-ITX (6.7-in x 6.7-in x 1.5-in, 17.0 cm x 17.0 cm x 3.8 cm)

ASRockInd IMB-1236 Jumper Setting Instruction

Gift Package:

- 1 x I/O Shield
- 1 x I/O Shield THIN
- 3 x SCREW M2*2, D=5
- 1 x SATA Power Cable
- 2 x COM Cable
- 2 x SATA Cable
- 1 x 2.54mm MINI JUMPER

Bulk Package:

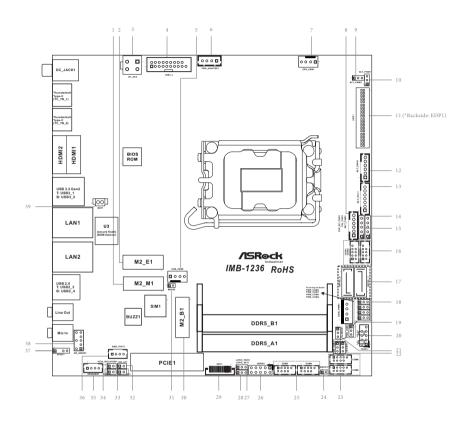
- 1 x I/O Shield
- 1 x I/O Shield THIN
- 3 x SCREW M2*2, D=5
- 1 x 2.54mm MINI JUMPER

1.2 Specifications

| Form Factor Dimensions (LxWxH) x 3.8 cm) | | | | |
|--|-------------|--------------|---|--|
| CPU | | Dimensions | Mini-ITX (6.7-in x 6.7-in x 1.5-in, 17.0 cm x 17.0 cm | |
| Processor System BIOS AMI SPI 256 Mbit Secure Flash AMI SPI 32Mbit (Optional) Technology Dual Channel DDR5 4800 MHz Capacity 64GB (32GB per DIMM) Socket 2 x 262-pin SO-DIMM Controller Intel® Iris® Xe Graphics Thomas Ami SPI 250 Mbit Socket 2 x 262-pin SO-DIMM Thomas Ami SPI 250 Mbit Socket 2 x 262-pin SO-DIMM Thomas Ami SPI 250 Mbit Ami Spi 250 Mbit Socket 2 x HDMI 2.0b Max resolution up to 4096x2160@60Hz EDP LVDS Dual channel 24 bit up to 1920 x 1200@60Hz EDP LAB Ami Spi 250 Mbit Ami Spi | Form Factor | (LxWxH) | x 3.8 cm) | |
| System BIOS AMI SPI 256 Mbit Secure Flash AMI SPI 32Mbit (Optional) | | CPU | Intel® Alder Lake-PS Core™ Processors, up to 45W | |
| Secure Flash AMI SPI 32Mbit (Optional) | Processor | Chipset | LGA 1700 | |
| Technology | System | BIOS | AMI SPI 256 Mbit | |
| Capacity 64GB (32GB per DIMM) Socket 2 x 262-pin SO-DIMM | | Secure Flash | AMI SPI 32Mbit (Optional) | |
| Socket 2 x 262-pin SO-DIMM | | Technology | Dual Channel DDR5 4800 MHz | |
| Controller Intel® Iris® Xe Graphics HDMI 2 x HDMI 2.0b Max resolution up to 4096x2160@60Hz IVDS Dual channel 24 bit up to 1920 x 1200@60Hz eDP .4b Max resolution up to 4096x2160@60Hz DisplayPort 2 x DisplayPort 1.4a Max resolution up to 4096x2160@60Hz MultiDisplay Quad display (Included 2 outputs from Type-C) PCIe 1 x PCIe x4 (Gen4) 1 x M.2 (Key B, 3042/3052) with PCIe Gen3 x1, USB 3.2 Gen1, USB 2.0 and SIM for 4G/5G 1 x M.2 (Key E, 2230) with PCIe Gen3 x1 and USB 2.0 SIM Socket 1 x Socket connected to M.2 Key B Interface Realtek ALC897 HD, High Definition Audio LAN1: Intel® 1226LM with 10/100/1000/2500 Mbps, supports vPro LAN2: Intel® 1210AT with 10/100/1000 Mbps Controller 2 x RJ-45 HDMI 2 x HDMI 2.0b Ethernet 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt®4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | Memory | Capacity | 64GB (32GB per DIMM) | |
| HDMI | | Socket | 2 x 262-pin SO-DIMM | |
| HDMI | | Controller | Intel® Iris® Xe Graphics | |
| Max resolution up to 4096x2160@60Hz | | HDM | 2 x HDMI 2.0b | |
| Graphics eDP eDP 1.4b Max resolution up to 4096x2160@60Hz DisplayPort MultiDisplay Quad display (Included 2 outputs from Type-C) PCIe 1 x PCIe x4 (Gen4) 1 x M.2 (Key B, 3042/3052) with PCIe Gen3 x1, USB 3.2 Gen1, USB 2.0 and SIM for 4G/5G 1 x M.2 (Key E, 2230) with PCIe Gen3 x1 and USB 2.0 SIM Socket 1 x Socket connected to M.2 Key B Audio Interface Realtek ALC897 HD, High Definition Audio LAN1: Intel* 1226LM with 10/100/1000/2500 Mbps, supports vPro LAN2: Intel* 1210AT with 10/100/1000 Mbps Controller Speed 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB 4.7 Hunderbolt* 4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | HDMI | Max resolution up to 4096x2160@60Hz | |
| Max resolution up to 4096x2160@60Hz | | LVDS | Dual channel 24 bit up to 1920 x 1200@60Hz | |
| Max resolution up to 4096x2160@60Hz DisplayPort | Graphics | -DD | eDP 1.4b | |
| Max resolution up to 4096x2160@60Hz MultiDisplay Quad display (Included 2 outputs from Type-C) PCIe 1 x PCIe x4 (Gen4) 1 x M.2 (Key B, 3042/3052) with PCIe Gen3 x1, USB 3.2 Gen1, USB 2.0 and SIM for 4G/5G 1 x M.2 (Key E, 2230) with PCIe Gen3 x1 and USB 2.0 SIM Socket 1 x Socket connected to M.2 Key B Audio Interface Realtek ALC897 HD, High Definition Audio LAN1: Intel* I226LM with 10/100/1000/2500 Mbps, supports vPro LAN2: Intel* I210AT with 10/100/1000 Mbps Controller 2 x RJ-45 HDMI 2 x HDMI 2.0b Ethernet 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB 3.2 Gen2 2 x USB 4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | eDP | Max resolution up to 4096x2160@60Hz | |
| MultiDisplay Quad display (Included 2 outputs from Type-C) PCIe 1 x PCIe x4 (Gen4) | | DieplayPort | 2 x DisplayPort 1.4a | |
| PCIe | | | | |
| Lambda L | | MultiDisplay | Quad display (Included 2 outputs from Type-C) | |
| USB 3.2 Gen1, USB 2.0 and SIM for 4G/5G 1 x M.2 (Key E, 2230) with PCIe Gen3 x1 and USB 2.0 SIM Socket 1 x Socket connected to M.2 Key B Audio Interface Realtek ALC897 HD, High Definition Audio LAN1: Intel® 1226LM with 10/100/1000/2500 Mbps, supports vPro LAN2: Intel® 1210AT with 10/100/1000 Mbps Controller 2 x RJ-45 HDMI 2 x HDMI 2.0b 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB 4/Thunderbolt® 4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | PCIe | ` ' | |
| Slot X M.2 (Key E, 2230) with PCIe Gen3 x1 and USB 2.0 SIM Socket 1 x Socket connected to M.2 Key B | | | 1 x M.2 (Key B, 3042/3052) with PCIe Gen3 x1, | |
| Slot 1 x M.2 (Key E, 2230) with PCIe Gen3 x1 and USB 2.0 SIM Socket 1 x Socket connected to M.2 Key B | Expansion | M 2 | USB 3.2 Gen1, USB 2.0 and SIM for 4G/5G | |
| SIM Socket 1 x Socket connected to M.2 Key B Audio Interface Realtek ALC897 HD, High Definition Audio Controller/ Speed LAN1: Intel® I226LM with 10/100/1000/2500 Mbps, supports vPro LAN2: Intel® I210AT with 10/100/1000 Mbps Controller 2 x RJ-45 HDMI 2 x HDMI 2.0b Ethernet 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | Slot | 101.2 | 1 x M.2 (Key E, 2230) with PCIe Gen3 x1 and | |
| Audio Interface Realtek ALC897 HD, High Definition Audio LAN1: Intel® I226LM with 10/100/1000/2500 Mbps, supports vPro LAN2: Intel® I210AT with 10/100/1000 Mbps Controller 2 x RJ-45 HDMI 2 x HDMI 2.0b Ethernet 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | | USB 2.0 | |
| Controller/ Speed LAN1: Intel® I226LM with 10/100/1000/2500 Mbps, supports vPro LAN2: Intel® I210AT with 10/100/1000 Mbps Controller 2 x RJ-45 HDMI 2 x HDMI 2.0b 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | | 1 x Socket connected to M.2 Key B | |
| Ethernet Speed Supports vPro | Audio | Interface | Realtek ALC897 HD, High Definition Audio | |
| Speed Supports vPro | | Controllor/ | LAN1: Intel® I226LM with 10/100/1000/2500 Mbps, | |
| Controller 2 x RJ-45 HDMI 2 x HDMI 2.0b Ethernet 1x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB 4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | Ethornot | | supports vPro | |
| HDMI 2 x HDMI 2.0b Ethernet 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | Ethernet | Speed | LAN2: Intel® I210AT with 10/100/1000 Mbps | |
| Rear I/O USB 1 x 1 Gigabit LAN 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt**4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | Controller | 2 x RJ-45 | |
| Rear I/O USB 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | HDMI | 2 x HDMI 2.0b | |
| Rear I/O USB 1 x 2.5 Gigabit LAN 2 x USB 2.0 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | Ethornot | 1 x 1 Gigabit LAN | |
| Rear I/O USB 2 x USB 3.2 Gen2 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | Ethernet | 1 x 2.5 Gigabit LAN | |
| Rear I/O USB 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | | 2 x USB 2.0 | |
| display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | | | 2 x USB 3.2 Gen2 | |
| display output) * For Thunderbolt support, please refer to support list. Audio 2 (Mic-In/Line-Out) | Rear I/O | TICD | 2 x USB4/Thunderbolt™4 (5V/3A, support DP 1.4a | |
| list. Audio 2 (Mic-In/Line-Out) | | USB | display output) | |
| Audio 2 (Mic-In/Line-Out) | | | * For Thunderbolt support, please refer to support | |
| | | | list. | |
| DC Jack 1 DC-in | | Audio | 2 (Mic-In/Line-Out) | |
| | | DC Jack | 1 DC-in | |

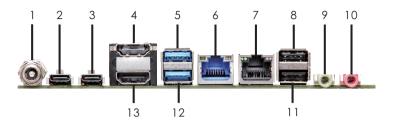
| USB | 1 x USB 3.2 Gen1 and 1 x USB 2.0 (1xUSB 3.2 header) |
|-----------------------|---|
| | COM1, COM2 (RS-232/422/485) |
| COM | COM3, COM4, COM5, COM6 (RS-232) |
| GPIO | 4 x GPI, 4 x GPO |
| LVDS | 1 |
| eDP | 1 |
| SATA PWR | 1 |
| Output | |
| Speaker | 1 |
| Header | |
| M 2 | 1 x M.2 (Key M, 2242/2260/2280) with PCIe |
| 141.2 | Gen4 x4 for SSD |
| SATA | 2 x SATA3 (6Gb/s) |
| | Intel® VMD RAID 0/1 |
| | **supported by identical interface (PCIE or |
| RAID | SATA) |
| | PCIE interface: M.2 Key B+ M.2 Key M |
| | SATA interface: 2 x SATA port |
| TPM | TPM 2.0 onboard IC |
| Output | From Super I/O to drag RESETCON# |
| Interval | 256 Segments, 0, 1, 2,255 sec |
| Input PWR | +12V~+28V DC in with 4-pin plug PWR cable or DC Jack |
| | AT/ATX Supported |
| D 0 | - AT: Directly PWR on as power input ready |
| Power On | - ATX: Press button to PWR on after power |
| ı | input ready |
| Operating Temperature | -20°C ~ 70°C |
| Storage | -40° C ~ 85° C |
| | |
| Operating Humidity | 5% ~ 90% |
| | |
| | COM GPIO LVDS eDP SATA PWR Output Speaker Header M.2 SATA RAID TPM Output Interval Input PWR Operating Temperature Storage Temperature Operating |

1.3 Motherboard Layout



- 1: M.2 Key-M Socket (M2_M1)
- 2: M.2 Key-E Socket (M2_E1)
- 3: 4-pin ATX PWR Connector (4P_ATX)
- 4: USB 3.2 Gen1 Header (USB3 3)
- 5: Chassis FAN Connector (+12V) (CHA_FAN2)
- 6: PWR ADAPTER1
- 7: CPU FAN Connector (+12V) (CPU FAN1)
- 8: eDP Inverter Power Control Wafer (EDP_BLT_PWR1)
- 9: Brightness Control Mode (BLT PWM2)
- 10: Brightness Control Mode (BLT_PWM1)
- 11: LVDS Panel Connector (LVDS1)
- 12: Inverter Power Control Wafer (BLT_PWR1)
- 13: Backlight Volume Control (BLT_VOL1)
- 14: Backlight Power Select (LCD_BLT_VCC) (BKT_PWR1)
- 15: Panel Power Select (LCD_VCC) (PNL_PWR1)
- 16: COM Port Headers (COM1~2) (RS232/422/485)
- 17: SATA3 Connectors (SATA3_0, SATA3_1)
- 18 : COM Port PWR Setting Jumpers
 - (PWR_COM1~6) (For COM Port1~6)
- $19: SATA\ Power\ Output\ Connector\ (SATA_PWR1)$
- 20: System Panel Header (PANEL1)
- 21: Clear CMOS Header (CLRMOS1)
- 22: HEATER1 Header (HEATER1)
- 23: COM Port Headers (COM5~6) (RS232)
- 24: DACC Jumper (DACC1)
- 25: COM Port Headers (COM3~4) (RS232)
- 26 : Digital Input/Output Pin Header (JGPIO1)
- 27: Digital Input/Output Power Select (JGPIO_PWR1)
- 28: Digital Input/Output Default Value Setting (JGPIO SET1)
- 29: ESPI Header (ESPI1)
- 30: M.2 Key-B Socket (M2_B1)
- 31: Buzzer Header (BUZZ2)
- 32: ATX/AT Mode Jumper (SIO AT1)
- 33: Chassis Intrusion Headers (CI1, CI2)
- 34: PCIE_ISOLATION1
- 35: 3W Audio AMP Output Wafer (SPEAKER1)
- 36: SMB_TEST1
- 37: SPDIF Header (SPDIF1)
- 38: Front Panel Audio Header (HD_AUDIO1)
- 39: Battery Connector (BAT1)

1.4 I/O Panel



- 1 DC Jack (DC_JACK1)
- 2 Thunderbolt Type-C Port (TC_TB_1)
- 3 Thunderbolt Type-C Port (TC_TB_0)
- 4 HDMI Port (HDMI1)
- 5 USB 3.2 Gen2 Port (USB3 1)
- 6 RJ45 LAN Port (LAN1) (supports vPro)*
- 7 RJ45 LAN Port (LAN2)**

- 8 USB 2.0 Port (USB2 3)
- 9 Audio Iack: Green Line Out
- 10 Audio Jack: Pink Mic In
- 11 USB 2.0 Port (USB2_4)
- 12 USB 3.2 Gen2 Port (USB3 2)
- 13 HDMI Port (HDMI2)

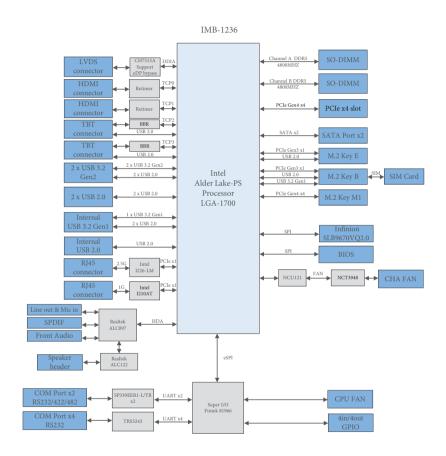
LAN1 Port LED Indications ACT/LINK SPEED LED LED Activity/Link LED SPEED LED Description Status Status Description 10Mbps connection Off No Link Off Blinking Data Activity Orange 100Mbps/1Gbps connection Link 2.5Gbps connection On Green

LAN2 Port LED Indications ACT/LINK SPEED LED LED Activity/Link LED SPEED LED Status Description Status Description Off No Link Off 10Mbps connection Blinking Data Activity Orange 100Mbps connection On Link Green 1Gbps connection LAN2 Port

^{*} There are two LED next to the LAN1 port. Please refer to the table below for the LAN1 port LED indications.

^{**} There are two LED next to the LAN2 port. Please refer to the table below for the LAN2 port LED indications.

1.5 Block Diagram



Chapter 2 Installation

This is a Mini-ITX (6.7-in \times 6.7-in \times 1.5-in, 17.0 cm \times 17.0 cm \times 3.8 cm) form factor mother-board. Before you install the motherboard, study the configuration of your chassis to ensure that the motherboard fits into it.



Make sure to unplug the power cord before installing or removing the motherboard. Failure to do so may cause physical injuries to you and damages to motherboard components.



ASRock Industrial has positioned the chipset on the rear of the PCB to optimize space and improve thermal dissipation when the chipset heatsink contacts the chassis via a thermal pad. Ensure the height of the chipset heatsink is considered during system integration.

2.1 Screw Holes

Place screws into the holes to secure the motherboard to the chassis.



Do not over-tighten the screws! Doing so may damage the motherboard.

2.2 Pre-installation Precautions

Take note of the following precautions before you install motherboard components or change any motherboard settings.

- 1. Unplug the power cord from the wall socket before touching any component.
- To avoid damaging the motherboard components due to static electricity, NEVER place your motherboard directly on the carpet or the like. Also remember to use a grounded wrist strap or touch a safety grounded object before you handle components.
- Hold components by the edges and do not touch the ICs.
- Whenever you uninstall any component, place it on a grounded antistatic pad or in the bag that comes with the component.
- 5. Heatsink (The thermal solution of whole system needs to be designed additionally.)



Before you install or remove any component, ensure that the power is switched off or the power cord is detached from the power supply. Failure to do so may cause severe damage to the motherboard, peripherals, and/or components.

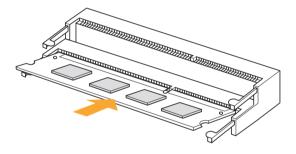
2.3 Installation of Memory Modules

IMB-1236 provides two 262-pin DDR5 (Double Data Rate 5) SO-DIMM slots, and supports Dual Channel Memory Technology.

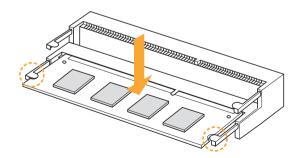


- The SO-DIMM only fits in one correct orientation. It will cause permanent damage to the motherboard and the SO-DIMM if you force the SO-DIMM into the slot at incorrect orientation.
- 2. Please do not intermix different voltage SO-DIMMs on this motherboard.

Step 1. Carefully insert the SO-DIMM memory modules into the slot at a 30-degree angle.



Step 2. Push down until the modules snap into place.



2.4 Expansion Slots

There are one PCI Express slot, three M.2 sockets and one SIM socket on this motherboard.

PCIE slot: 1 x PCIe1 (PCIE 4.0 x4 slot) is used for PCI Express x4 lane-width cards.

M.2 sockets: 1 x M.2 (Key B, 3042/3052) with PCIex1, USB 3.2, USB 2.0 and SIM for 4G/5G

1 x M.2 (Key E, 2230) with PCIe and USB 2.0

1 x M.2 (Key M, 2242/2260/2280) with PCIe Gen4 x4 for SSD

SIM Socket: 1 x SIM socket connected to M.2 key B

M.2 Key-B Socket (M2_B1)

M.2 Key-E Socket (M2_E1) M.2 Key-M Socket (M2_M1)

| Pin | Signal Name | Signal Name | Pin |
|----------|-------------|-------------------------|-----|
| 1 | NA | +3.3V | 2 |
| 3 | GND | +3.3V | 4 |
| 5 | GND | FuLL_Card_ Power off | 6 |
| 7 | USB D+ | W DISABLE1# | 8 |
| 9 | USB D- | WWAN LED# | 10 |
| 11 | GND | | |
| \dashv | | NA | 20 |
| 21 | GND | NA | 22 |
| 23 | NA | NA | 24 |
| 25 | NA | W DISABLE2# | 26 |
| 27 | GND | NA | 28 |
| 29 | USB3 RX- | UIM RESET | 30 |
| 31 | USB3 RX+ | UIM CLK | 32 |
| 33 | GND | UIM DATA | 34 |
| 35 | USB3 TX- | UIM PWR | 36 |
| 37 | USB3_TX+ | NA | 38 |
| 39 | GND | NA | 40 |
| 41 | PERn0 | NA | 42 |
| 43 | PERP0 | NA | 44 |
| 45 | GND | NA | 46 |
| 47 | PETn0 | NA | 48 |
| 49 | PETP0 | PERST# | 50 |
| 51 | GND | CLKREQ# | 52 |
| 53 | PEFCLKn | WAKE# | 54 |
| 55 | PEFCLKp | NA | 56 |
| 57 | GND | NA | 58 |
| 59 | NA | NA | 60 |
| 61 | NA | NA | 62 |
| 63 | NA | NA | 64 |
| 65 | NA | NA | 66 |
| 67 | NA | NA | 68 |
| 69 | PEDET | +3.3V | 70 |
| 71 | GND | +3.3V | 72 |
| 73 | GND | +3.3V | 74 |
| 75 | NA | | |

| P | in | Signal Name | Signal Name | Pir |
|-----|----|--------------------------|--------------------|-----|
| Г | 1 | GND | +3.3V | 2 |
| П | 3 | USB_D+ | +3.3V | 4 |
| | 5 | USB_D- | NA | 6 |
| | 7 | GND | NA | 8 |
| - 1 | 9 | | CNV_RF_RESET | 10 |
| 1 | 11 | CNV_WGR_D1+ | NA | 12 |
| Γ, | 13 | GND | MODEM_ | 14 |
| | | | CLKREQ | |
| | 15 | CNV_WGR_D0- | NA | 16 |
| | 17 | CNV_WGR_D0+ | GND | 18 |
| 1 | 19 | GND | NA | 20 |
| 2 | 21 | CNV_WGR_ CLK- | CNV_BRI_RSP | 22 |
| 2 | 23 | CNV_WGR_ CLK+ | | |
| | | | | |
| | | | CNV_BGI_DT | 32 |
| | 33 | GND | CNV_RGI_RSP | 34 |
| 3 | 35 | PETp | CNV_BRI_DT | 36 |
| 3 | 37 | PETn | NA | 38 |
| 3 | 39 | GND | NA | 40 |
| 4 | 11 | PERp | NA | 42 |
| 4 | 13 | PERn | NA | 44 |
| 4 | 15 | GND | NA | 46 |
| 4 | 17 | PEFCLKp | NA | 48 |
| | 19 | PEFCLKn | SUSCLK | 50 |
| | 51 | GND | PERST0# | 52 |
| 5 | 53 | CLKREQ# | W_DISABLE1# | 54 |
| 5 | 55 | WAKE# | W_DISABLE2# | 56 |
| 5 | 57 | GND | SMB_DATA | 58 |
| 5 | 59 | CNV_WT_D1- CNV_WT_D1+ | SMB_CLK | 60 |
| 6 | 51 | CNV_WT_D1+ | NA | 62 |
| 6 | 53 | GND | CLKIN_XTAL_ LCP | 64 |
| 6 | 55 | CNV_WT_D0- | NA | 66 |
| 6 | 57 | CNV_WT_D0+ | NA | 68 |
| 6 | 59 | GND | NA | 70 |
| 7 | 71 | CNV_WT_CLK- | +3.3V | 72 |
| | | CNV_WT_CLK+ | +3.3V | 74 |
| 7 | 75 | GND | | |
| | | | | |

| Pin | Signal Name | Signal Name | Pin |
|--------|-------------|-------------|-----|
| 1 | GND | +3.3V | 2 |
| 3 | GND | +3.3V | 4 |
| 5 | PERn3 | NA NA | 6 |
| 7 | PERD3 | NA NA | 8 |
| 9 | GND | SATA LED | 10 |
| 11 | PETn3 | +3.3V | 12 |
| 13 | PETp3 | +3.3V | 14 |
| 15 | GND | +3.3V | 16 |
| 17 | PERn2 | +3.3V | 18 |
| 19 | PERn2 | NA NA | 20 |
| 21 | GND | NA NA | 22 |
| 23 | PETn2 | NA NA | 24 |
| 25 | PETp2 | NA NA | 26 |
| 27 | GND | NA NA | 28 |
| 29 | PERn1 | NA NA | 30 |
| 31 | PERp1 | NA NA | 32 |
| 33 | GND | NA NA | 34 |
| 35 | PETn1 | NA NA | 36 |
| 37 | PETp1 | NA NA | 38 |
| 39 | GND | NA NA | 40 |
| 41 | PERn0 | NA | 42. |
| 43 | PERp0 | NA | 44 |
| 45 | GND | NA | 46 |
| 47 | PETn0 | NA | 48 |
| 49 | PETp0 | PERST# | 50 |
| 51 | GND | CLKREO# | 52 |
| 53 | PEFCLKn | WAKE# | 54 |
| 55 | PEFCLKp | NA | 56 |
| 57 | GND | NA | 58 |
| \Box | | | |
| 67 | NA | NA | 68 |
| 69 | PEDET | +3.3V | 70 |
| 71 | GND | +3.3V | 72 |
| 73 | GND | +3.3V | 74 |
| 75 | GND | | |

2.5 Jumpers Setup

The illustration shows how jumpers are setup. When the jumper cap is placed on pins, the jumper is "Short." If no jumper cap is placed on pins, the jumper is "Open." The illustration shows a 3-pin jumper whose pin1 and pin2 are "Short" when jumper cap is placed on these 2 pins.







Short

Open

Brightness Control Mode

(3-pin BLT_PWM2)

(see p. 4, No. 9)

| O | 0 | 0 |
|---|---|---|
| 1 | | 3 |

| Setting | Description |
|---------|--------------------|
| 1-2 | 3V Level (Default) |
| 2-3 | 5V Level |

Brightness Control Mode

(3-pin BLT_PWM1)

(see p. 4, No. 10)



| Setting | Description |
|---------|--|
| 1-2 | From eDP PWM to CON_ LBKLT_CTL |
| 2-3 | From LVDS PWM to CON_LBKLT_CTL (Default) |

Please set to 1-2 when adjusting brightness by Brightness Control bar under OS. Please set to 2-3 when adjusting brightness by BLT VOL1.

Backlight Power Select (LCD_BLT_VCC)

(5-pin BKT_PWR1)

(see p. 4, No. 14)

| 0 | 5 |
|----|---|
| 0 | |
| 0 | |
| [0 | |
| 0 | 1 |

| Setting | Description |
|---------|-------------------------------|
| 1-2 | LCD_BLT_VCC: +5V (Default) |
| 2-3 | LCD_BLT_VCC: +12V |
| 4-5 | LCD_BLT_VCC: DC_IN |

Use this header to set up the backlight power of the LVDS connector and the panel backlight power of BLT_PWM1.

Panel Power Select (LCD_VCC)

(5-pin PNL_PWR1)

(see p. 4, No. 15)



| Setting | Description |
|---------|------------------------|
| 1-2 | LCD_VCC: +3V (Default) |
| 2-3 | LCD_VCC: +5V |
| 4-5 | LCD_VCC: +12V |

Use this to set up the VDD power of the LVDS connector.

COM Port Pin9 PWR Setting Jumpers

(3-pin PWR COM1 (For COM Port1)) (3-pin PWR COM2 (For COM Port2)) (3-pin PWR COM5 (For COM Port5)) (3-pin PWR COM6 (For COM Port6)) (see p. 4, No. 18)

| Setting | Description |
|---------|---------------|
| 1-2 | +5V (Default) |
| 2-3 | +12V |

(3-pin PWR COM3 (For COM Port3)) (3-pin PWR_COM4 (For COM Port4)) (see p. 4, No. 18)



The maximum current for per port is 1A, and the power supply is either 5V or 12V. Use the jumpers to set the power for COM port pin 9.

Clear CMOS Header (3-pin CLRMOS1)

(see p. 4, No. 21)

| 0 | |
|---|--|
| 0 | |
| 0 | |

| Setting | Description |
|---------|------------------|
| 1-2 | Normal (Default) |
| 2-3 | Clear CMOS |

NOTE: CLRMOS1 allows you to clear the data in CMOS. To clear and reset the system parameters to default setup, please turn off the computer and unplug the power cord from the power supply. After waiting for 15 seconds, use a jumper cap to short pin2 and pin3 on CLRMOS1 for 5 seconds. However, please do not clear the CMOS right after you update the BIOS. If you need to clear the CMOS when you just finish updating the BIOS, you must boot up the system first, and then shut it down before you do the clear-CMOS action. Please be noted that the password, date, and time will be cleared only if the CMOS battery is removed.

DACC Jumper

(2-pin DACC1)

(see p. 4, No. 24)



| Setting | Description |
|---------|---------------|
| Open | No ACC |
| Short | ACC (Default) |

Auto clear CMOS when system boot improperly.

Digital Input/Output Power Select (JGPIOPWR)

(3-pin JGPIO PWR1)

(see p. 4, No. 27)



| Setting | Description |
|---------|---------------|
| 1-2 | +12V |
| 2-3 | +5V (Default) |

Digital Input/Output Default Value Setting (3-pin JGPIO_SET1)

(see p. 4, No. 28)



| Setting | Description |
|---------|---------------------|
| 1-2 | Pull-High (Default) |
| 2-3 | Pull-Low |

The header is used for GPIO default value setting for either pull high or pull low. Pulling the header to a high/low value means the voltage is anchored to VCC/GND, in a stable, non-floating state.

ATX/AT Mode Jumper

(3-pin JGPIO_SET1) (see p. 4, No. 32)



| Setting | Description | |
|---------|--------------------|--|
| Open | ATX Mode (Default) | |
| Short | AT Mode | |

Chassis Intrusion Headers

(2-pin CI1)

(see p. 4, No. 33)

O 0

(2-pin CI2)

(see p. 4, No. 33)



CI1

| Setting | Description | |
|---------|------------------|--|
| Open | Normal (Default) | |
| Short | Active Case Open | |

CI2

| Setting | Description |
|---------|------------------|
| Open | Active Case Open |
| Short | Normal (Default) |

This motherboard supports CASE OPEN detection feature that detects if the chassis cover has been removed. This feature requires a chassis with chassis intrusion detection design.

2.6 Onboard Headers and Connectors



Onboard headers and connectors are NOT jumpers. Do NOT place jumper caps over these headers and connectors. Placing jumper caps over the headers and connectors will cause permanent damage to the motherboard!

4-pin ATX PWR Connector (4-pin 4P_ATX) (see p. 4, No. 3)



| Pin | Signal Name | Signal Name | Pin |
|-----|-------------|-------------|-----|
| 1 | GND | GND | 2 |
| 3 | DC Input | DC Input | 4 |

Please connect a DC +12V~28V power supply to this connector.

USB 3.2 Gen1 Connector (19-pin USB3_3) (see p. 4, No. 4)



| Pin | Signal Name | Signal Name | Pin |
|-----|-------------|--------------|-----|
| 1 | ID | IntA_P_D+ | 2 |
| 3 | IntA_P_D+ | IntA_P_D- | 4 |
| 5 | IntA_P_D- | GND | 6 |
| 7 | GND | IntA_P_SSTX+ | 8 |
| 9 | DUMMY | IntA_P_SSTX- | 10 |
| 11 | DUMMY | GND | 12 |
| 13 | GND | IntA_P_SSRX+ | 14 |
| 15 | DUMMY | IntA_P_SSRX- | 16 |
| 17 | DUMMY | Vbus | 18 |
| 19 | Vbus | | 20 |

There is one USB 3.2 Gen1 connector on the motherboard. This header can support one USB 3.2 Gen1 port with maximum power current 0.9A.

Chassis FAN Connector (+12V) (4-pin CHA_FAN2) (see p. 4, No. 5)



| Pin | Signal Name |
|-----|-------------------|
| 1 | GND |
| 2 | +12V |
| 3 | CHA_FAN_SPEED |
| 4 | FAN_SPEED_CONTROL |



The board offers three 4-pin chassis fan (Smart Fan) connectors which are compatible with 3-pin chassis fan. If you connect a 3-pin chassis fan to the chassis fan connector on this motherboard, please connect it to pin 1-3. The maximum current is 1A.

PWR_ADAPTER1

(4-pin PWR_ADAPTER1) (see p. 4, No. 6)



| Pin | Signal Name |
|-----|-------------|
| 1 | GND |
| 2 | REMOTE_CTL |
| 3 | +5VA |
| 4 | GND |

CPU Fan Connector (+12V) (4-pin CPU_FAN1)

(4-pin CPU_FAN1 (see p. 4, No. 7)



| Pin | Signal Name |
|-----|-------------------|
| 1 | GND |
| 2 | +12V |
| 3 | CPU_FAN_SPEED |
| 4 | FAN_SPEED_CONTROL |



The board offers three 4-pin CPU fan (Smart Fan) connectors which are compatible with 3-pin CPU fan. If you connect a 3-pin CPU fan to the CPU fan connector on this mother-board, please connect it to pin 1-3. The maximum current is 1A.

eDP Inverter Power Control Wafer

(6-pin EDP_BLT_PWR1)

(see p. 4, No. 8)

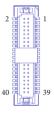


| Pin | Signal Name |
|-----|---------------|
| 1 | GND |
| 2 | GND |
| 3 | CON_LBKLT_CTL |
| 4 | CON_LBKLT_EN |
| 5 | LCD_BLT_VCC |
| 6 | LCD BLT VCC |

LVDS Panel Connector*

(40-pin LVDS1)

(see p. 4, No. 11)



| Pin | Signal Name | Signal Name | Pin |
|-----|---------------|---------------|-----|
| 1 | LCD_VCC | LCD_VCC | 2 |
| 3 | +3.3V | NA | 4 |
| 5 | NA | LVDS_A_DATA0# | 6 |
| 7 | LVDS_A_DATA0 | GND | 8 |
| 9 | LVDS_A_DATA1# | LVDS_A_DATA1 | 10 |
| 11 | GND | LVDS_A_DATA2# | 12 |
| 13 | LVDS_A_DATA2 | GND | 14 |
| 15 | LVDS_A_DATA3# | LVDS_A_DATA3 | 16 |
| 17 | GND | LVDS_A_CLK# | 18 |
| 19 | LVDS_A_CLK | GND | 20 |
| 21 | LVDS_B_DATA0# | LVDS_B_DATA0 | 22 |
| 23 | GND | LVDS_B_DATA1# | 24 |
| 25 | LVDS_B_DATA1 | GND | 26 |
| 27 | LVDS_B_DATA2# | LVDS_B_DATA2 | 28 |
| 29 | DPLVDD_EN | LVDS_B_DATA3# | 30 |
| 31 | LVDS_B_DATA3 | GND | 32 |
| 33 | LVDS_B_CLK# | LVDS_B_CLK | 34 |
| 35 | GND | CON_LBKLT_EN | 36 |
| 37 | CON_LBKLT_CTL | LCD_BLT_VCC | 38 |
| 39 | LCD_BLT_VCC | LCD_BLT_VCC | 40 |
| | | | |

^{*} PD (Panel Detection): Connect this pin to LVDS Panel's Ground pin to detect Panel detection.

* eDP Connector (on the Backside of PCB)



| 1 NA 2 GND 3 eDP_TX#3_CON 4 eDP_TX3_CON 5 GND 6 eDP_TX#2_CON 7 eDP_TX2_CON 7 eDP_TX2_CON 8 GND 9 eDP_TX1_CON 11 GND 12 eDP_TX4_CON 13 eDP_TX4_CON 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 16 eDP_CON 17 GND 18 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 30 GND 31 GND 31 GND 32 eDP_BXITEN 33 eDP_BKITEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC | Pin | Signal Name |
|--|----------|---------------|
| 2 GND 3 eDP_TX#3_CON 4 eDP_TX3_CON 5 GND 6 eDP_TX42_CON 7 eDP_TX2_CON 8 GND 9 eDP_TX4_CON 10 eDP_TX1_CON 11 GND 12 eDP_TX4_CON 13 eDP_TX_0CON 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 17 GND 18 LCD_VCC 20 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | | |
| 3 eDP_TX#3_CON 4 eDP_TX3_CON 5 GND 6 eDP_TX#2_CON 7 eDP_TX2_CON 8 GND 9 eDP_TX*1_CON 10 eDP_TX1_CON 11 GND 12 eDP_TX*0_CON 13 eDP_TX0_CON 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC | | |
| 4 eDP_TX3_CON 5 GND 6 eDP_TX#2_CON 7 eDP_TX2_CON 8 GND 9 eDP_TX#1_CON 10 eDP_TX1_CON 11 GND 12 eDP_TX0_CON 13 eDP_TX0_CON 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 16 eDP_AUX_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 31 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC | | |
| 5 GND 6 eDP_TX#2_CON 7 eDP_TXZ_CON 8 GND 9 eDP_TX*1_CON 10 eDP_TX*1_CON 11 GND 12 eDP_TX*0_CON 13 eDP_TX*0_CON 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 31 GND 31 GND 32 eDP_BKITEN 33 eDP_BKITETL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC | <u> </u> | |
| 6 eDP_TX#2_CON 7 eDP_TX2_CON 8 GND 9 eDP_TX#1_CON 10 eDP_TX1_CON 11 GND 12 eDP_TX0_CON 13 eDP_TX0_CON 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 31 GND 32 eDP_BKITEN 33 eDP_BKITEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC | | |
| 7 | _ | |
| 8 GND 9 eDP_TX#1_CON 10 eDP_TXJ_CON 11 GND 12 eDP_TX#0_CON 13 eDP_TXOON 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 31 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC | _ | eDP_TX#2_CON |
| 9 eDP_TX#1_CON 10 eDP_TX1_CON 11 GND 12 eDP_TX#0_CON 13 eDP_TX0_CON 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 31 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC 39 LCD_BLT_VCC | | |
| 10 | | 9512 |
| 11 | <u> </u> | |
| 12 eDP_TX#0_CON 13 eDP_TX0_CON 14 GND 15 eDP_AUX_CON 16 eDP_AUX#_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 30 GND 31 GND 32 eDP_BKITEN 33 eDP_BKITEN 33 eDP_BKITCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 39 LCD_BLT_VCC | _ | |
| 13 | | |
| 14 GND 15 eDP_AUX_CON 16 eDP_AUX_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 39 LCD_BLT_VCC | | |
| 15 | _ | |
| 16 eDP_AUX#_CON 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC | 14 | GND |
| 17 GND 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 30 GND 31 GND 32 eDP_BKITEN 33 eDP_BKITEN 33 eDP_BKITCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 15 | |
| 18 LCD_VCC 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC | 16 | eDP_AUX#_CON |
| 19 LCD_VCC 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 17 | GND |
| 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 18 | LCD_VCC |
| 20 LCD_VCC 21 LCD_VCC 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTEN 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 19 | LCD_VCC |
| 22 NA 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 20 | LCD_VCC |
| 23 GND 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 32 eDP_BKLITEN 33 eDP_BKLITCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 21 | LCD_VCC |
| 24 GND 25 GND 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 22 | NA |
| 25 GND 26 GND 27 eDP_HPD_CON 28 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 23 | GND |
| 26 GND 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 24 | GND |
| 27 eDP_HPD_CON 28 GND 29 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 25 | GND |
| 28 GND 29 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 26 | GND |
| 29 GND 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 27 | eDP_HPD_CON |
| 30 GND 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 28 | GND |
| 31 GND 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 29 | GND |
| 32 eDP_BKLTEN 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 30 | GND |
| 33 eDP_BKLTCTL_R 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 31 | GND |
| 34 SMB_DATA_MAIN 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 32 | eDP_BKLTEN |
| 35 SMB_CLK_MAIN 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 33 | eDP_BKLTCTL_R |
| 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 34 | SMB_DATA_MAIN |
| 36 LCD_BLT_VCC 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 35 | SMB_CLK_MAIN |
| 37 LCD_BLT_VCC 38 LCD_BLT_VCC 39 LCD_BLT_VCC | 36 | LCD_BLT_VCC |
| 39 LCD_BLT_VCC | 37 | LCD RIT VCC |
| 39 LCD_BLT_VCC | 38 | LCD_BLT_VCC |
| | 39 | LCD_BLT_VCC |
| 10/1 | 40 | NA |

Inverter Power Control Wafer (6-pin BLT_PWR1)

(see p. 4, No. 12)



| Pin | Signal Name |
|-----|---------------|
| 1 | GND |
| 2 | GND |
| 3 | CON_LBKLT_CTL |
| 4 | CON_LBKLT_EN |
| 5 | LCD_BLT_VCC |
| 6 | LCD_BLT_VCC |

Backlight Volume Control (7-pin BLT_VOL1)

(see p. 4, No. 13)



| Pin | Signal Name |
|-----|-------------|
| 1 | GPIO_VOL_UP |
| 2 | GPIO_VOL_DW |
| 3 | PWRDN |
| 4 | BLT_UP |
| 5 | BLT_DW |
| 6 | GND |
| 7 | GND |



| Pin | Signal Name | Signal Name | Pin |
|-----|-------------|-------------|-----|
| 1 | DDCD# | RRXD | 2 |
| 3 | TTXD | DDTR# | 4 |
| 5 | GND | DDSR# | 6 |
| 7 | RRTS# | CCTS# | 8 |
| 9 | PWR | | 10 |



These are six 2.54mm-pitch COM port headers (COM1~COM6), with COM1 and COM2 supporting RS232/422/485, and with COM3~COM6 supporting RS232. The maximum current is 1A per port. The power supply of pin 9 is either 5V or 12V; use the COM Port Pin 9 PWR Setting Jumper to control it.



* This motherboard supports RS232/422/485 on COM1, 2 ports. Please refer to the table below for the pin definition. In addition, COM1, 2 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM1, 2 Ports Pin Definition

| Pin | RS232 | RS422 | RS485 |
|-----|-------|-------|-------|
| 1 | DCD | TX- | RTX- |
| 2 | RXD | TX+ | RTX+ |
| 3 | TXD | RX+ | NA |
| 4 | DTR | RX- | NA |
| 5 | GND | GND | GND |
| 6 | DSR | NA | NA |
| 7 | RTS | NA | NA |
| 8 | CTS | NA | NA |
| 9 | PWR | PWR | PWR |

SATA3 Connectors (7-pin SATA3_0, SATA3_1) (see p. 4, No. 17)



| Pin | Signal Name |
|-----|-------------|
| 1 | GND |
| 2 | SATA-A+ |
| 3 | SATA-A- |
| 4 | GND |
| 5 | SATA-B- |
| 6 | SATA-B+ |
| 7 | GND |

The Serial ATA3 (SATA3) connector supports SATA data cables for internal storage devices. The current SATA3 interface allows up to 6.0 Gb/s data transfer rate.

SATA Power Output Connector

(4-pin SATA_PWR1)

(see p. 4, No. 19)



| Pin | Signal Name |
|-----|-------------|
| 1 | +5V |
| 2 | GND |
| 3 | GND |
| 4 | +12V |

Please connect a SATA power cable to this connector.

| System Panel | l Header |
|----------------|----------|
| (9-pin PANEL1) | |

(see p. 4, No. 20)



| Pin | Signal Name | Signal Name | Pin |
|-----|-------------|-------------|-----|
| 1 | HDLED+ | PLED+ | 2 |
| 3 | HDLED- | PLED- | 4 |
| 5 | GND | PWRBTN# | 6 |
| 7 | RESET# | GND | 8 |
| 9 | +5VSB | | 10 |

This header accommodates several system front panel functions.



Connect the power switch, reset switch and system status indicator on the chassis to this header according to the pin assignments below. Note the positive and negative pins before connecting the cables.

PWRBTN (Power Switch):

Connect to the power switch on the chassis front panel. You may configure the way to turn off your system using the power switch.

RESET (Reset Switch):

Connect to the reset switch on the chassis front panel. Press the reset switch to restart the computer if the computer freezes and fails to perform a normal restart.

PLED (System Power LED):

Connect to the power status indicator on the chassis front panel. The LED is on when the system is operating. The LED keeps blinking when the sys-tem is in S1 sleep state. The LED is off when the system is in S3/S4 sleep state or powered off (S5).

HDLED (Hard Drive Activity LED):

Connect to the hard drive activity LED on the chassis front panel. The LED is on when the hard drive is reading or writing data.

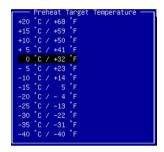
The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker and etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assign-ments are matched correctly.

| HEATER1 Header |
|--------------------|
| (3-pin HEATER1) |
| (see p. 4, No. 22) |

| 1 | 0 | 1 |
|---|---|---|
| | 0 | |
| | 0 | 3 |

| [| Pin | Signal Name |
|---|-----|---|
| | 1 | Heater_PWR (5V/1A) |
| Γ | 2 | GND |
| | 3 | NTC (Negative Temperature Coefficient) thermistors |

^{*} Deep mode is not supported when the preheat function is enabled. Refer to the following Preheat Targer Temperature list.



Digital Input/Output Pin Header 9 1 (10-pin JGPIO1) (see p. 4, No. 26) $\boxed{0}$ 0 0 0 $\boxed{0}$ $\boxed{0}$

| Pin | Signal Name | Signal Name | Pin |
|-----|-------------|-------------|-----|
| 1 | SIO_GP71 | GPP_H23 | 2 |
| 3 | SIO_GP72 | GPP_I10 | 4 |
| 5 | SIO_GP73 | GPP_E5 | 6 |
| 7 | SIO_GP74 | GPP_E6 | 8 |
| 9 | JGPIOPWR_R | GND | 10 |

| Parameter | Range | |
|--------------------------------------|-----------|--|
| GPIO input Low Voltage | Max. 0.8V | |
| GPIO input High Voltage Min. 2.31V | | |
| GPIO output Low Voltage Max. 0.4V | | |
| GPIO output High Voltage Min. 2.85 | | |
| Note: | | |
| Max. load per GPIO pin : 2mA | | |
| JGPIO1 Pin 9 - JGPIOPWR_R, Current | | |
| Max. 1A | | |

^{*} The 10k Ohm NTC thermistors is suggested.

ESPI Header (20-pin ESPI1) (see p. 4, No. 29)



The header is reserved for Port 80 code display and debugging purposes.

| Pin | Signal Name |
|-----|--------------|
| 1 | GND |
| 2 | ESPI_CLK |
| 3 | GND |
| 4 | ESPI_CS# |
| 5 | ESPI_RESET# |
| 6 | GND |
| 7 | +3V |
| 8 | GND |
| 9 | SMB_CLK |
| 10 | SMB_DATA |
| 11 | ESPI_IO0 |
| 12 | ESPI_IO1 |
| 13 | ESPI_IO2 |
| 14 | ESPI_IO3 |
| 15 | GND |
| 16 | +3VSB |
| 17 | Internal Use |
| 18 | Internal Use |
| 19 | ESPI_ALERT# |
| 20 | GND |

Buzzer Header

(2-pin BUZZ2) (see p. 4, No. 31)



| Pin | Signal Name |
|-----|-------------|
| 1 | +5V |
| 2 | BUZZ |

PCIE_ISOLATION1

(2-pin PCIE_ISOLATION1) (see p. 4, No. 34)



| Pin | Signal Name |
|-----|-------------|
| 1 | PSON# |
| 2 | GND |

* Connect to PCIE_ISOLATION_1 header on VGA-PWR card.

3W Audio AMP Output Wafer (4-pin SPEAKER1)

(see p. 4, No. 35)



| Pin | Signal Name |
|-----|-------------|
| 1 | OUTLN |
| 2 | OUTLP |
| 3 | OUTRP |
| 4 | OUTRN |

SMBUS_TEST1 (4-pin SMBUS_TEST1)

(see p. 4, No. 36)



| Pi | ı | Signal Name |
|----|---|-------------|
| 1 | | GPIO |
| 2 | | SMB_CLK |
| 3 | | SMB_DATA |
| 4 | | GND |

| SPDIF Header |
|--------------------|
| (3-pin SPDIF1) |
| (see p. 4, No. 37) |



| Pin | Signal Name |
|-----|-------------|
| 1 | +5V |
| 2 | |
| 3 | SPDIF OUT |
| 4 | GND |

SPDIF header, providing SPDIF audio output to HDMI VGA card, allows the system to connect HDMI Digital TV/projector/LCD devices. Please connect the SPDIF connector of HDMI VGA card to this header.

Front Panel Audio Header (8-pin HD_AUDIO1)

(see p. 4, No. 38)

| Pin | Signal Name | Signal Name | Pin |
|-----|-------------|-------------|-----|
| 1 | MIC2_L | GND | 2 |
| 3 | MIC2_R | | 4 |
| 5 | OUT2_R | MIC_RET | 6 |
| 7 | J_SENSE | | 8 |
| 9 | OUT2_L | OUT_RET | 10 |

This is line out/microphone interface for front panel audio cable that allows jack detection, convenient connection and control of audio devices.

Battery Connector (BAT1) (see p. 4, No. 39)



| Pin | Signal Name | |
|-----|-------------|--|
| 1 | +BAT | |
| 2 | GND | |
| | | |

Chapter 3 UEFI SETUP UTILITY

3.1 Introduction

ASRock Industrial UEFI (Unified Extensible Firmware Interface) is a BIOS utility which offers tweak-friendly options in an advanced viewing interface. The UEFI system works with a USB mouse and offers users a faster, sleeker experience.

This BIOS utility can perform the Power-On Self-Test (POST) during system startup, record hardware parameters of the system, load operating system, and so on. The battery on the motherboard supplies the power needed to the CMOS when the system power is turned off, and the values configured in the UEFI utility are kept in the CMOS.

Please note that inadequate BIOS settings may cause system instability, mulfunction or boot failure. We strongly recommend that you do not alter the UEFI default configurations or change the settings only with the assistance of a trained service person.

If the system becomes unstable or fails to boot after you change the setting, try to clear the CMOS values and reset the board to default values. See your motherboard manual for instructions

3.1.1 Entering BIOS Setup

You may run the UEFI SETUP UTILITY by pressing <F2> or <Delete> right after you power on the computer; otherwise, the Power-On-Self-Test (POST) will continue with its test routines. If you wish to enter the UEFI SETUP UTILITY after POST, restart the system by pressing <Ctl> + <Alt> + <Delete>, or by pressing the reset button on the system chassis. You may also restart by turning the system off and then back on.

This setup guide explains how to use the UEFI SETUP UTILITY to configure all the supported system. The screenshots in this manual are for reference only. UEFI Settings and options may vary owing to different BIOS release versions or CPU installed. Please refer to the actual BIOS version of the motherboard you purchased for detailed screens, settings and options.

3.1.2 UEFI Menu Bar

The top of the screen has a menu bar with the following selections:

| Main | For setting system time/date information |
|-------------|---|
| Advanced | For advanced system configurations |
| H/W Monitor | Displays current hardware status |
| Security | For security settings |
| Boot | For configuring boot settings and boot priority |
| Exit | Exit the current screen or the UEFI Setup Utility |



Because the UEFI software is constantly being updated, the following UEFI setup screens and descriptions for reference purpose only, and may vary from the latest BIOS and do not exactly match what you see on your screen.

3.1.3 Navigation Keys

Use $< \longleftarrow >$ key or $< \longrightarrow >$ key to choose among the selections on the menu bar, and use $< \bigwedge^{\blacktriangle} >$ key or $< \bigvee^{\blacktriangledown} >$ key to move the cursor up or down to select items, then press <Enter> to get into the sub screen. You can also use the mouse to click your required item.

Please check the following table for the descriptions of each navigation key.

| Navigation Key(s) | Description |
|-------------------|--|
| +/- | To change option for the selected items |
| <tab></tab> | Switch to next function |
| <pgup></pgup> | Go to the previous page |
| <pgdn></pgdn> | Go to the next page |
| <home></home> | Go to the top of the screen |
| <end></end> | Go to the bottom of the screen |
| <f1></f1> | To display the General Help Screen |
| <f7></f7> | Discard changes and exit the SETUP UTILITY |
| <f9></f9> | Load optimal default values for all the settings |
| <f10></f10> | Save changes and exit the SETUP UTILITY |
| <f12></f12> | Print screen |
| <esc></esc> | Jump to the Exit Screen or exit the current screen |

3.2 Main Screen

When you enter the UEFI Setup Utility, the Main screen will appear and display the system overview.





Because the UEFI software is constantly being updated, the following UEFI setup screens and descriptions are for reference purpose only, and they may not exactly match what you see on your screen. Options may also vary depending on the features of your motherboard.

3.3 Advanced Screen

In this section, you may set the configurations for the following items: CPU Configuration, Chipset Configuration, Storage Configuration, Super IO Configuration, AMT Configuration, ACPI Configuration, USB Configuration and Trusted Computing.





Setting wrong values in this section may cause the system to malfunction.

Instant Flash

Instant Flash is a UEFI flash utility embedded in Flash ROM. This convenient UEFI update tool allows you to update system UEFI without entering operating systems first like Windows*. Just launch this tool and save the new UEFI file to your USB flash drive, floppy disk or hard drive, and then you can update your UEFI in only a few clicks without preparing an additional floppy diskette or other complicated flash utility. Please be noted that the USB flash drive or hard drive must use FAT32/16/12 file system. If you execute Instant Flash utility, the utility will show the UEFI files and their respective information. Select the proper UEFI file to update your UEFI, and reboot your system after UEFI update process completes.

3.3.1 CPU Configuration



Intel Hyper Threading Technology

Intel Hyper Threading Technology allows multiple threads to run on each core, so that the overall performance on threaded software is improved.

Configuration options: [Enabled] [Disabled]

Active Processor P-Cores

This allows you to select the number of cores to enable in each processor package.

Active Processor E-Cores

This allows you to select the number of E-Cores to enable in each processor package. NOTE: Number of P-Cores and E-Cores are looked at together. When both are {0,0}, Pcode will enable all cores

CPU C States Support

This allows you to enable CPU C States Support for power saving. It is recommended to keep C3, C6 and C7 all enabled for better power saving.

Configuration options: [Enabled] [Disabled]

Enhanced Halt State (C1E)

The option allows you to enable Enhanced Halt State (C1E) for lower power consumption.

Configuration options: [Enabled] [Disabled]

Package C State Support

The option allows you to enable CPU, PCIe, Memory, Graphics C State Support for power saving.

CFG Lock

The option allows you to enable or disable the CFG Lock.

Configuration options: [Enabled] [Disabled]

Intel Virtualization Technology

Intel Virtualization Technology allows a platform to run multiple operating systems and applications in independent partitions, so that one computer system can function as multiple virtual systems.

Configuration options: [Enabled] [Disabled]

Intel SpeedStep Technology

Intel SpeedStep technology allows processors to switch between multiple frequencies and voltage points for better power saving and heat dissipation. CPU turbo ratio can be fixed when Intel SpeedStep Technology is set to [Disabled].

Configuration options: [Enabled] [Disabled].

If you install Windows* 10 and want to enable this function, please set this item to [Enabled]. This item will be hidden if the current CPU does not support Intel SpeedStep technology.



Please note that enabling this function may reduce CPU voltage and lead to system stability or compatibility issues with some power supplies. Please set this item to [Disabled] if above issues occur.

Intel Turbo Boost Technology

Intel Turbo Boost Technology enables the processor to run above its base operating frequency when the operating system requests the highest performance state. The default value is [Enabled].

Configuration options: [Enabled] [Disabled]

CPU Thermal Throttling

CPU Thermal Throttling allows you to enable CPU internal thermal control mechanisms to keep the CPU from overheating.

Configuration options: [Enabled] [Disabled]

CPU Operating Mode

If working at [Normal Mode], M/B works with the default Smart Fan Setting and can provide better cooler experience. If [Performance Mode] is selected, CPU can provide optimal performance, but the CPU fan speed will work at higher speed.

Configuration options: [Normal Mode] [Performance Mode]

3.3.2 Chipset Configuration



Primary Graphics Adapter

The option allows you to select a primary VGA.

Configuration options: [Onboard] [PCI Express] (Options vary when you have installed a graphics card on your motherboard.)

VT-d

Intel® Virtualization Technology for Directed I/O helps your virtual machine monitor better utilize hardware by improving application compatibility and reliability, and providing additional levels of manageability, security, isolation, and I/O performance.

Configuration options: [Enabled] [Disabled]

Re-Size BAR Support

If system has Resizable BAR capable PCIe Devices, this option enables or disables Resizable BAR Support.

PCIe Speed

Configure PCIe Speed

Share Memory

Share memory allows you to configure the size of memory that is allocated to the integrated graphics processor when the system boots up.

Configuration options: [Auto] [32M] [64M] [128M] [256M] [512M] Options vary depending on the memory you use on your motherboard.

IGPU Multi-Monitor

Select [Disabled] to disable the integrated graphics when an external graphics card is installed. Select [Enabled] to keep the integrated graphics enabled at all times.

Configuration options: [Enabled] [Disabled]

Render Standby

Power down the render unit when the GPU is idle for lower power consumption.

Active LVDS

Use this option to enable or disable the LVDS. The default value is [Disabled]. Set the item to [Enabled]. Then press <F10> to save the setting and restart the system. Now the default value of Active LVDS is changed to ENABLED (F9 load default is also set to ENABLED).

Change the setting from [Enabled] to [Disabled], and then press <F10> to save the setting and restart the system. Likewise, the default value of Active LVDS is changed to DIS-ABLED (F9 load default is also set to DISABLED).

Onboard I AN1

This allows you to enable or disable the Onboard LAN1 feature.

vPro Function

Enable/Disable vPro SMLink of LAN port. Select Enabled if you would like to utilize LAN port with vPro. Select Disabled if you would like to utilize LAN port as DHCP Server. Please turn off the computer and unplug the power cord from the power supply after the setting has been changed.

Onboard LAN2

This allows you to enable or disable the Onboard LAN2 feature.

Onboard HD Audio

Onboard HD Audio allows you to enable or disable the onboard HD audio controller. Set this item to [Auto] to enable the onboard HD and automatically disable it when a sound card is installed

Configuration options: [Enabled] [Disabled]

Restore on AC/Power Loss

The option allows you to select the power state after a power failure.

[Power Off] sets the power to remain off when the power recovers.

[Power On] sets the system to start to boot up when the power recovers.

3.3.3 Storage Configuration



VMD Configuration

This item allows you to enable or disable the Intel VMD support function.

SATA Controller(s)

The option allows you to enable or disable the SATA controllers.

Configuration options: [Enabled] [Disabled]

SATA Mode Selection

AHCI supports new features that improve performance.

Configuration option: [AHCI]



AHCI (Advanced Host Controller Interface) supports NCQ and other new features that will improve SATA disk performance.

Hybrid Storage Detection and Configuration Mode

The option allows you to select Hybrid Storage Detection and Configuration Mode.

Configuration options: [Dynamic Configuration for Hybrid Storage Enable] [Disabled]

SATA Aggressive Link Power Management

SATA Aggressive Link Power Management allows SATA devices to enter a low power state during periods of inactivity to save power. It is supported only by AHCI mode.

Configuration options: [Enabled] [Disabled]

Hard Disk S.M.A.R.T.

S.M.A.R.T stands for Self-Monitoring, Analysis, and Reporting Technology. It is a monitoring system for computer hard disk drives to detect and report on various indicators of reliability.

Configuration options: [Enabled] [Disabled]

3.3.4 Super IO Configuration



COM1 Configuration

Use this to set parameters of COM1.

Type Select

Use this to select COM1 port type: [RS232], [RS422] or [RS485].

COM2 Configuration

Use this to set parameters of COM2.

Type Select

Use this to select COM2 port type: [RS232], [RS422] or [RS485].

COM3 Configuration

Use this to set parameters of COM3.

COM4 Configuration

Use this to set parameters of COM4.

COM5 Configuration

Use this to set parameters of COM5.

COM6 Configuration

Use this to set parameters of COM6.

WDT Timeout Reset

Use this to set the Watch Dog Timer.

3.3.5 AMT Configuration



USB Provisioning of AMT

Use this to enable or disable AMT USB Provisioning. The default is [Disabled].

MAC Pass Through

The option enables or disables MAC Pass Through function.

Activate Remote Assistance Process

Trigger CIRA boot. The default is [Disabled].

Un-Configure ME

Un-Configure ME without password. The default is [Disabled].

ASF Configuration

The option allows you to configure Alert Standard Format parameters.

Secure Erase Configuration

Secure Erase configuration menu.

One Click Recovery(OCR) Configuration

Configuration setting for One Click Recovery. This allows access for AMT to boot a recovery OS application

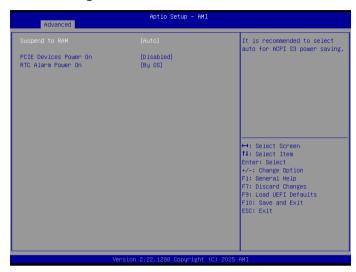
Remote Platform Erase Configuration

Remote Platform Erase configuration menu.

MEBx

This Formset contains forms for configuring MEBx.

3.3.6 ACPI Configuration



Suspend to RAM

Suspend to RAM allows you to select [Disabled] for ACPI suspend type S1. It is recommended to select [Auto] for ACPI S3 power saving.

Configuration options: [Auto] [Disabled]

PCIE Devices Power On

Use this item to enable or disable PCIE devices to turn on the system from the power-soft-off mode.

RTC Alarm Power On

RTC Alarm Power On allows the system to be waked up by the real time clock alarm. Set it to By OS to let it be handled by your operating system.

Configuration options: [Enabled] [Disabled] [By OS]

3.3.7 USB Configuration



USB Power Control

Use this option to control USB power.

M.2 Key_B USB Function

The item enables or disables M.2 Key_B USB function.

3.3.8 Trusted Computing



NOTE: Options vary depending on the version of your connected TPM module.

Security Device Support

Security Device Support allows you to enable or disable BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

Configuration options: [Enabled] [Disabled]

Active PCR banks

This item displays active PCR Banks.

Available PCR Banks

This item displays available PCR Banks.

SHA256 PCR Bank

SHA256 PCR Bank allows you to enable or disable SHA256 PCR Bank.

Configuration options: [Enabled] [Disabled]

SHA384 PCR Bank

SHA384 PCR Bank allows you to enable or disable SHA384 PCR Bank.

Configuration options: [Enabled] [Disabled]

Pending Operation

Pending Operation allows you to schedule an Operation for the Security Device.

NOTE: Your computer will reboot during restart in order to change State of the Device.

Configuration options: [None] [TPM Clear]

Platform Hierarchy

This item allows you to enable or disable Platform Hierarchy.

Configuration options: [Enabled] [Disabled]

Storage Hierarchy

This item allows you to enable or disable Storage Hierarchy.

Configuration options: [Enabled] [Disabled]

Endorsement Hierarchy

This item allows you to enable or disable Endorsement Hierarchy.

Configuration options: [Enabled] [Disabled]

Physical Presence Spec Version

Select this item to tell OS to support PPI spec version 1.2 or 1.3. Please note that some HCK tests might not support version 1.3.

Configuration options: [1.2] [1.3]

TPM 2.0 InterfaceType

This item allows you to view the Communication Interface to TPM 2.0 Device: CRB or ITS.

Device Select

This item allows you to select the TPM device to be supported.

[TPM 1.2] restricts support to TPM 1.2 devices.

[TPM 2.0] restricts support to TPM 2.0 devices.

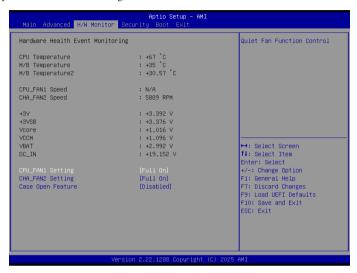
[Auto] supports both TPM 1.2 and TPM 2.0 devices with the default set to TPM 2.0 devices. If TPM 2.0 devices are not found, TPM 1.2 devices will be enumerated.

Onboard TPM

The option enables or disables Intel PTT in ME. Disable this option to use discrete TPM Module

3.4 Hardware Health Event Monitoring Screen

This section allows you to monitor the status of the hardware on your system, including the parameters of the CPU temperature, motherboard temperature, CPU fan speed, chassis fan speed, and the critical voltage.



NOTE: Options vary depending on the features of your motherboard.

CPU Fan 1 Setting

This item allows you to select a fan mode for CPU Fan 1. The default value is [Full On].

Configuration options: [Full On] [Automatic Mode]

CHA_Fan 1 Setting

This allows you to set chassis fan 1's speed. The default value is [Full On].

Configuration options: [Full On] [Automatic Mode]

Case Open Feature

This item allows you to enable or disable case open detection feature. The default is value [Disabled].

Clear Status

This option appears only when the case open has been detected. Use this option to keep or clear the record of previous chassis intrusion status.

3.5 Security Screen

In this section you may set or change the supervisor/user password for the system. You may also clear the user password.



Supervisor Password

Set or change the password for the administrator account. Only the administrator has the authority to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

User Password

Set or change the password for the user account. Users are unable to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

Secure Boot

Press [Enter] to configure the Secure Boot Settings. The feature protects the system from unauthorized access and malwares during POST.

3.6 Boot Screen

This section displays the available devices on your system for you to configure the boot settings and the boot priority.



Boot From Onboard LAN

The item allows the system to be waked up by the onboard LAN.

Configuration options: [Enabled] [Disabled]

Setup Prompt Timeout

The item allows you to configures the number of seconds to wait for the UEFI setup utility.

Configuration options: [1] - [65535]

Bootup Num-Lock

The item allows you to select whether Num Lock should be turned on or off when the system boots up.

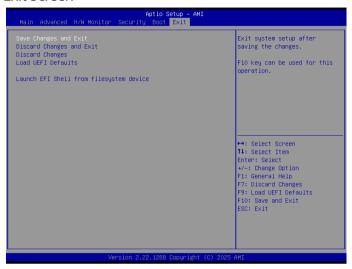
Configuration options: [On] [Off]

Full Screen Logo

[Enabled] Select this item to display the boot logo.

[Disabled] Select this item to show normal POST messages.

3.7 Exit Screen



Save Changes and Exit

When you select this option, the following message "Save configuration changes and exit setup?" will pop out. Select [Yes] to save the changes and exit the UEFI SETUP UTILITY.

Discard Changes and Exit

When you select this option, the following message "Discard changes and exit setup?" will pop out. Select [Yes] to exit the UEFI SETUP UTILITY without saving any changes.

Discard Changes

When you select this option, the following message "Discard changes?" will pop out. Select [Yes] to discard all the changes.

Load UEFI Defaults

The item allows you to load UEFI default values for all options. The F9 key can be used for this operation.

Launch EFI Shell from filesystem device

The item allows you to copy shellx64.efi to the root directory to launch EFI Shell.