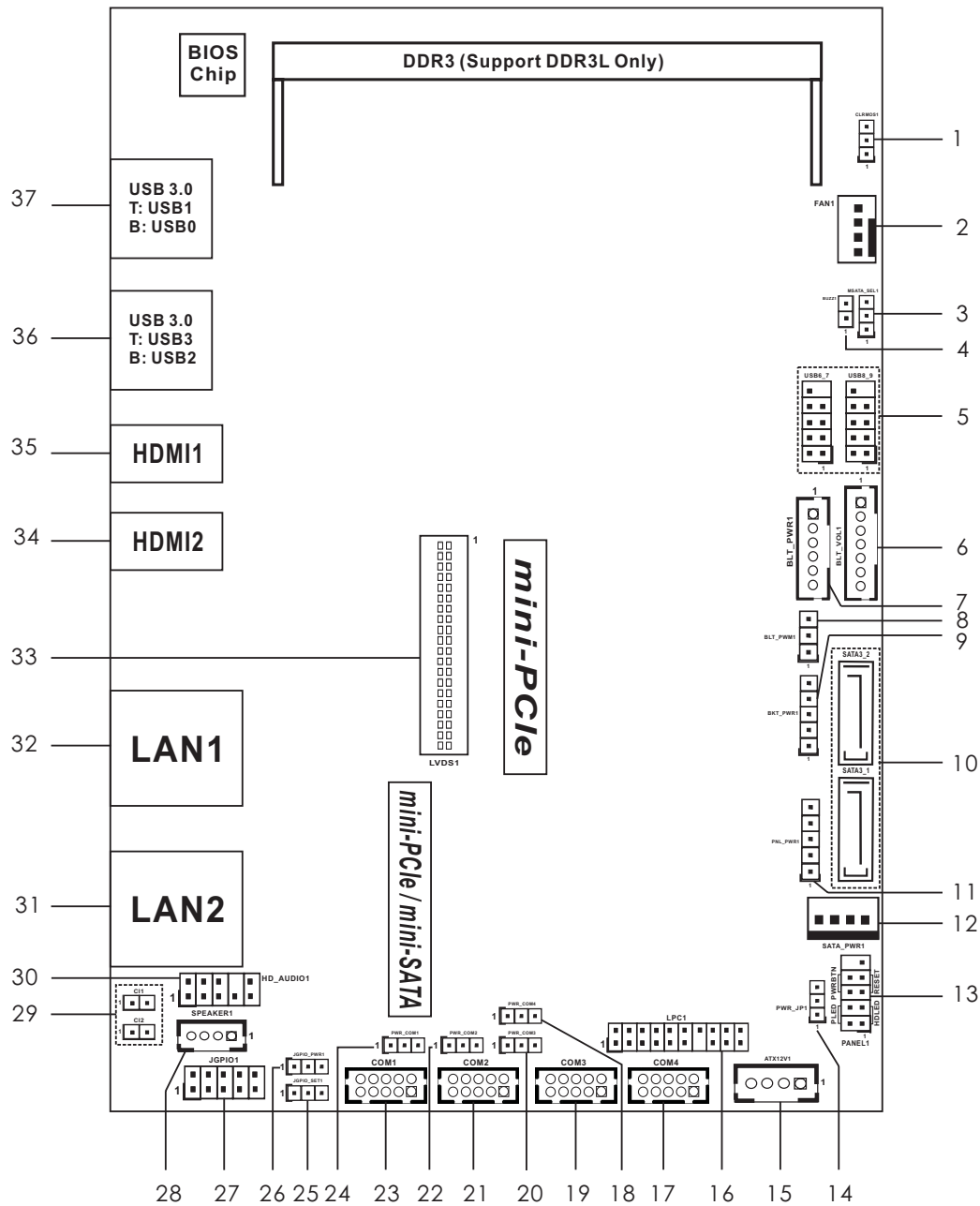


ASRock® Jumpers and Headers Setting Guide

SBC-310

SBC-311

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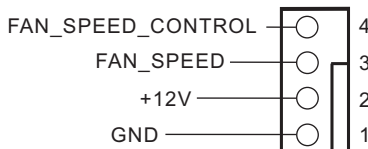


1 : Clear CMOS Header

1-2 : Normal
2-3 : Clear CMOS



2 : 4-Pin FAN Connector (+12V)

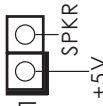


3 : mSATA Select

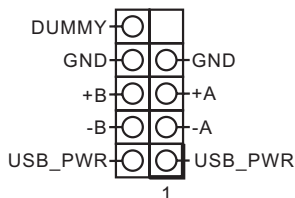
1-2 : mini-PCle
2-3 : mSATA



4 : 2-Pin Buzzer Header



5 : USB2.0 Headers (USB6_7, USB8_9)



6 : Backlight & Amp Volume Control (BLT_VOL1)



PIN	Signal Name
1	GPIO_VOL_UP
2	GPIO_VOL_DW
3	PWRDN
4	GPIO_BLT_UP
5	GPIO_BLT_DW
6	GND
7	GND

7 : Inverter Power Control Wafer (BLT_PWR1)



PIN	Signal Name
1	GND
2	GND
3	CON_LBKLT_CTL
4	CON_LBKLT_EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC

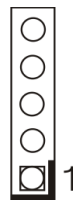
8 : Backlight Control Level (BLT_PWM1)

1-2 : +3V
2-3 : +5V



9 : Backlight Power Select (LCD_BLT_VCC) (BKT_PWR1)

1-2 : +5V
2-3 : +12V
3-4 : +12V
4-5 : DC_IN Power



10 : SATA3 Connectors (SATA3_1, SATA3_2)

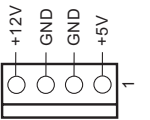


11 : Panel Power Selection (LCD_VCC) (PNL_PWR1)

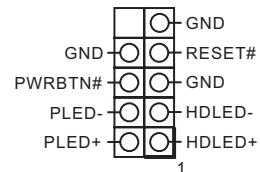
1-2 : +3V
2-3 : +5V
3-4 : +5V
4-5 : +12V



12 : SATA Power Output Connector

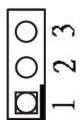


13 : System Panel Header



14 : ATX/AT Mode Select

1-2 : AT Mode
2-3 : ATX Mode

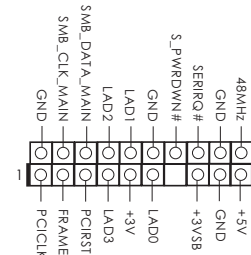


15 : ATX Power Connector (Input 9V-36V)

1-4 : GND
2-3 : DC Input



16 : LPC Header



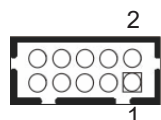
COM1, 2, 3, 4 Headers

17 : COM4

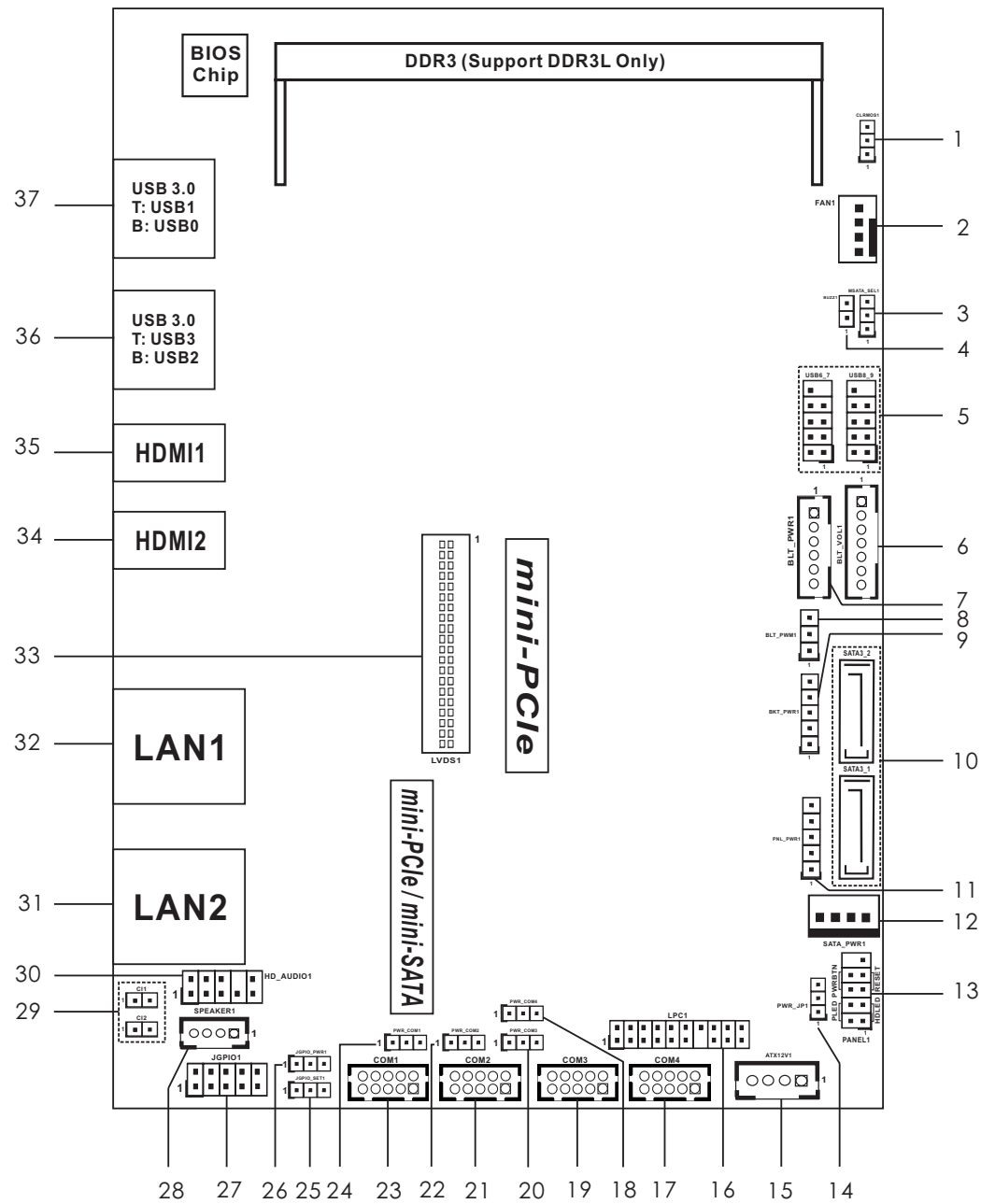
19 : COM3

21 : COM2

23 : COM1



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
10	DUMMY	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD
9	DUMMY	7	RRTS#	5	GND	3	TTXD	1	DDCD#



* This motherboard supports RS232/422/485 on COM1/COM2 port. Please refer to below table for the pin definition. In addition, COM1/COM2 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to page 32 of our user manual for details.

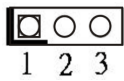
COM1/COM2 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	NA/+5V/+12V	N/A	N/A

COM Port Pin9 PWR Setting Jumpers

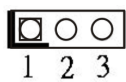
- 18 : PWR_COM4 (For COM Port4)
- 20 : PWR_COM3 (For COM Port3)
- 22 : PWR_COM2 (For COM Port2)
- 24 : PWR_COM1 (For COM Port1)

1-2 : +5V
2-3 : +12V



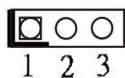
25 : GPIO Default Setting

1-2 : Pull-High
2-3 : Pull-Low



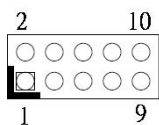
26 : Digital Input / Output Power Select

1-2 : +12V
2-3 : +5V



27 : Digital Input / Output Pin Header

PIN	Signal Name	PIN	Signal Name
1	SIO_GP34	2	SIO_GP30
3	SIO_GP35	4	SIO_GP31
5	SIO_GP36	6	SIO_GP32
7	SIO_GP37	8	SIO_GP33
9	JGPIO_PWR	10	GND

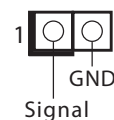


28 : 3W Audio AMP Output Wafer

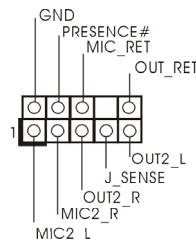
PIN	Signal Name
1	SPK L-
2	SPK L+
3	SPK R+
4	SPK R-



29 : Chassis Intrusion Headers (CI1, CI2)



30 : Front Panel Audio Header

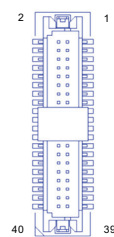


31 : RJ45 LAN Port (LAN2)

32 : RJ45 LAN Port (LAN1)

33 : LVDS Panel Connector

PIN	Signal Name	PIN	Signal Name
2	LCD_VCC	1	LCD_VCC
4	LDDC_CLK	3	+3.3V
6	LVDS_A_DATA0#	5	LDDC_DATA
8	GND	7	LVDS_A_DATA0
10	LVDS_A_DATA1	9	LVDS_A_DATA1#
12	LVDS_A_DATA2#	11	GND
14	GND	13	LVDS_A_DATA2
16	LVDS_A_DATA3	15	LVDS_A_DATA3#
18	LVDS_A_CLK#	17	GND
20	GND	19	LVDS_A_CLK
22	LVDS_B_DATA0	21	LVDS_B_DATA0#
24	LVDS_B_DATA1#	23	GND
26	GND	25	LVDS_B_DATA1
28	LVDS_B_DATA2	27	LVDS_B_DATA2#
30	LVDS_B_DATA3#	29	DPLVDD_EN
32	GND	31	LVDS_B_DATA3
34	LVDS_B_CLK	33	LVDS_B_CLK#
36	CON_LBKLT_EN	35	GND
38	LCD_BLT_VCC	37	CON_LBKLT_CTL
40	LCD_BLT_VCC	39	LCD_BLT_VCC



34 : HDMI Port (HDMI2)

35 : HDMI Port (HDMI1)

36 : USB3.0 Ports (USB23)

37 : USB3.0 Ports (USB01)

* The thermal solution of whole system needs to be designed additionally.