P/ N: 15G06M051001AK V1.1

Jumpers and Headers Setting Guide

SBC-230D
SBC-230L
SBC-230J

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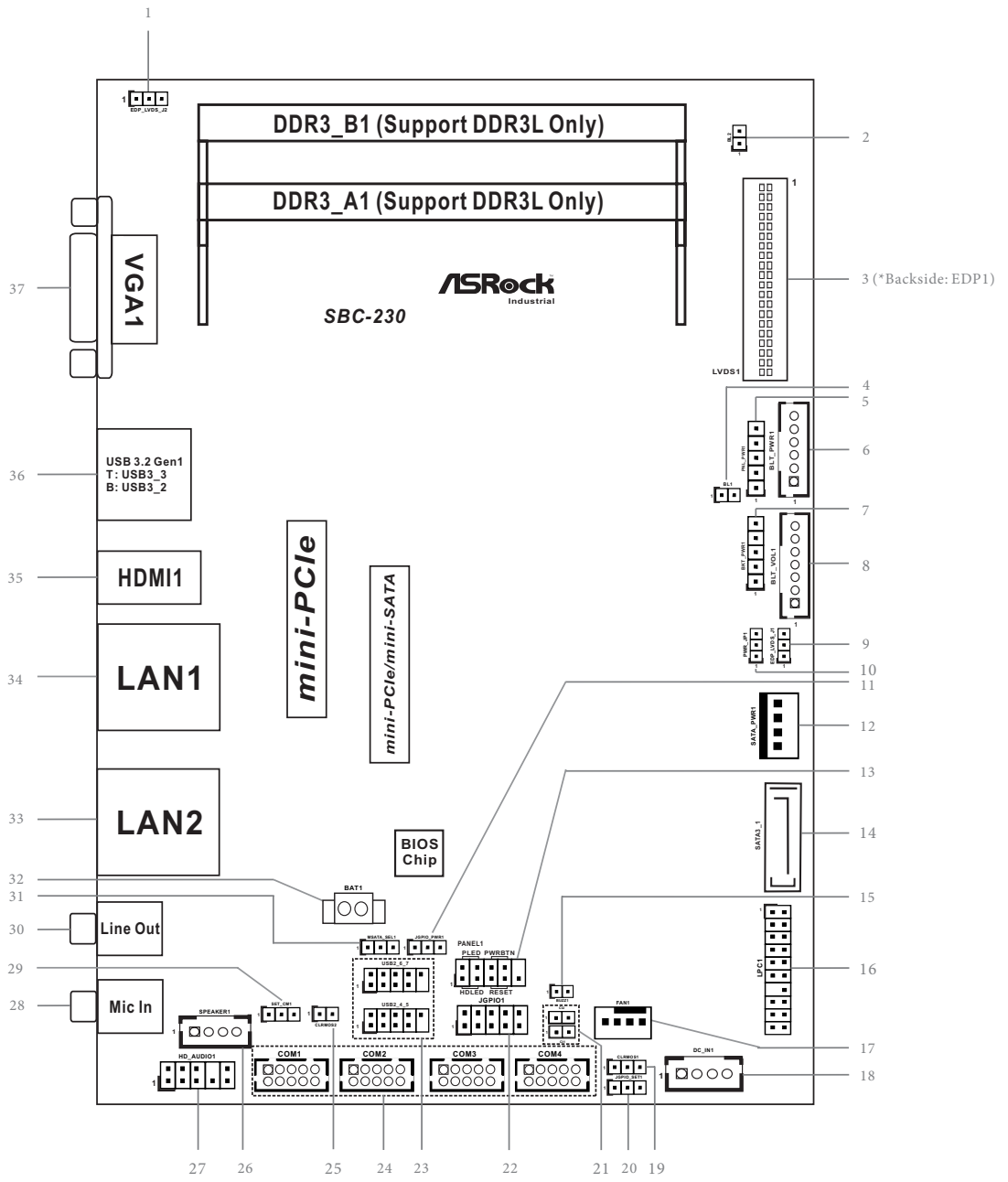
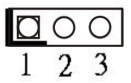


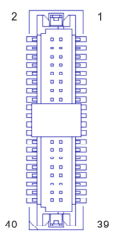
1 : EDP_LVDS_J2
1-2 : eDP
2-3 : LVDS



2 : BL2

3 : LVDS Panel Connector

PIN	Signal Name	PIN	Signal Name
2	LCD_VCC	1	LCD_VCC
4	N/A	3	+3.3V
6	LVDS_A_DATA0#	5	N/A
8	GND	7	LVDS_A_DATA0
10	LVDS_A_DATA1	9	LVDS_A_DATA1#
12	LVDS_A_DATA2#	11	GND
14	GND	13	LVDS_A_DATA2
16	LVDS_A_DATA3	15	LVDS_A_DATA3#
18	LVDS_A_CLK#	17	GND
20	GND	19	LVDS_A_CLK
22	LVDS_B_DATA0	21	LVDS_B_DATA0#
24	LVDS_B_DATA1#	23	GND
26	GND	25	LVDS_B_DATA1
28	LVDS_B_DATA2	27	LVDS_B_DATA2#
30	LVDS_B_DATA3#	29	DPLVDD_EN
32	NC	31	LVDS_B_DATA3
34	LVDS_B_CLK	33	LVDS_B_CLK#
36	CON_LBKLT_EN	35	GND
38	LCD_BLT_VCC	37	CON_LBKLT_CTL
40	LCD_BLT_VCC	39	LCD_BLT_VCC

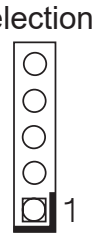


* eDP Connector
(on the Backside of PCB)

PIN	Signal Name
40	NA
39	LCD_BLT_VCC
38	LCD_BLT_VCC
37	LCD_BLT_VCC
36	LCD_BLT_VCC
35	NA
34	NA
33	CON_LBKLT_CTL
32	CON_LBKLT_EN
31	GND
30	GND
29	GND
28	GND
27	eDP_HPD_CON
26	GND
25	GND
24	GND
23	GND
22	NA
21	LCD_VCC
20	LCD_VCC
19	LCD_VCC
18	LCD_VCC
17	GND
16	eDP_AUX#_CON
15	eDP_AUX_CON
14	GND
13	eDP_TX0_CON
12	eDP_TX#0_CON
11	GND
10	eDP_TX1_CON
9	eDP_TX#1_CON
8	GND
7	eDP_TX2_CON
6	eDP_TX#2_CON
5	GND
4	eDP_TX3_CON
3	eDP_TX#3_CON
2	GND
1	NA

4 : BL1

5 : Panel Power Selection (LCD_VCC)
(PNL_PWR1)
1-2 : +3V
2-3 : +5V
3-4 : +5V
4-5 : +12V



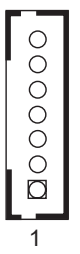
6 : Inverter Power Control Wafer
(BLT_PWR1)

PIN	Signal Name
6	LCD_BLT_VCC
5	LCD_BLT_VCC
4	CON_LBKLT_EN
3	CON_LBKLT_CTL
2	GND
1	GND

7 : Backlight Power Select
(LCD_BLT_VCC) (BKT_PWR1)
1-2 : +5V
2-3 : +12V
3-4 : +12V
4-5 : DC_IN Power

8 : Backlight & Amp Volume Control
(BLT_VOL1)

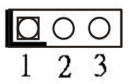
PIN	Signal Name
7	GND
6	GND
5	GPIO_BLT_DW
4	GPIO_BLT_UP
3	PWRDN
2	GPIO_VOL_DW
1	GPIO_VOL_UP



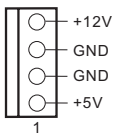
9 : EDP_LVDS_J1
1-2 : eDP
2-3 : LVDS

10 : ATX/AT Mode Select (PWR_JP1)
1-2 : AT Mode
2-3 : ATX Mode

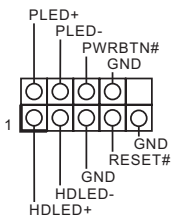
11 : Digital Input / Output Power Select
(JGPIO_PWR1)
1-2 : +12V
2-3 : +5V



12 : SATA Power Output Connector

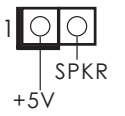


13 : System Panel Header

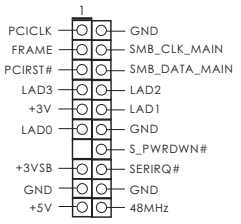


14 : SATA3 Connector
(SATA3_1)

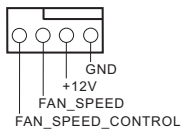
15 : 2-Pin Buzzer Header



16 : LPC Header

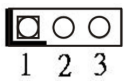


17 : 4-Pin FAN Connector
(+12V)

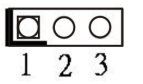


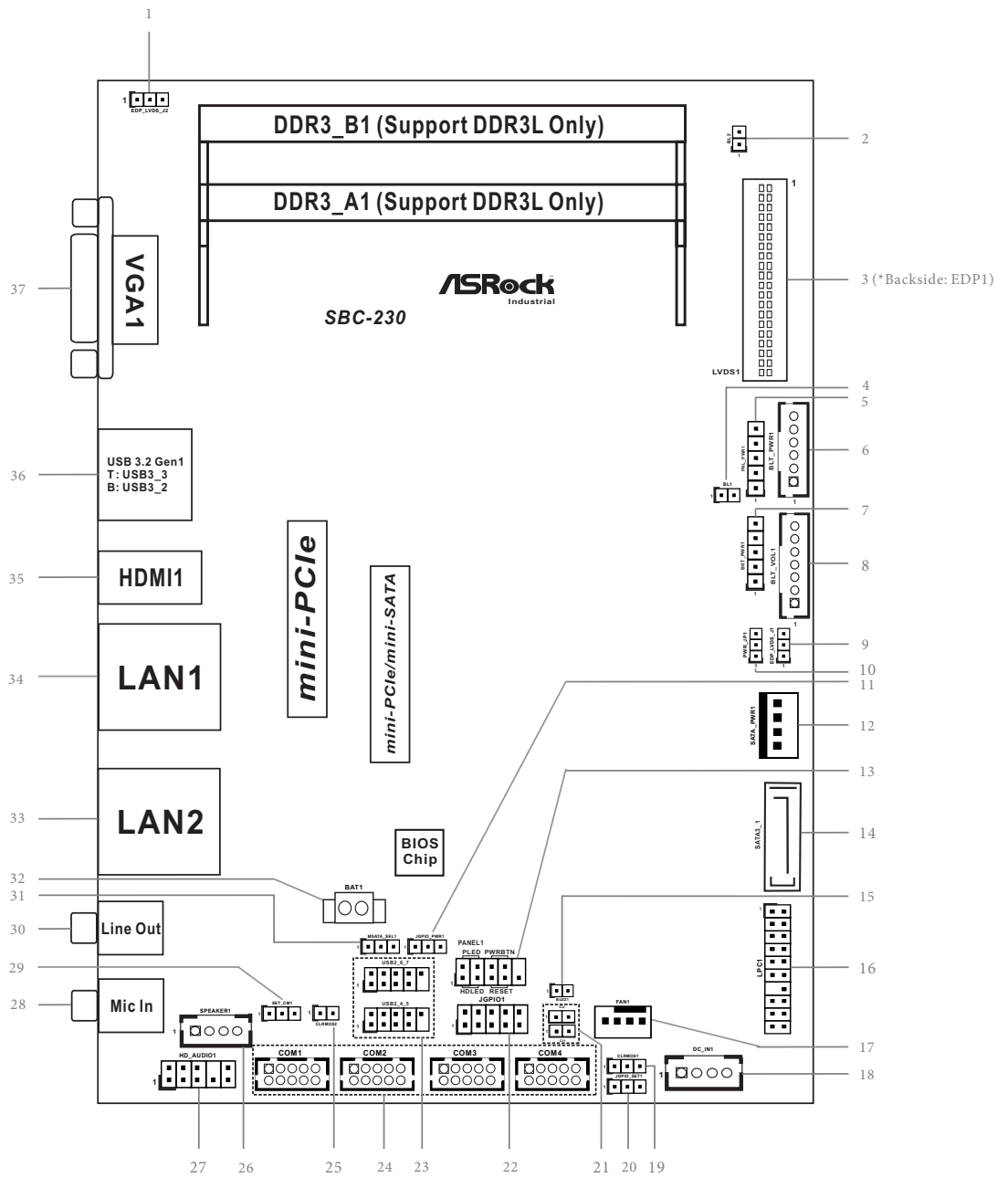
18 : ATX Power Connector (Input 9V~36V)
1-4 : GND
2-3 : DC Input

19 : Clear CMOS Header (CLRMOS1)
1-2 : Normal
2-3 : Clear CMOS

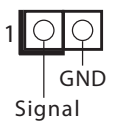


20 : GPIO Default Setting (JGPIO_SET1)
1-2 : Pull-High
2-3 : Pull-Low



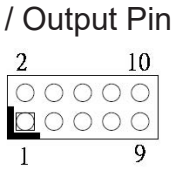


21 : Chassis Intrusion Headers



CI1 :
Close : Active Case Open
Open : Normal
CI2 :
Close : Normal
Open : Active Case Open

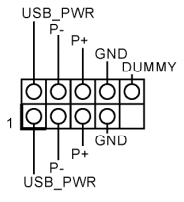
22 : Digital Input / Output Pin Header (JGPIO1)



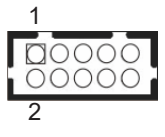
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
2	SIO_GP30	4	SIO_GP31	6	SIO_GP32	8	SIO_GP33	10	GND
1	SIO_GP34	3	SIO_GP35	5	SIO_GP36	7	SIO_GP37	9	JGPIO_PWR

Parameter	Range
GPIO input Low voltage	Max: 0.8V
GPIO input High voltage	Low: 2V
GPIO output Low voltage	Max: 0.4V
GPIO output High voltage	Low: 2.4V
Note: Max. load per GPI/O pin: 12mA Current Max. 1A per power pin.	

23 : USB2.0 Connectors (USB2_4_5, USB2_6_7)



24 : COM1, 2, 3, 4 Headers



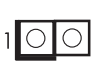
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	DDCD#	3	TTXD	5	GND	7	RRTS#	9	COM_PWR
2	RRXD	4	DDTR#	6	DDSR#	8	CCTS#	10	DUMMY

* This motherboard supports RS232/422/485 on COM1 port. Please refer to below table for the pin definition. In addition, COM1 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM1 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	NA/+5V/+12V	N/A	N/A

25 : Clear CMOS Header (CLRMOS2)

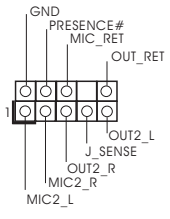


Open: Normal
Short: Auto Clear CMOS (Power Off)

26 : 3W Audio AMP Output Wafer

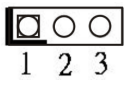
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	SPK L-	2	SPK L+	3	SPK R+	4	SPK R-

27 : Front Panel Audio Header



28 : Audio Output: Pink – Mic In

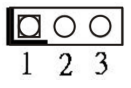
29 : COM1 Pin9 PWR Setting Jumper (SET_CM1)



1-2 : +5V
2-3 : +12V

30 : Audio Output: Green – Line Out

31 : mSATA Select (MSATA_SEL1)



1-2 : Auto Detect or Auto Select
2-3 : Fixed Mini-SATA

32 : Battery Connector
33 : LAN Port (LAN2)
34 : LAN Port (LAN1)
35 : HDMI Port (HDMI1)
36 : USB3.2 Gen1 Ports (USB3_2_3)
37 : VGA Port (VGA1)

* The thermal solution of whole system needs to be designed additionally.